

DESCRIPTION

CPU Interface

- Support for most popular CPU interfaces
- Direct and indirect addressing
- 8/16-bit interface support
- SPI

Display Support

- Single panel implementation can be:
 - RGB Interface TFT panel
 - Color and monochrome STN
- 16/18/24-bit color TFT
- Programmable resolutions up to 800x480 @ 8 bpp
- Programmable color depths up to 24 bpp

Display Features

- Multiple window support for Main and PIP windows
- Alpha blending and transparency
- PIP flashing
- LUT 256x24bitx3pcs for both Main and PIP layer
- SwivelView™ 90°, 180°, 270° display image rotation

Display Memory

- 384 KB of embedded SRAM
- Maximum resolution for WQVGA:
 - 1 layer: 480x272 @ 24 bpp
or 800x480 @ 8 bpp
 - 2 layer: Main 480x272 @ 16 bpp
and PIP 480x272 @ 8 bpp

Miscellaneous

- 2D BitBLT Engine
- Internal system clock: 66MHz
- Software initiated power save mode
- Multiple general purpose input/output pins
- Flexible clock structure:
- Embedded PLL
- Digital clock inputs
- Operating temperature range: -40°~ 85°C
- PLL/COREVDD 1.5 volts
- IO/HIOVDD 3.3 or 1.8 volts
- Package: QFP 100-pin, 0.5mm pin pitch

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