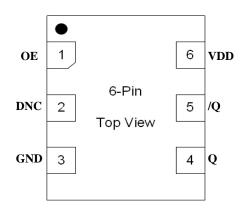
Ordering Information

| Ordering Part Number | Marking Line 1 | Marking Line 3 | Shipping | Package |
|----------------------|----------------|----------------|---------------|---------------------|
| MX575ABF25M0000 | MX575AB | F25M0000 | Tube | 6-Pin 7mm x 5mm LGA |
| MX575ABF25M0000 TR | MX575AB | F25M0000 | Tape and Reel | 6-Pin 7mm x 5mm LGA |

Devices are Green and RoHS compliant. Sample material may have only a partial top mark.

Pin Configuration

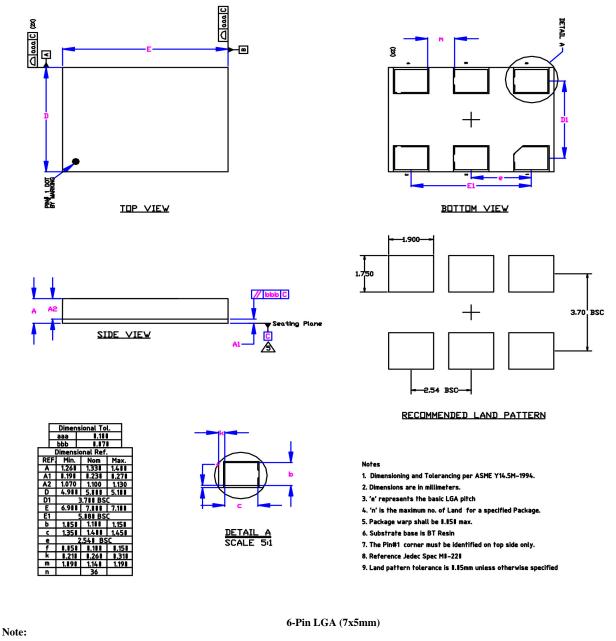


Pin Description

| Pin Number | Pin Name | Pin Type | Pin Level | Pin Function |
|------------|----------|----------|-----------|---|
| 1 | OE | I, SE | LVCMOS | Output Enable, disables output to tri-state, 1 = Disabled, 0 = Enabled, 50k Ohms Pull-Down |
| 2 | DNC | | | Make no connection, leave floating. |
| 3 | GND | PWR | | Power Supply Ground |
| 4, 5 | Q, /Q | O, Diff | LVPECL | Clock Output Frequency = 25MHz |
| 6 | VDD | PWR | | Power Supply |

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Package Information and Recommended Land Pattern for 6-Pin LGA³



3. Package information is correct as of the publication date. For updates and most current information, go to www.micrel.com.

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May 09, 2014 MX575AB1-2048