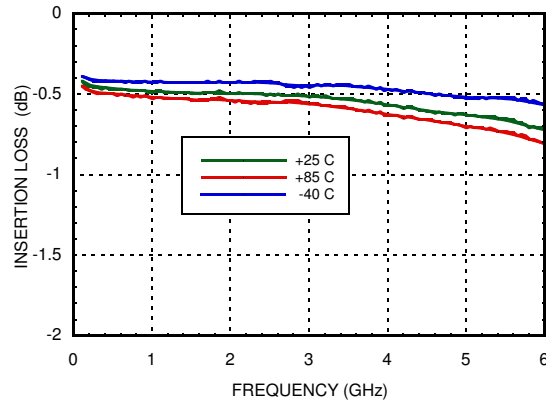
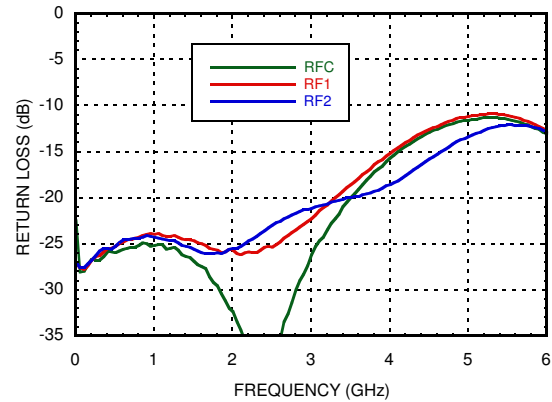
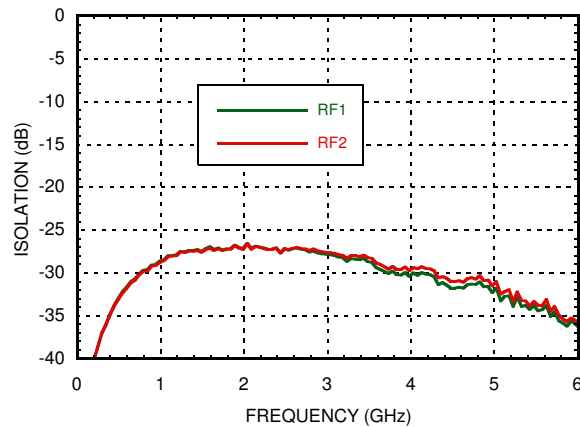
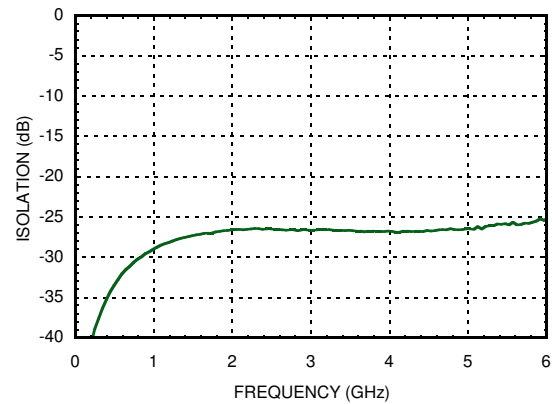
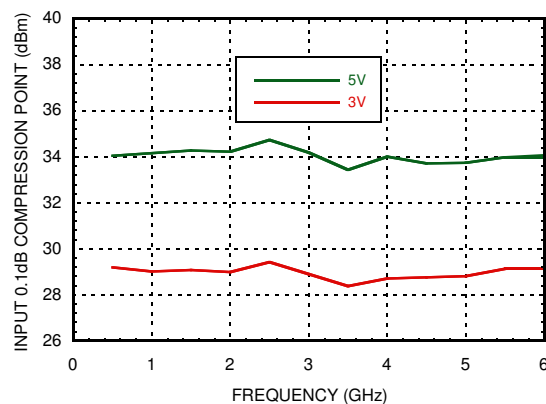
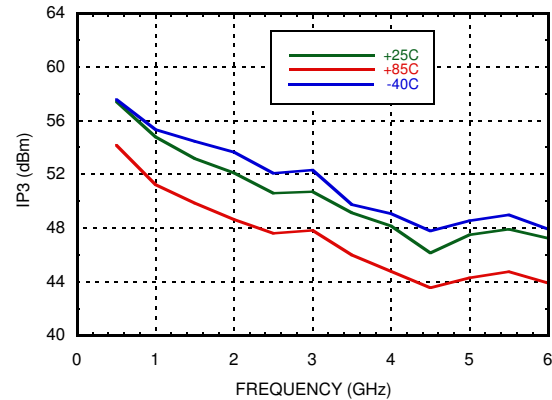


GaAs MMIC POSITIVE CONTROL T/R SWITCH, DC - 6 GHz

Insertion Loss

Return Loss

**Isolation Between
Ports RFC and RF1 / RF2**

Isolation Between Ports RF1 and RF2

Input 0.1 dB Compression Point

**Input Third Order
Intercept Point, Vctl = 3v**




GaAs MMIC POSITIVE CONTROL T/R SWITCH, DC - 6 GHz

Absolute Maximum Ratings

Control Voltage Range	-0.5 to +7.5 Vdc
Hot Switch Power Level (Vctl = +3V)	+29 dBm
Channel Temperature	150 °C
Continuous Pdiss (T = 85 °C) (derate 13 mW/°C above 85 °C)	0.867 W
Thermal Resistance	75 °C/W
Storage Temperature	-65 to +150 °C
Operating Temperature	-40 to +85 °C
ESD Sensitivity (HBM)	Class 1A

Control Voltages

*Control Input Tolerances are ± 0.2 Vdc

State	Bias Condition*
Low	0 Vdc @ 25 μ A Typical
High	+3 Vdc to +5 Vdc @ 25 μ A Typical

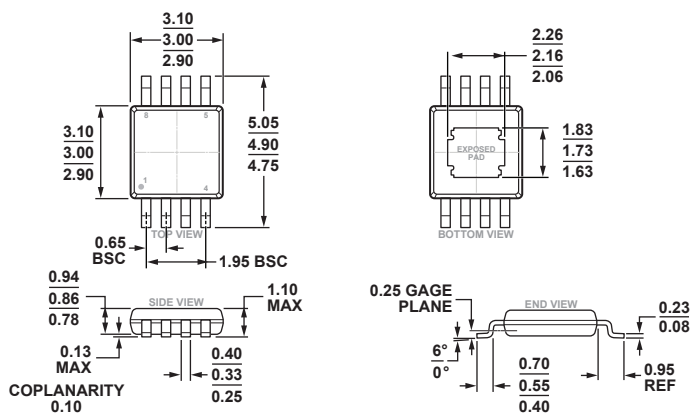
Truth Table

Control Input		Signal Path State
A	B	RFC to:
Low	High	RF1
High	Low	RF2

DC blocks are required at ports RFC, RF1, RF2.

Choose value for lowest frequency of operation.

Outline Drawing



COMPLIANT TO JEDEC STANDARDS MO-187-AA-T

8-Lead Mini Small Outline Package with Exposed Pad [MINI_SO_EP]
(RH-8-1)

Dimensions shown in millimeters

Package Information

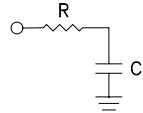
Part Number	Package Body Material	Lead Finish	MSL Rating ^[1]	Package Marking ^[2]
HMC536MS8GE	RoHS-compliant Low Stress Injection Molded Plastic	100% matte Sn	MSL3	H536 XXXX
HMC536MS8GETR	RoHS-compliant Low Stress Injection Molded Plastic	100% matte Sn	MSL3	H536 XXXX

[1] Max peak reflow temperature of 260 °C

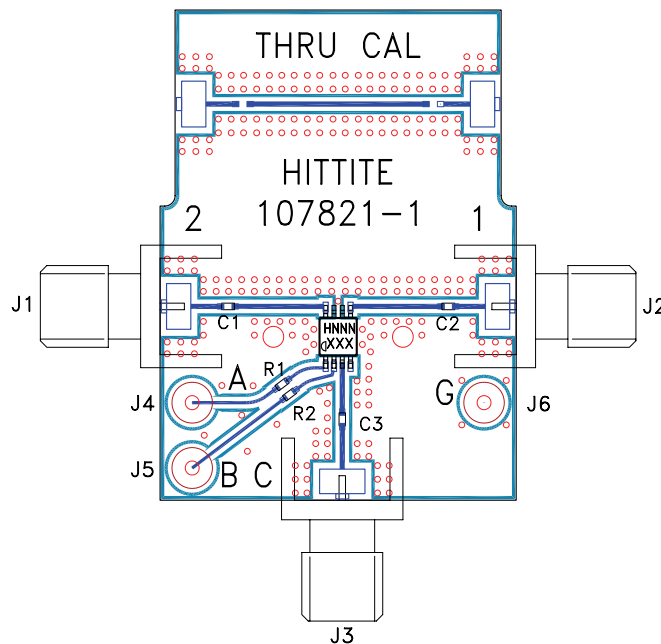
[2] 4-Digit lot number XXXX

GaAs MMIC POSITIVE CONTROL T/R SWITCH, DC - 6 GHz

Pin Descriptions

Pin Number	Function	Description	Interface Schematic
1	A	See truth and control voltage tables.	
2	B	See truth and control voltage tables.	
3, 5, 8	RFC, RF1, RF2	These pins are DC coupled and matched to 50 Ohms. Blocking capacitors are required.	
4	N/C	No connection required. This pin may be connected to RF/DC ground without affecting performance	
6, 7	GND	Package bottom has exposed metal paddle that must also be connected to RF/DC ground.	

Evaluation PCB



List of Materials for Evaluation PCB 105143 ^[1]

Item	Description
J1 - J3	PCB Mount SMA RF Connector
J4 - J6	DC Pin
C1 - C3	100 pF Capacitor, 0402 Pkg.
R1 - R2	100 Ohm Resistor, 0402 Pkg.
U1	HMC536MS8G / HMC536MS8GE SPDT Switch
PCB ^[2]	107821 Evaluation PCB

[1] Reference this number when ordering complete evaluation PCB

[2] Circuit Board Material: Rogers 4350

The circuit board used in the application should be generated with proper RF circuit design techniques. Signal lines at the RF port should have 50 Ohm impedance and the package ground leads and exposed paddle should be connected directly to the ground plane similar to that shown above. The evaluation circuit board shown above is available from Analog Devices upon request.