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REVISION HISTORY

5/15—Revision 0: Initial Version

SPECIFICATIONS

$T_A = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$, $V_{CC} = 5\text{ V}$, unless otherwise noted.

Table 1.

Parameter	Min	Typ	Max	Unit	Test Conditions/Comments
FREQUENCY					
Range					
f_{OUT}	10.38		11.30	GHz	
$f_{OUT}/2$	5.19		5.65	GHz	
Drift Rate		1.0		MHz/ $^{\circ}\text{C}$	
Pulling		0.3		MHz p-p	Pulling into a 2.0:1 voltage standing wave ratio (VSWR)
Pushing		8.0		MHz/V	At $V_{TUNE} = 5\text{ V}$
POWER OUTPUT					
RFOUT	4	8	12	dBm	
RFOUT/2	4	8	12	dBm	
Supply Current (I_{CC})		175		mA	$V_{CC} = 4.75\text{ V}$
	160	200	290	mA	$V_{CC} = 5.00\text{ V}$
		218		mA	$V_{CC} = 5.25\text{ V}$
HARMONICS, SUBHARMONICS					RFOUT
Half		39		dBc	
Second		20		dBc	
Third		30		dBc	
TUNING					
Voltage (V_{TUNE})	2		13	V	
Sensitivity	80		300	MHz/V	
Tune Port Leakage Current			10	μA	$V_{TUNE} = 13\text{ V}$
OUTPUT RETURN LOSS (RFOUT)		3.5		dB	
SSB PHASE NOISE					
10 kHz Offset		-86	-82	dBc/Hz	
100 kHz Offset		-114	-110	dBc/Hz	

ABSOLUTE MAXIMUM RATINGS

Table 2.

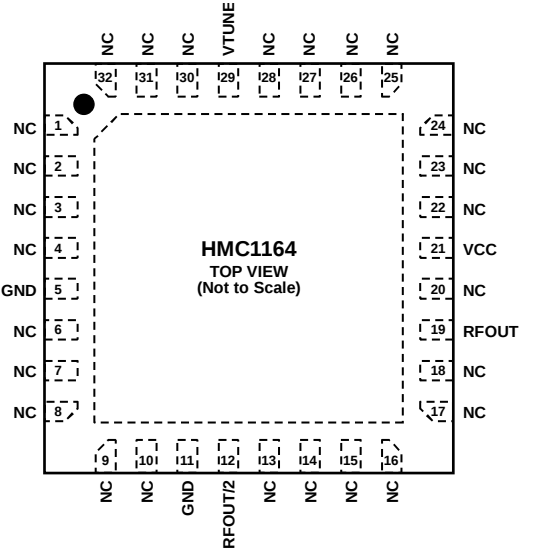
Parameter	Rating
V _{CC}	5.5 V dc
VTUNE	0 V to 15 V
Temperature	
Operating	–40°C to +85°C
Storage	–65°C to +150°C
Nominal Junction (to Maintain 1 Million Hours MTTF)	135°C
Nominal Junction (T _A = 85°C)	111°C
Maximum Reflow Temperature (MSL3 Rating)	260°C
Thermal Resistance (Junction to Ground Paddle)	26°C/W
ESD Sensitivity (Human Body Model)	Class 1A

Stresses at or above those listed under Absolute Maximum Ratings may cause permanent damage to the product. This is a stress rating only; functional operation of the product at these or any other conditions above those indicated in the operational section of this specification is not implied. Operation beyond the maximum operating conditions for extended periods may affect product reliability.

ESD CAUTION**ESD (electrostatic discharge) sensitive device.**

Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.

PIN CONFIGURATION AND FUNCTION DESCRIPTIONS



- NOTES
- 1. NC = NO CONNECT. HOWEVER, THESE PINS MAY BE CONNECTED TO RF/DC GROUND WITHOUT AFFECTING THE PERFORMANCE OF THE DEVICE.
 - 2. THE EXPOSED PAD MUST BE CONNECTED TO RF/DC GROUND.

Figure 2. Pin Configuration

Table 3. Pin Function Descriptions

Pin No.	Mnemonic	Description
1 to 4, 6 to 10, 13 to 18, 20, 22 to 28, 30 to 32	NC	No Connect. However, these pins can be connected to RF/dc ground without affecting the performance of the device.
5, 11	GND	Ground. Connect these pins to RF/dc ground.
12	RFOUT/2	Half Frequency Output. This pin is ac-coupled.
19	RFOUT	RF Output. This pin is ac-coupled.
21	VCC	Supply Voltage (5 V).
29	VTUNE	Control Voltage and Modulation Input. The modulation bandwidth is dependent on the drive source impedance.
	EP	Exposed Pad. The exposed metal pad must be connected to RF/dc ground.

INTERFACE SCHEMATICS

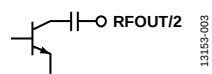


Figure 3. RFOUT/2 Interface

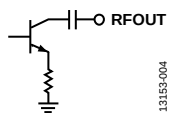


Figure 4. RFOUT Interface

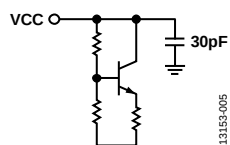


Figure 5. VCC Interface

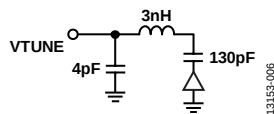


Figure 6. VTUNE Interface

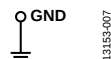
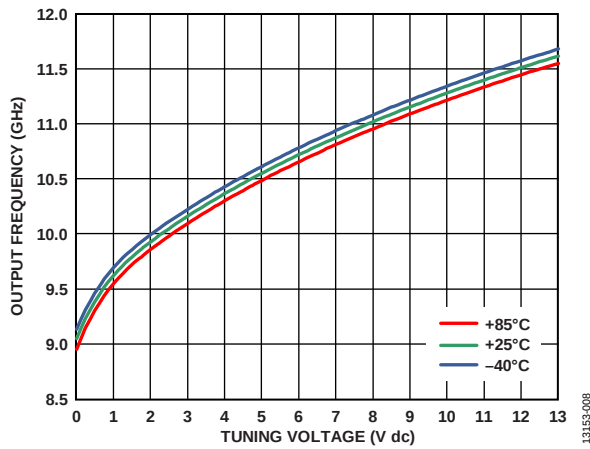
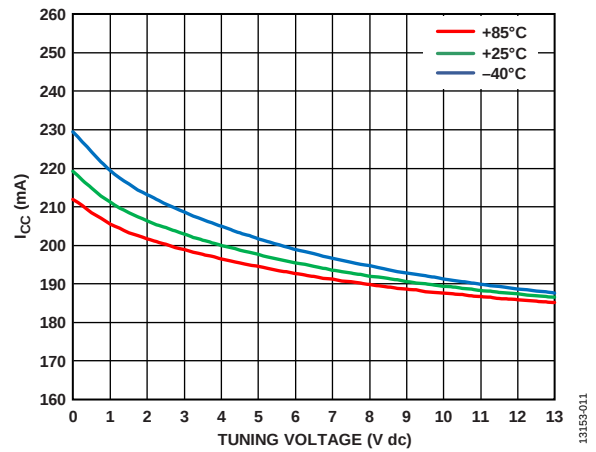
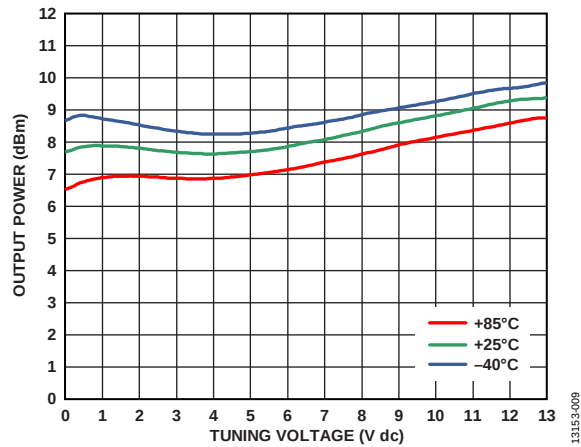
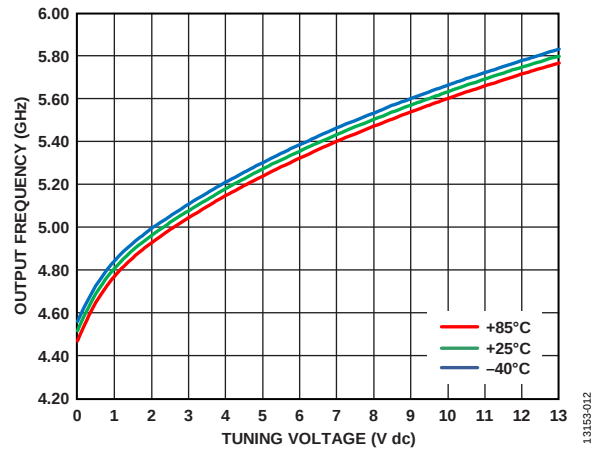
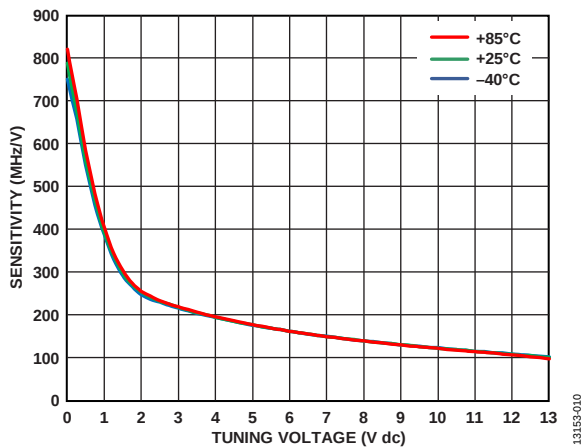
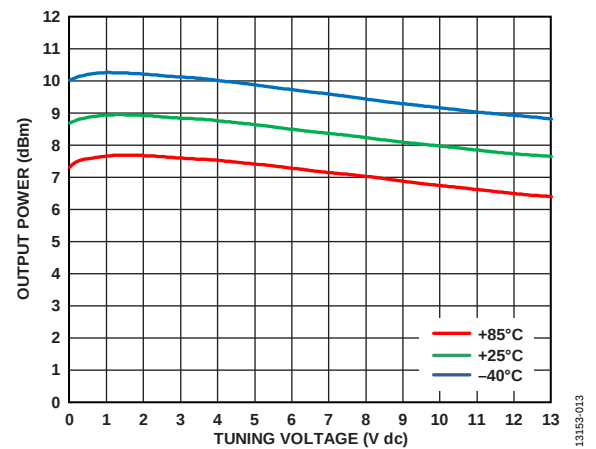


Figure 7. GND Interface

TYPICAL PERFORMANCE CHARACTERISTICS

Figure 8. Output Frequency vs. Tuning Voltage (V_{TUNE})Figure 11. Supply Current (I_{CC}) vs. Tuning Voltage (V_{TUNE})Figure 9. Output Power vs. Tuning Voltage (V_{TUNE})Figure 12. RFOUT/2 Output Frequency vs. Tuning Voltage (V_{TUNE})Figure 10. Sensitivity vs. Tuning Voltage (V_{TUNE})Figure 13. RFOUT/2 Output Power vs. Tuning Voltage (V_{TUNE})

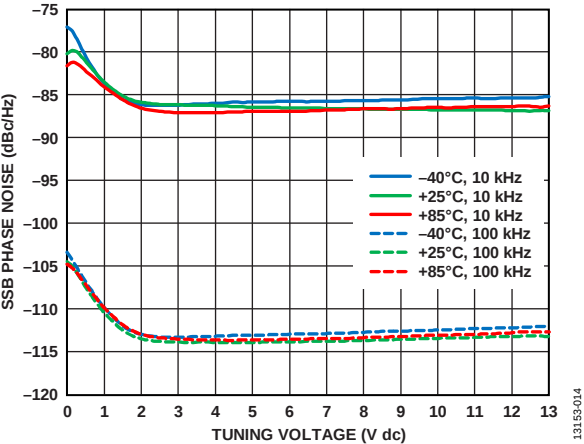


Figure 14. SSB Phase Noise vs. Tuning Voltage (V_{TUNE})

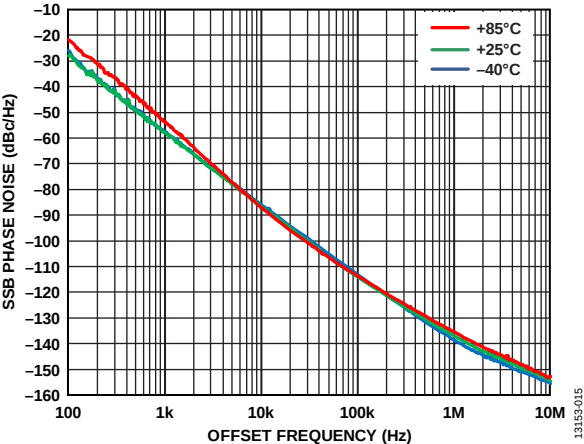


Figure 15. SSB Phase Noise vs. Offset Frequency at $V_{TUNE} = 5$ V

EVALUATION PRINTED CIRCUIT BOARD (PCB)

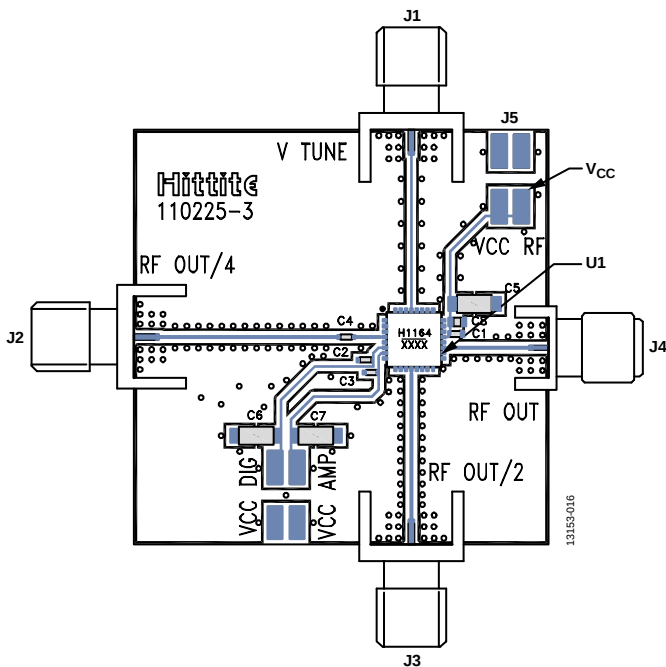


Figure 16. 110225-3 Evaluation PCB

The circuit board used in this application uses RF circuit design techniques. Ensure that signal lines have 50 Ω impedance and connect the package ground leads and backside ground paddle directly to the ground plane.

Use a sufficient number of via holes to connect the top and bottom ground planes. The evaluation circuit board shown in Figure 16 is available from Analog Devices, Inc., upon request.

BILL OF MATERIALS

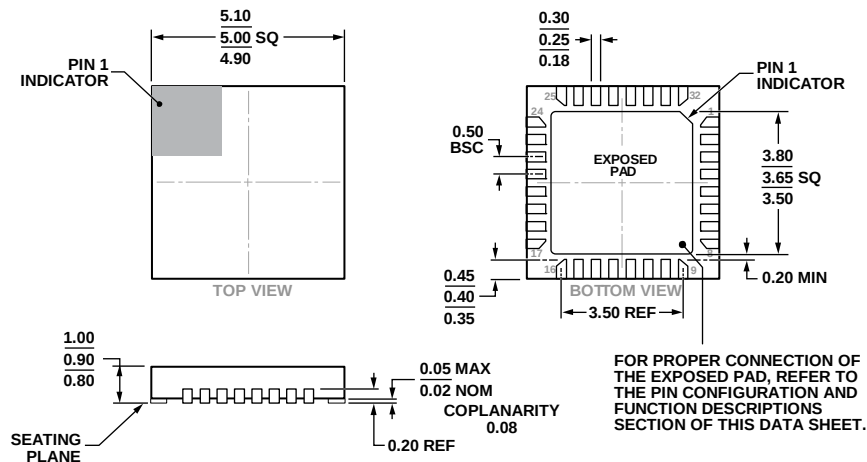
Table 4. Bill of Materials for the EV1HMC1164LP5

Item	Description
J1 to J4	PCB mount SMA RF connectors
J5, J6	2 mm dc header
C1 to C3	100 pF capacitors, 0402 package
C4	1000 pF capacitor, 0402 package
C5 to C7	2.2 μF tantalum capacitors
C8	0.01 μF capacitor, 0603 package
U1	HMC1164LP5E VCO
PCB ¹	110225-3 evaluation board ²

¹ Circuit board material is Rogers 4350.
² Reference this number when ordering the complete evaluation PCB.

PACKAGING AND ORDERING INFORMATION

OUTLINE DIMENSIONS



COMPLIANT TO JEDEC STANDARDS MO-220-VHHD-4.

Figure 17. 32-Lead Lead Frame Chip Scale Package [LFCSP_VQ]
5 mm x 5 mm Body, Very Thin Quad
(HCP-32-1)
Dimensions shown in millimeters

ORDERING GUIDE

Model ¹	Temperature Range	MSL Rating ²	Package Description	Package Option	Qty.	Branding ³
HMC1164LP5E	-40°C to +85°C	MSL3	32-Lead LFCSP_VQ	HCP-32-1		H1164 XXXX
HMC1164LP5ETR	-40°C to +85°C	MSL3	32-Lead LFCSP_VQ, 7" Tape and Reel	HCP-32-1	500	H1164 XXXX
EV1HMC1164LP5			Evaluation Board			

¹ The HMC1164LP5E and HMC1164LP5ETR are RoHS-compliant parts.

² See the Absolute Maximum Ratings section, Table 2.

³ XXXX is a placeholder for the 4-digit lot number.