Truth Table

Α	В	Y
L	L	Н
L	Н	Н
Н	L	Н
Н	Н	L

Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating
Supply voltage range	V _{CC}	-0.5 to 7
DC input voltage	V _{IN}	-0.5 to V _{CC} ≠ 0.5 V
DC output voltage	V _{OUT}	-0.5 to V _{CC} + 0.5 V
Input diode current	I _{IK}	#20 mA
Output diode current	lok	±20 mA
DC output current	lout	±25 mA
DC V _{CC} /ground current	Icc	±50 mA
Power dissipation	PD	180 mW
Storage temperature	T _{stg}	-65 to 150 °C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions")" Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to 65°C. From Ta = 65 to 85°C a derating factor of -10 mW/°C shall be applied until 300 mW.

Operating Ranges (Note)

Characteristics Symbol		Rating	Unit
Supply voltage	Vcc	2 to 6	V
Input voltage	VIN	0 to V _{CC}	V
Output voltage	Vout	0 to V _{CC}	V
Operating temperature	T _{opr}	-40 to 85	°C
\rightarrow	\rightarrow	0 to 1000 ($V_{CC} = 2.0 \text{ V}$)	
Input rise and fall time	t _r , t _f	0 to 500 ($V_{CC} = 4.5 \text{ V}$)	ns
		0 to 400 ($V_{CC} = 6.0 \text{ V}$)	

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

Electrical Characteristics

DC Characteristics

Characteristics Symbol		Test Condition $V_{CC}\left(V\right)$		Ta = 25°C			Ta = -40 to 85°C		Unit	
				V _{CC} (V)	Min	Тур.	Max	Min	Max	
		_		2.0	1.50	_ <	/-	1.50	_	
High-level input voltage	V_{IH}			4.5	3.15	_		3.15	_	V
ŭ				6.0	4.20		(\	4.20	_	
				2.0	_	10	0.50	_	0.50	
Low-level input voltage	V_{IL}	_		4.5		/ / /	1).35	_	1.35	V
				6.0	-(1.80	_	1.80	
				2.0	1.9	2.0	_	1.9	_	
High lavel autout		V _{IN} = V _{IH} or V _{IL}	$I_{OH} = -20 \mu A$	4.5	4.4	4.5	_	4.4	_	
High-level output voltage	V _{OH}			6.0	5.9	6.0		5.9	\rightarrow	V
			$I_{OH} = -4 \text{ mA}$	4.5	4.18	4.31	-6	4.13	> —	
			$I_{OH} = -5.2 \text{ mA}$	6.0	5.68	5.80	7	5.63) —	
				2.0	_	0.0	0.1		0.1	
Low lovel cutout		V _{IN} = V _{IH} or V _{IL}	I _{OL} = 20 μA	4.5	_	0.0	0.1	> _	0.1	
Low-level output voltage	V_{OL}			6.0	_	0.0	(0.1)	_	0.1	V
			I _{OL} = 4 mA	4.5	- ((0.17)	0.26	_	0.33	
			I _{OL} = 5.2 mA	6.0		0.18	0.26	_	0.33	
Input leakage current	I _{IN}	V _{IN} = V _{CC} or	GND	6.0		_	±0.1	_	±1.0	μΑ
Quiescent supply current	Icc	V _{IN} = V _{CC} or	GND	6.0		//_	1.0	_	10.0	μА

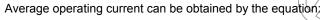
AC Characteristics ($C_L = 15 \text{ pF}$, $V_{CC} = 5 \text{ V}$, $Ta = 25^{\circ}\text{C}$, input: $t_r = t_f = 6 \text{ ns}$)

Characteristics	Symbol	Fest Condition	Min	Тур.	Max	Unit
Output transition time	t _{TLH}	<u> </u>	_	4	8	ns
Propagation delay time	t _{pL} H t _{pHL}			6	12	ns

AC Characteristics ($C_L = 50 \text{ pF}$, input: $t_r = t_f = 6 \text{ ns}$)

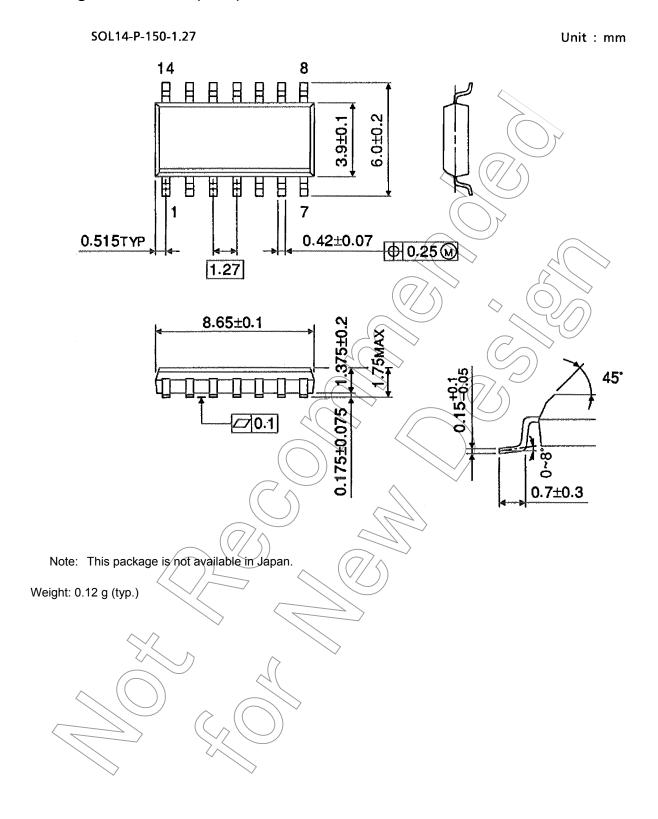
Characteristics	Test Condition			Ta = 25°C			Ta –40 to	Unit	
	•		V _{CC} (V)	Min	Тур.	Max	Min	Max	
	4 —		2.0	_	25	75	_	95	
Output transition time t _{TLH}		_	4.5	_	7 <	15	_	19	ns
	ЧHL		6.0	_	6	13	_	16	
	4		2.0	_	27	(75	4	95	
time	t _{pLH}	_{tpHL}	4.5	_	9	15	<i>7</i> –	19	ns
	^Ҭ рНL		6.0	~	8	1 3	_	16	
Input capacitance	C _{IN}	_		->	5		_	10	pF
itanaa	C _{PD}			((20)	>			nE.
	(Note)	_			20)				pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.



 I_{CC} (opr) = $C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/4$ (per gate)

Package Dimensions (Note)



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