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1 Electrical ratings

Table 2. Absolute maximum ratings

		Value	•	
Symbol	Parameter	D²PAK, TO-220,TO-247	TO-220FP	Unit
V _{DS}	Drain-source voltage	600		V
V _{GS}	Gate- source voltage	± 25		
I _D	Drain current (continuous) at T _C = 25 °C	13	13 ⁽¹⁾	Α
I _D	Drain current (continuous) at T _C = 100 °C	8.2	8.2 ⁽¹⁾	Α
I _{DM} ⁽²⁾	Drain current (pulsed)	52 52 ⁽¹⁾		Α
P _{TOT}	Total dissipation at T _C = 25 °C	110 30		W
I _{AR}	Avalanche current, repetitive or not-repetitive (pulse width limited by T_J max)	4.5		Α
E _{AS}	Single pulse avalanche energy (starting $T_J = 25$ °C, $I_D = I_{AR}$, $V_{DD} = 50$ V)	350		mJ
dv/dt ⁽³⁾	Peak diode recovery voltage slope	15		V/ns
V _{ISO}	Insulation withstand voltage (RMS) from all three leads to external heat sink (t=1 s;Tc=25 °C)	2500		V
T _J T _{stg}	Operating junction temperature Storage temperature	-55 to 1	50	°C

- 1. Limited by maximum junction temperature.
- 2. Pulse width limited by safe operating area.
- 3. $I_{SD} \leq 13 \text{ A, di/dt} \leq 400 \text{ A/µs, V}_{DD} \leq 80 \% \text{ V}_{(BR)DSS}, \text{V}_{DS(peak)} \leq \text{V}_{(BR)DSS}$

Table 3. Thermal data

Symbol	Parameter	D ² PAK TO-220 TO-247		TO-220FP	Unit	
R _{thj-case}	Thermal resistance junction-case max	1.14		4.17		
R _{thj-amb}	Thermal resistance junction-amb max	62.5 50		62.5	°C/W	
R _{thj-pcb} ⁽¹⁾	Thermal resistance junction-pcb max	30				

1. When mounted on 1inch² FR-4 board, 2 oz Cu.

2 Electrical characteristics

(T_{CASE}=25 °C unless otherwise specified)

Table 4. On/off states

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V _{(BR)DSS}	Drain-source breakdown voltage	I _D = 1 mA, V _{GS} = 0	600			V
I _{DSS}	Zero gate voltage drain current (V _{GS} = 0)	V _{DS} = 600 V V _{DS} = 600 V, T _J =125 °C			1 10	μ Α μ Α
I _{GSS}	Gate body leakage current (V _{DS} = 0)	V _{GS} = ±25 V			±100	nA
V _{GS(th)}	Gate threshold voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2	3	4	V
R _{DS(on)}	Static drain-source on- resistance	V _{GS} = 10 V, I _D =6.5 A		0.260	0.285	Ω

Table 5. Dynamic

Symbol	Parameter	eter Test conditions		Тур.	Max.	Unit
C _{iss} C _{oss} C _{rss}	Input capacitance Output capacitance Reverse transfer capacitance	$V_{DS} = 50 \text{ V, f} = 1 \text{ MHz,}$ $V_{GS} = 0$	-	1000 60 3	-	pF pF pF
C _{oss eq.} ⁽¹⁾	Output equivalent capacitance	V _{DS} = 0, to 480 V, V _{GS} =0	-	225	-	pF
R_g	Intrinsic resistance	f=1 MHz open drain	-	3.5	-	Ω
Q _g Q _{gs} Q _{gd}	Total gate charge Gate-source charge Gate-drain charge	V_{DD} = 480 V, I_{D} = 13 A V_{GS} = 10 V (see Figure 19)	1	35 6 20	1	nC nC nC

^{1.} $C_{oss\ eq.}$ is defined as a constant equivalent capacitance giving the same charging time as C_{oss} when V_{DS} increases from 0 to 80% V_{DS} .

Table 6. Switching times

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
t _{d(on)} t _r t _{d(off)} t _f	Turn-on delay time Rise time Turn-off delay time Fall time	$V_{DD} = 300 \text{ V}, I_{D} = 6.5 \text{ A},$ $R_{G} = 4.7 \Omega, V_{GS} = 10 \text{ V}$ (see Figure 18)	-	12 15 55 25	-	ns ns ns ns

Table 7. Source drain diode

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I _{SD}	Source-drain current Source-drain current (pulsed)		-		13 52	A A
V _{SD} ⁽²⁾	Forward on voltage	I _{SD} = 13 A, V _{GS} =0	-		1.6	٧
t _{rr} Q _{rr} I _{RRM}	Reverse recovery time Reverse recovery charge Reverse recovery current	I _{SD} =13 A, di/dt =100 A/μs, V _{DD} = 60 V (see Figure 20)	-	300 4.0 25		ns µC A
t _{rr} Q _{rr} I _{RRM}	Reverse recovery time Reverse recovery charge Reverse recovery current	$V_{DD} = 60 \text{ V}$ di/dt =100 A/ μ s, $I_{SD} = 13 \text{ A}$ Tj = 150°C (see Figure 20)	1	360 4.5 25		ns μC Α

^{1.} Pulse width limited by safe operating area

^{2.} Pulsed: pulse duration = $300 \mu s$, duty cycle 1.5%

2.1 Electrical characteristics (curves)

Figure 2. Safe operating area for TO-220 and Figure 3. Thermal impedance for TO-220 and D2PAK D2PAK

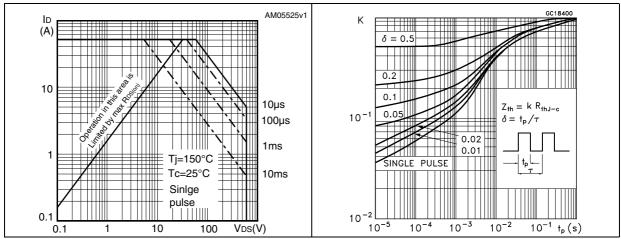


Figure 4. Safe operating area for TO-220FP Figure 5. Thermal impedance for TO-220FP

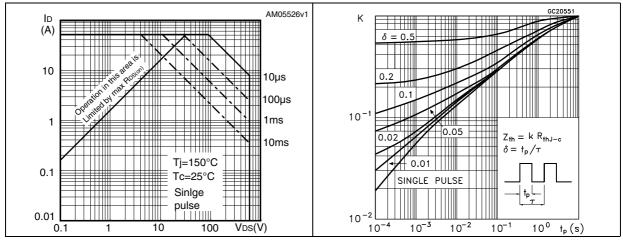


Figure 6. Safe operating area for TO-247 Figure 7. Thermal impedance for TO-247

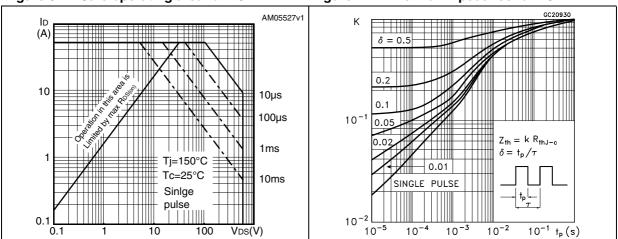


Figure 8. Output characteristics

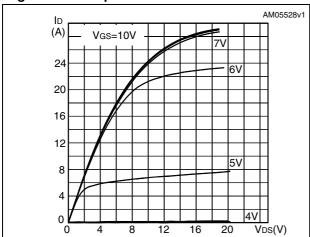


Figure 9. Transfer characteristics

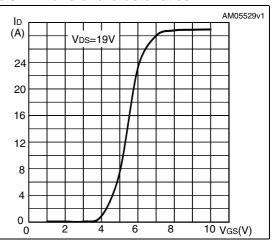
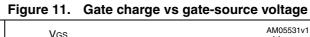
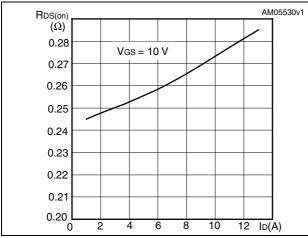


Figure 10. Static drain-source on resistance





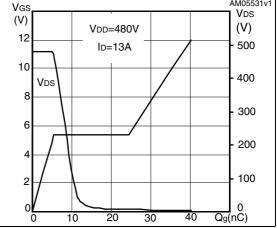
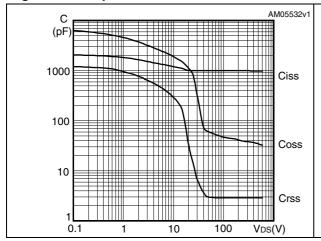
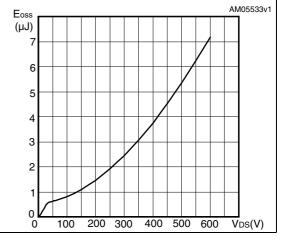


Figure 12. Capacitance variations

Figure 13. Output capacitance stored energy





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Figure 14. Normalized gate threshold voltage Figure 15. Normalized on resistance vs vs temperature temperature

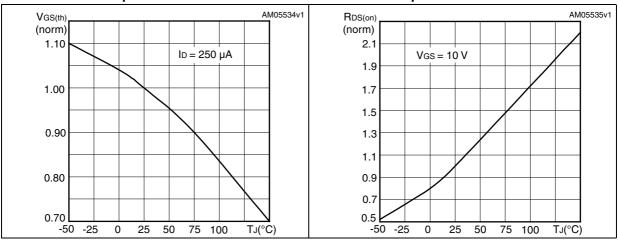
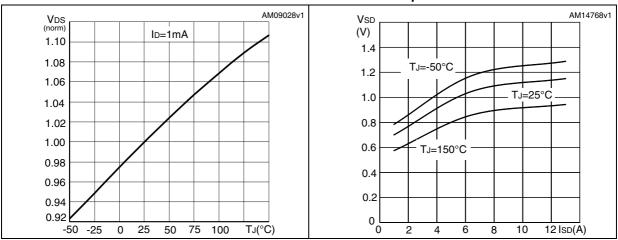


Figure 16. Normalized B_{VDSS} vs temperature

Figure 17. Source-drain diode forward vs temperature



3 Test circuits

Figure 18. Switching times test circuit for resistive load

Figure 19. Gate charge test circuit

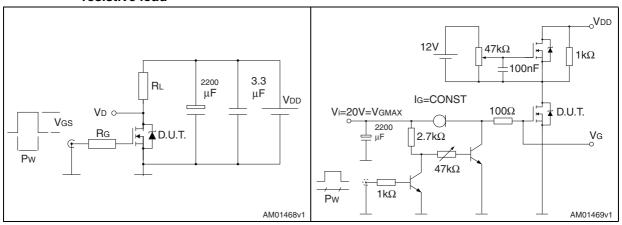


Figure 20. Test circuit for inductive load switching and diode recovery times

Figure 21. Unclamped inductive load test circuit

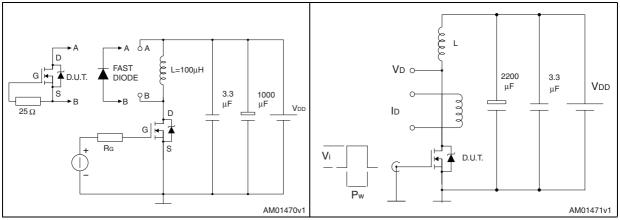
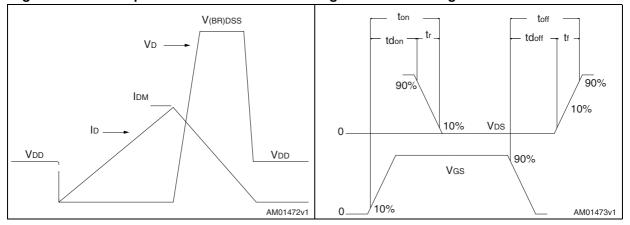


Figure 22. Unclamped inductive waveform

Figure 23. Switching time waveform



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4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: www.st.com. ECOPACK is an ST trademark.

Table 8. D²PAK (TO-263) mechanical data

Dim.		mm	
Dim.	Min.	Тур.	Max.
А	4.40		4.60
A1	0.03		0.23
b	0.70		0.93
b2	1.14		1.70
С	0.45		0.60
c2	1.23		1.36
D	8.95		9.35
D1	7.50		
Е	10		10.40
E1	8.50		
е		2.54	
e1	4.88		5.28
Н	15		15.85
J1	2.49		2.69
L	2.29		2.79
L1	1.27		1.40
L2	1.30		1.75
R		0.4	
V2	0°		8°

Figure 24. D²PAK (TO-263) drawing

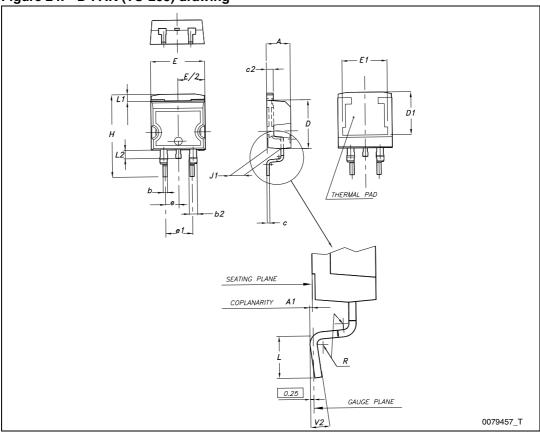
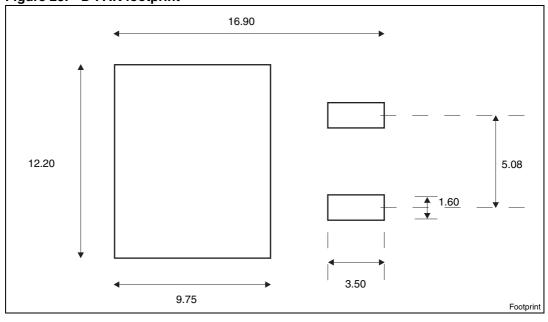


Figure 25. D²PAK footprint^(a)



a. All dimension are in millimeters

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Table 9. TO-220FP mechanical data

Dim	mm				
Dim.	Min.	Тур.	Max.		
А	4.4		4.6		
В	2.5		2.7		
D	2.5		2.75		
E	0.45		0.7		
F	0.75		1		
F1	1.15		1.70		
F2	1.15		1.70		
G	4.95		5.2		
G1	2.4		2.7		
Н	10		10.4		
L2		16			
L3	28.6		30.6		
L4	9.8		10.6		
L5	2.9		3.6		
L6	15.9		16.4		
L7	9		9.3		
Dia	3		3.2		

-*B*-Dia L6 *L2 L7* L3 F1 L4 F2 E -G1-7012510_Rev_K_B

Figure 26. TO-220FP drawing

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Table 10. TO-220 type A mechanical data

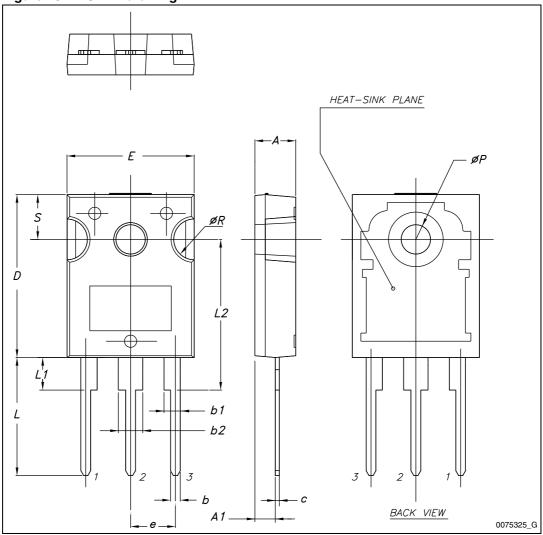
Dim	mm				
Dim.	Min.	Тур.	Max.		
Α	4.40		4.60		
b	0.61		0.88		
b1	1.14		1.70		
С	0.48		0.70		
D	15.25		15.75		
D1		1.27			
Е	10		10.40		
е	2.40		2.70		
e1	4.95		5.15		
F	1.23		1.32		
H1	6.20		6.60		
J1	2.40		2.72		
L	13		14		
L1	3.50		3.93		
L20		16.40			
L30		28.90			
ØP	3.75		3.85		
Q	2.65		2.95		

Figure 27. TO-220 type A drawing

Table 11. TO-247 mechanical data

Dim		mm.	
Dim.	Min.	Тур.	Max.
A	4.85		5.15
A1	2.20		2.60
b	1.0		1.40
b1	2.0		2.40
b2	3.0		3.40
С	0.40		0.80
D	19.85		20.15
E	15.45		15.75
е	5.30	5.45	5.60
L	14.20		14.80
L1	3.70		4.30
L2		18.50	
ØP	3.55		3.65
ØR	4.50		5.50
S	5.30	5.50	5.70

Figure 28. TO-247 drawing



5 Packaging mechanical data

Table 12. D²PAK (TO-263) tape and reel mechanical data

Таре				Reel		
		m	Dim	mm		
Dim.	Min.	Max.	Dim.	Min.	Max.	
A0	10.5	10.7	А		330	
В0	15.7	15.9	В	1.5		
D	1.5	1.6	С	12.8	13.2	
D1	1.59	1.61	D	20.2		
Е	1.65	1.85	G	24.4	26.4	
F	11.4	11.6	N	100		
K0	4.8	5.0	Т		30.4	
P0	3.9	4.1				
P1	11.9	12.1		Base qty	1000	
P2	1.9	2.1		Bulk qty	1000	
R	50					
Т	0.25	0.35				
W	23.7	24.3				

Figure 29. Tape

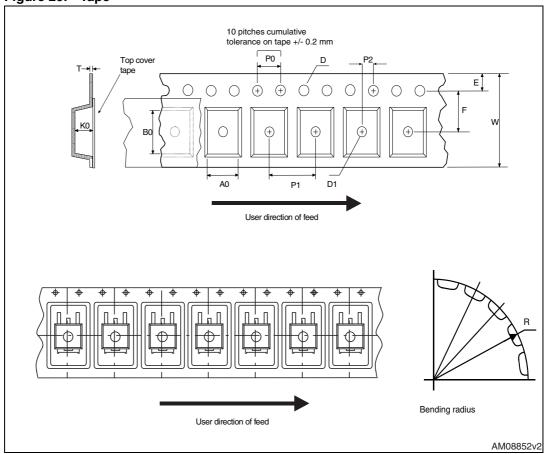
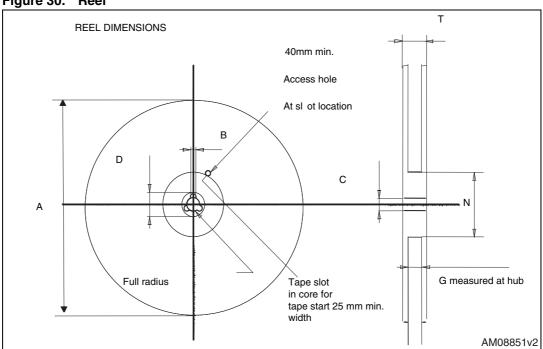


Figure 30. Reel



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6 Revision history

Table 13. Document revision history

Date	Revision	Changes
15-Jun-2009	1	First release
11-Nov-2009	2	 Added R_{DS(on)} typical value Added new package, mechanical data: I²PAK Document status promoted from preliminary data to datasheet
06-Oct-2010	3	Inserted new value in <i>Table 5</i> .
01-Oct-2012	4	Updated title and description on the cover page. Updated figures 10, 11, 14, 15 and 16. Updated Section 4: Package mechanical data and Section 5: Packaging mechanical data.

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