

64-Bit MIPS RISC Microprocessors with Integrated L2 Cache

PACKAGING

- Fully Static 0.13μ CMOS design with dynamic power down logic.
- RM7035C is available in a 128-pin ExposedPad™ 20 x 20 mm package, pin-compatible with the RM5231A 128-pin ExposedPad™ product.
- RM7065 package options:
 - 256-pin TBGA package, 27 x 27 mm, pin compatible with the RM7065A TBGA product.
 - 216-pin ExposedPad™ package, 24 x 24 mm, pin compatible with the RM5261A ExposedPad™ product.
- Lead-free (Pb-free) options available.

DEVELOPMENT TOOLS

- Operating Systems:
 - Linux by MontaVista and Red Hat
 - VxWorks by Wind River Systems
 - Nucleus by Accelerated Technology
 - Neutrino by QNX Software Systems

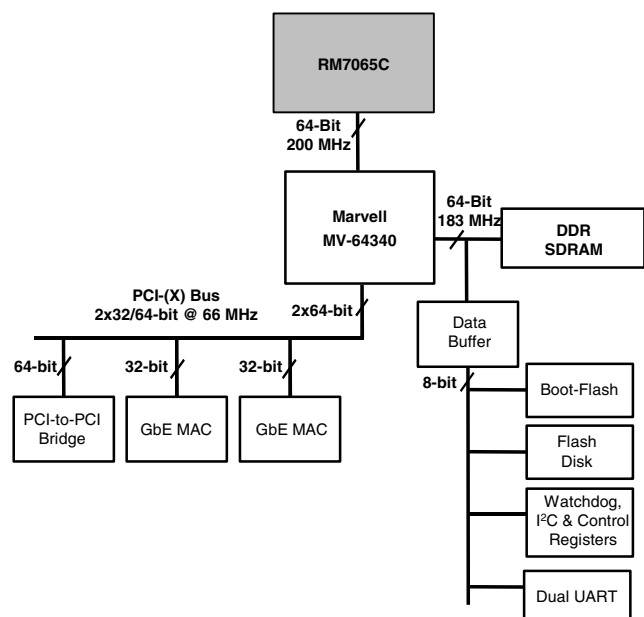
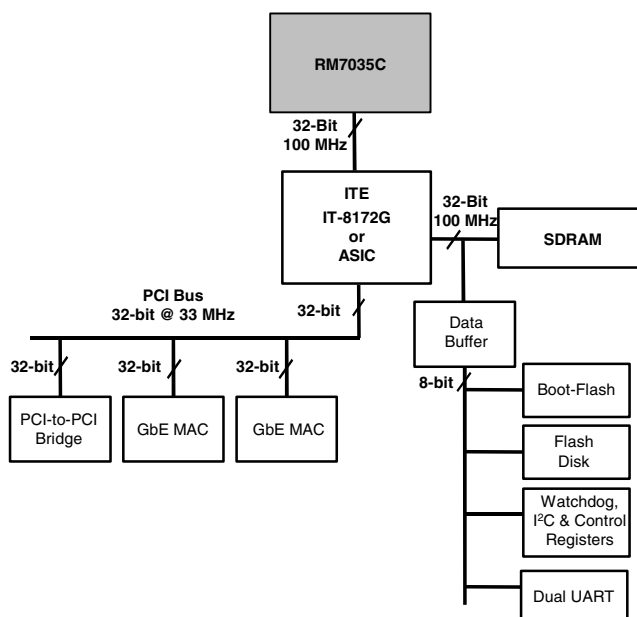
- Compiler Suites
 - GNU
 - MIPS Technologies
 - Green Hills Software
 - Red Hat
- Evaluation Boards and Companion Chips
 - ITE (RM7035C only):
 - System controller: IT8172G
 - Evaluation board: IT8172 PDK-BSP
 - Marvell Semiconductor (RM7065C only):
 - System controllers: MV-64340, GT-64240, GT-96122
 - Evaluation platforms: EV-64340, EV-64240. Both Marvell evaluation boards feature the RM7000C, which features the same CPU core as the RM7065C. The RM7065C does not provide L3 cache functionality.

- Momentum Computer
 - Evaluation platform: Ocelot C. Features RM7000C.
- Logic Analyzers and Emulation
 - HP
 - Tektronix
 - Corelis
 - Crescent Heart Software

APPLICATIONS

- Voice Gateways
- Multi-Service Access Platforms
- DSLAMs/Access Concentrators
- Remote Access Switches
- Web Switches
- Layer 3 Switches
- Backbone Switches/Routers
- RAIDs
- Set Top Boxes
- Networked Printers
- Cellular Base Stations

TYPICAL APPLICATIONS



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PMC-2020578 (P2)
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