

ELECTRICAL SPECIFICATIONS

Input		
Input voltage range	(See Note 3)	10.8 - 13.2 Vdc
Input current	No load	10 mA typical
Remote ON/OFF	(See Note 1)	Positive logic
Start-up time		1 V/ms
Undervoltage lockout		8.8 - 10.4 V typical
Track input voltage	Pin 8 (See Notes 6)	$\pm 0.3 V_{in}$
Output		
Voltage adjustability	(See Note 4)	1.2 - 5.5 Vdc (Suffix 'W') 0.8 - 1.8 Vdc (Suffix 'L')
Setpoint accuracy		$\pm 2.0\% V_o$
Line regulation		± 5 mV typical
Load regulation		± 5 mV typical
Total regulation		$\pm 3.0\% V_o$
Minimum load		0 A
Ripple and noise 20 MHz bandwidth	Suffix 'W': $V_o < 2.5$ V $V_o > 2.5$ V Suffix 'L': $V_o < 1.0$ V $V_o > 1.0$ V	25 mV pk-pk 1% V_o 20 mV pk-pk 30 mV pk-pk
Temperature co-efficient	-40 °C to +85 °C	$\pm 0.5\% V_o$
Transient response	(See Note 5)	70 μ s recovery time Overshoot/undershoot 100 mV
Margin adjustment		$\pm 5.0\% V_o$

All specifications are typical at nominal input, full load at 25 °C unless otherwise stated.
 $C_{in} = 100 \mu F$, $C_{out} = 0 \mu F$.

GENERAL SPECIFICATIONS

Efficiency		See Efficiency Table
Insulation voltage		Non-isolated
Switching frequency	Over V_{in} and I_o ranges	Suffix 'W': 320 kHz typical Suffix 'L': 250 kHz typical
Approvals and standards		EN60950, UL/cUL60950
Material flammability		UL94V-0
Dimensions	L x W x H	22.10 x 12.57 x 8.50 mm 0.870 x 0.495 x 0.335 in
Weight		2.9 g (0.10 oz)
MTBF	Telcordia SR-332	7,092,000 hours

EMC CHARACTERISTICS

Electrostatic discharge	EN61000-4-2, IEC801-2
Conducted immunity	EN61000-4-6
Radiated immunity	EN61000-4-3

ENVIRONMENTAL SPECIFICATIONS

Thermal performance (See Note 2)	Operating ambient temperature Non-operating temperature	-40 °C to +85 °C -40 °C to +125 °C
MSL ('Z' suffix only)	JEDEC J-STD-020C	Level 3
Protection		
Short-circuit	Auto reset	14 A typical

ORDERING INFORMATION

Model Number ⁽⁹⁾	Output Power (Max.)	Input Voltage	Output Voltage	Output Current (Min.)	Output Current (Max.)	Efficiency (Typical)	Regulation	
							Line	Load
PTH12050L	33 W	10.8 - 13.2 Vdc	0.8 - 1.8 Vdc	0 A	6 A	88%	±5 mV	±5 mV
PTH12050W	33 W	10.8 - 13.2 Vdc	1.2 - 5.5 Vdc	0 A	6 A	93%	±5 mV	±5 mV

PART NUMBER SYSTEM WITH OPTIONS

Product Family	Input Voltage	Output Current	Mechanical Package	Output Voltage Code	Pin Option ⁽⁸⁾	Mounting Options	Pin Option
PTH	12	05	0	W	A	S	T
Point-of-Load Alliance compatible	12 = 12 V	05 = 6 A	Always 0	W = Wide L = Low Voltage		D = Horizontal through-hole (RoHS 6/6) Z = Surface-mount solder ball (RoHS 6/6)	No Suffix = Trays T = Tape and Reel ⁽⁸⁾

OUTPUT VOLTAGE ADJUSTMENT

The ultra-wide output voltage trim range offers major advantages to users who select the PTH12050. It is no longer necessary to purchase a variety of modules in order to cover different output voltages. The output voltage can be trimmed in a range of 1.2 Vdc to 5.5 Vdc for suffix 'W' and 0.8 Vdc to 1.8 Vdc for suffix 'L'. When the PTH12050 converter leaves the factory the output has been adjusted to the default voltage of 1.2 V for the PTH12050W and 0.8 V for the PTH12050L.

Efficiency Table: PTH12050W (Io = 5 A)

Output Voltage	Efficiency
Vo = 5.0 V	93%
Vo = 3.3 V	91%
Vo = 2.5 V	89%
Vo = 2.0 V	88%
Vo = 1.8 V	87%
Vo = 1.5 V	86%
Vo = 1.2 V	84%

Efficiency Table: PTH12050L (Io = 5 A)

Output Voltage	Efficiency
Vo = 1.8 V	88%
Vo = 1.5 V	87%
Vo = 1.2 V	85%
Vo = 1.0 V	83%
Vo = 0.8 V	81%

Notes:

1. Remote ON/OFF, Positive Logic

ON: Pin 3 open; or $V > V_{in} - 0.5\text{ V}$

OFF: Pin 3 GND; or $V < 0.8\text{ V}$ (min - 0.2 V).

2. See Figure 1 for safe operating curve.

3. A 100 μF electrolytic input capacitor is required for proper operation. The capacitor must be rated for a minimum of 750 mA rms of ripple current. C2 = 10 μF ceramic capacitor, required for output voltages of 3.3 V and higher.

4. An external output capacitor is not required for basic operation. Adding 100 μF of distributed capacitance at the load will improve the transient response.

5. 1 A/ μs load step, 50 to 100% I_{omax}, C_{out} = 100 μF .

6. If utilized V_{out} will track applied voltage by $\pm 0.3\text{ V}$ (up to V_o set point).

7. Tape and reel packaging only available on the surface-mount versions.

8. The pk-pk output ripple voltage is measured with an external 10 μF ceramic capacitor. See Figure 3 for Standard application schematic.

9. NOTICE: Some models do not support all options. Please contact your local Artesyn representative or use the on-line model number search tool at <http://www.artesyn.com> to find a suitable alternative.

CHARACTERISTIC DATA

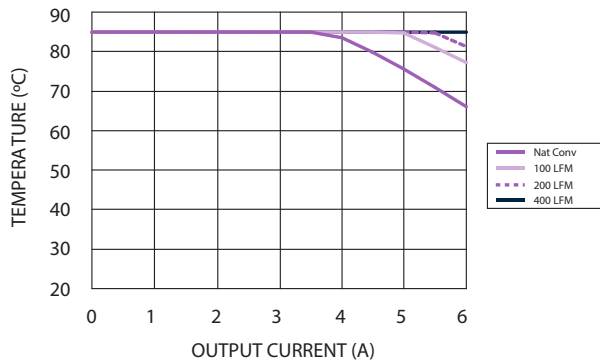


Figure 1 - Safe Operating Area for PTH12050W
Vin = 12 V, Output Voltage = 3.3 V (See Note A)

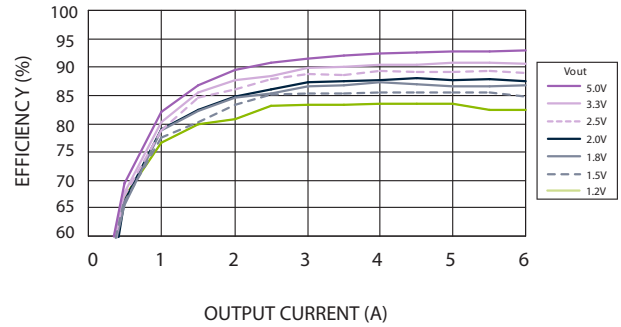


Figure 2 - Efficiency vs Load Current for PTH12050W
Vin = 12 V (See Note B)

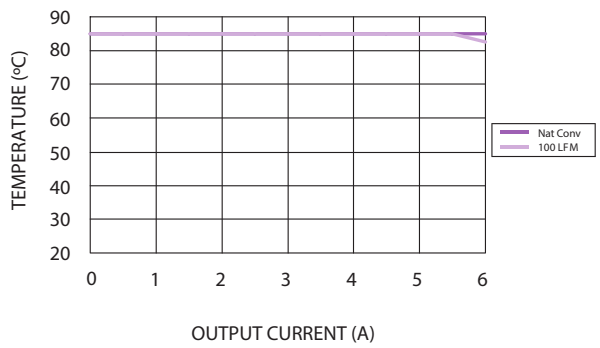


Figure 3 - Safe Operating Area for PTH12050L
Vin = 12 V, Output Voltage = 1.8 V (See Note A)

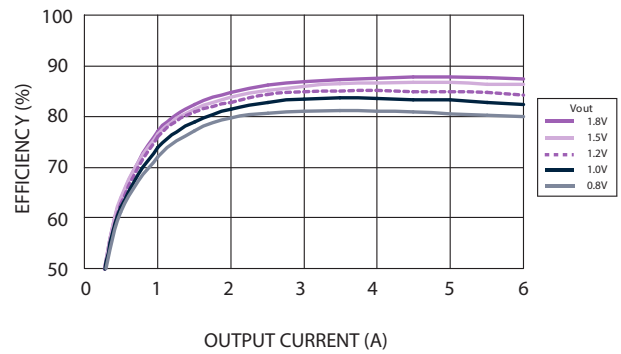


Figure 4 - Efficiency vs Load Current for PTH12050L
Vin = 12 V (See Note B)

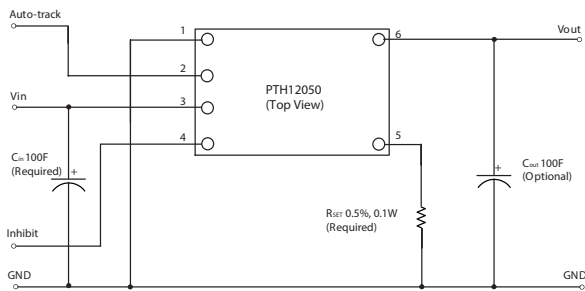


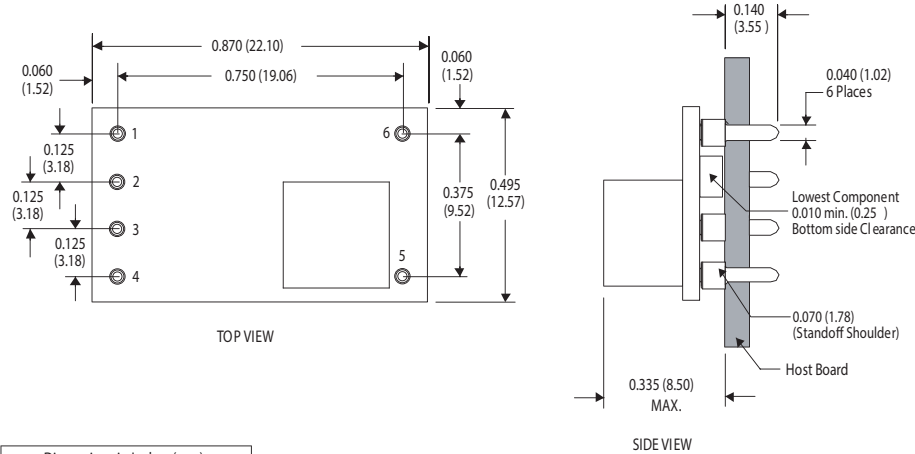
Figure 5 - Standard Application - All Models

Notes:

- SOA curves represent the conditions at which internal components are within the Artesyn derating guidelines.
- Characteristic data has been developed from actual products tested at 25 °C. This data is considered typical data for the converter.

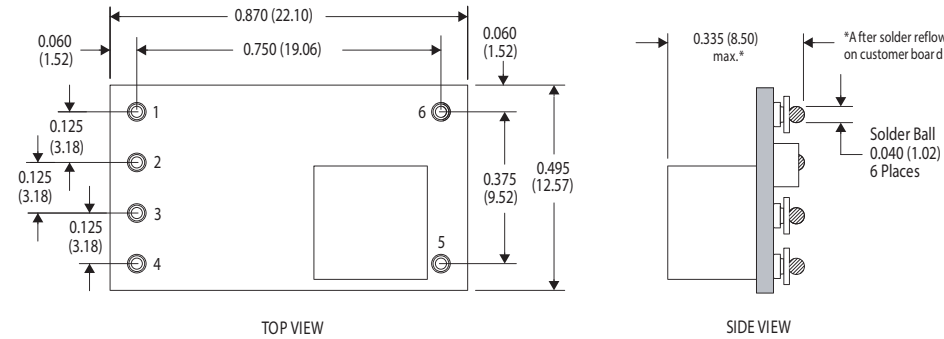
MECHANICAL DRAWINGS

Plated through-hole



Dimensions in Inches (mm)
Tolerances (unless otherwise specified)
2 Places 0.030 (0.76)
3 Places 0.010 (0.25)

Surface-mount



Dimensions in Inches (mm)
Tolerances (unless otherwise specified)
2 Places 0.030 (0.76)
3 Places 0.010 (0.25)

Pin Assignments	
Pin	Function
1	Ground
2	Track
3	Vin
4	Inhibit*
5	Vo adjust
6	Vout
*Denotes negative logic: Open = Normal operation Ground = Function active	



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