Contents PD84008L-E

Contents

1	Elec	Electrical data					
	1.1	Maximum ratings	3				
	1.2	Thermal data	3				
2	Elec	trical characteristics	4				
	2.1	Static	4				
	2.2	Dynamic	4				
	2.3	ESD protection characteristics	4				
	2.4	Moisture sensitivity level	4				
3	Impe	edance	5				
4	Турі	cal performance	6				
5	Pack	kage mechanical data	9				
6	Revi	sion history	12				

PD84008L-E Electrical data

1 Electrical data

1.1 Maximum ratings

Table 2. Absolute maximum ratings $(T_{CASE} = 25^{\circ}C)$

Symbol	Parameter	Value	Unit
V _{(BR)DSS}	Drain-source voltage	25	V
V _{GS}	Gate-source voltage	-0.5 to +15	V
I _D	Drain current	7	Α
P _{DISS}	Power dissipation (@ T _C = 70 °C)	26.7	W
TJ	Max. operating junction temperature	150	°C
T _{STG}	Storage temperature	-65 to +150	°C

1.2 Thermal data

Table 3. Thermal data

Symbol	Parameter	Value	Unit
R_{thJC}	Junction - case thermal resistance	3	°C/W

Electrical characteristics PD84008L-E

2 Electrical characteristics

 $T_{CASE} = +25$ °C

2.1 Static

Table 4. Static

Symbol	Test conditions				Тур	Max	Unit
I _{DSS}	$V_{GS} = 0 V$	$V_{DS} = 25 V$				1	μΑ
I _{GSS}	V _{GS} = 5 V	V _{DS} = 0 V				1	μΑ
V _{GS(Q)}	V _{DS} = 10 V	I _D = 250mA		3.2		4.8	V
V _{DS(ON)}	V _{GS} = 10 V	I _D = 1 A			0.27	0.31	V
C _{ISS}	V _{GS} = 0 V	$V_{DS} = 7 V$	f = 1 MHz		57		pF
C _{OSS}	V _{GS} = 0 V	$V_{DS} = 7 V$	f = 1 MHz		46		pF
C _{RSS}	V _{GS} = 0 V	V _{DS} = 7 V	f = 1 MHz		2		рF

2.2 Dynamic

Table 5. Dynamic

Symbol	Test conditions	Min	Тур	Max	Unit
P3dB	$V_{DD} = 7.5 \text{ V}, I_{DQ} = 250 \text{ mA}$ f = 870 MHz	8	9		W
G _P	$V_{DD} = 7.5 \text{ V}, I_{DQ} = 250 \text{ mA}, P_{OUT} = 2 \text{ W}, f = 870 \text{ MHz}$	13	15.5		dB
h _D	$V_{DD} = 7.5 \text{ V}, I_{DQ} = 250 \text{ mA}, P_{OUT} = P3dB, f = 870 \text{ MHz}$		57		%
Load mismatch	$V_{DD} = 9.5 \text{ V}, I_{DQ} = 250 \text{ mA}, P_{OUT} = 10 \text{ W}, f = 870 \text{ MHz}$ All phase angles	20:1			VSWR

2.3 ESD protection characteristics

Table 6. ESD protection characteristics

Test conditions	Class
Human body model	2
Machine model	M3

2.4 Moisture sensitivity level

Table 7. Moisture sensitivity level

Test methodology	Rating
J-STD-020B	MSL 3

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PD84008L-E Impedance

3 Impedance

Figure 2. Current conventions

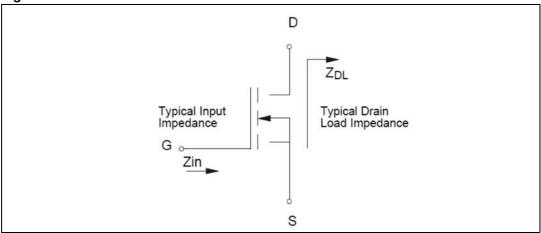


Table 8. Impedance data

Freq. (MHz)	Z _{IN} (Ω)	$Z_{DL}(\Omega)$
870 MHz	TBD	TBD

4 Typical performance

Figure 3. Capacitances vs drain voltage

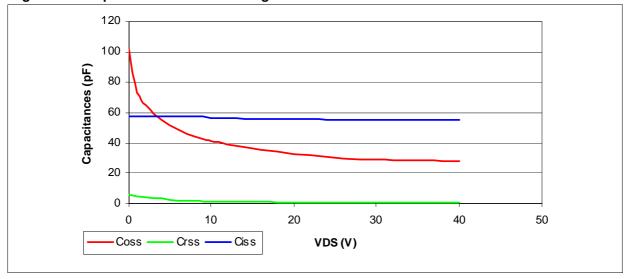
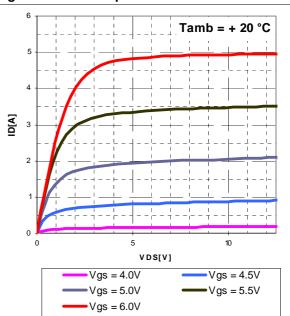


Figure 4. DC output characteristics

Tamb = -40 °C

| Tamb = -40 °C |
| Tamb = -40 °C

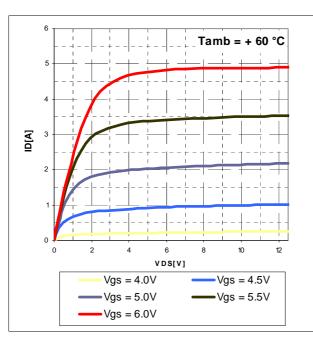
Figure 5. DC output characteristics



PD84008L-E Typical performance

Figure 6. DC output characteristics

Figure 7. Output power and drain current vs gate voltage



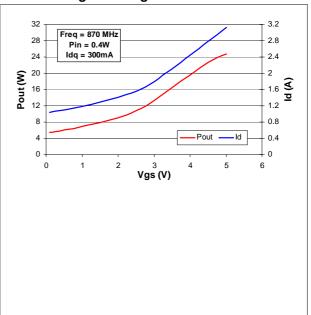
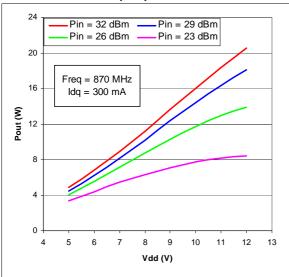


Figure 8. Output power vs supply voltage and input power

Figure 9. Gain and efficiency vs output power



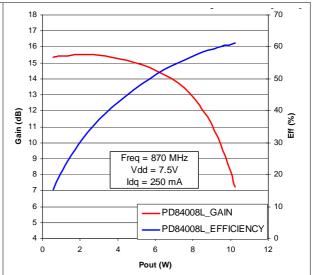
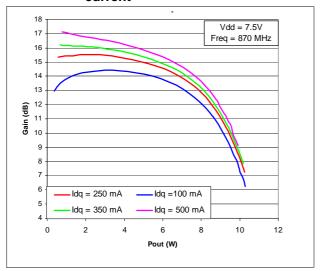


Figure 10. Gain vs output power and bias current



5 Package mechanical data

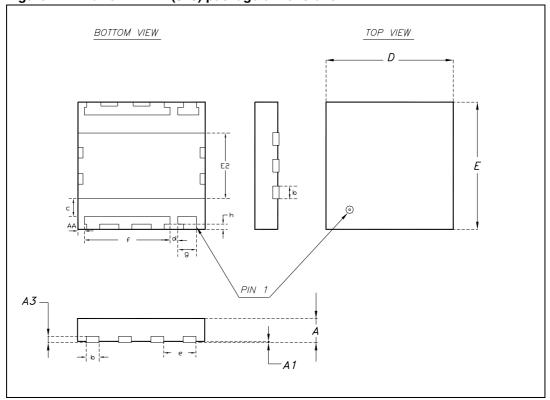
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Table 9. PowerFLAT™ (5x5) mechanical data

Dim.		mm			inch	
Dilli.	Min	Тур	Max	Min	Тур	Max
Α		0.90	1.00		0.035	0.039
A1		0.02	0.05		0.001	0.002
A3		0.24			0.009	
AA	0.15	0.25	0.35	0.006	0.01	0.014
b	0.43	0.51	0.58	0.017	0.020	0.023
С	0.64	0.71	0.79	0.025	0.028	0.031
D		5.00			0.197	
d		0.30			0.011	
E		5.00			0.197	
E2	2.49	2.57	2.64	0.098	0.101	0.104
е		1.27			0.050	
f		3.37			0.132	
g		0.74			0.03	
h		0.21			0.008	

Figure 11. PowerFLAT™ (5x5) package dimensions

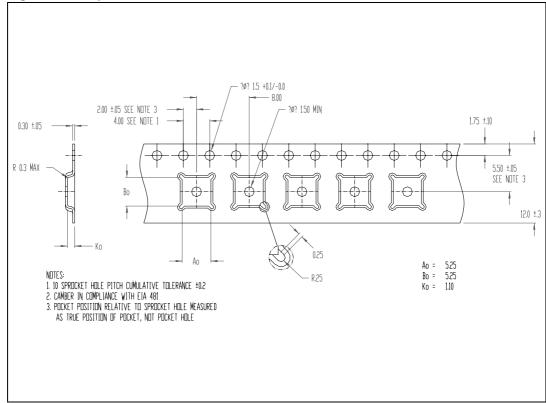


477

Table 10. Tape and reel dimensions

Dim.	Mm				
Dilli.	Min	Тур	Max		
Ao	5.15	5.25	5.35		
Во	5.15	5.25	5.35		
Ko	1.0	1.1	1.2		

Figure 12. Tape and reel dimensions



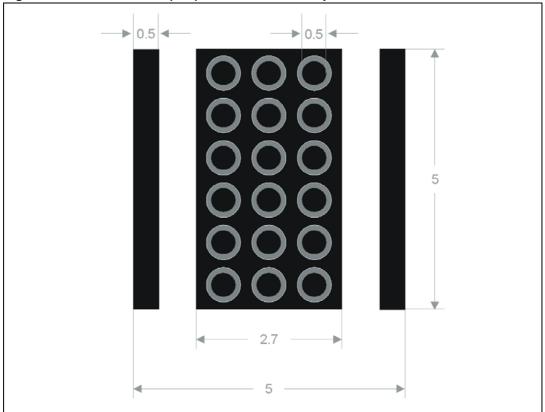


Figure 13. PowerFLAT™ (5x5) recommended footprint

47/

PD84008L-E Revision history

6 Revision history

Table 11. Document revision history

Date	Revision	Changes
05-Dec-2007	1	Initial release.
05-Mar-2008	2	Updated Table 4 on page 4.
15-Feb-2011	3	Updated Table 4 on page 4

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