

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.
For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V _{RRM}	200	V
Working Peak Reverse Voltage	V _{RWM}		
DC Blocking Voltage (Note 7) @ I _R = 5μA	V _R		
RMS Reverse Voltage	V _{R(RMS)}	141	V
Average Rectified Output Current @ T _T = +135°C	I _O	1.0	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	40	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Typical Total Capacitance (Note 6)	C _T	27	pF
Typical Thermal Resistance, Junction to Terminal (Note 5)	R _{θJT}	15	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Forward Voltage @ I _F = 1.0A, T _J = +25°C	V _{FM}	0.875	V
@ I _F = 1.0A, T _J = +150°C		0.710	
Peak Reverse Current @ T _A = +25°C	I _{RM}	2.0	μA
at Rated DC Blocking Voltage (Note 9) @ T _A = +150°C		50	
Reverse Recovery Time (Note 7)	t _{RR}	25	ns
Forward Recovery Time (Note 8)	t _{FR}	25	ns

- Notes:
5. Unit mounted on PC board with 5.0mm² (0.013mm thick) copper pads as heat sink.
 6. Measured at 1.0MHz and applied reverse voltage of 4V DC.
 7. Measured with I_F = 0.5A, I_R = 1.0A, I_{RR} = 0.25A. See Figure 5.
 8. Measured with I_F = 1.0A, di/dt = 100A/μs, Duty Cycle ≤ 2.0%.
 9. Short duration pulse test used to minimize self-heating effect.

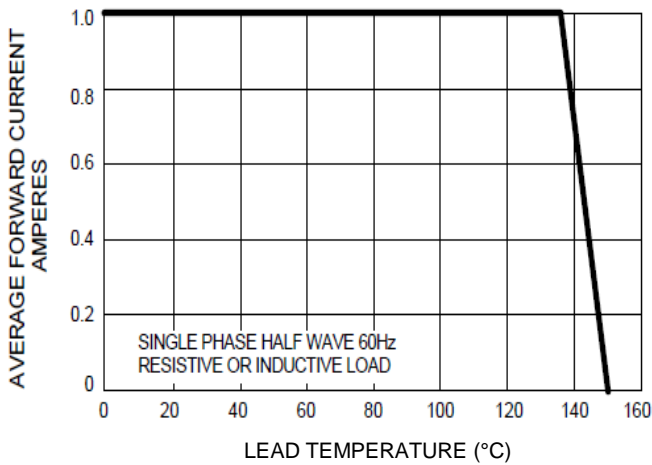


Fig. 1 Forward Current Derating Curve

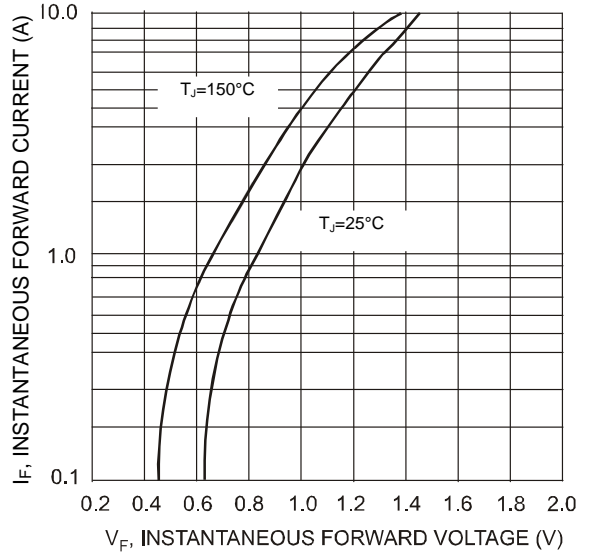


Fig. 2 Typical Forward Characteristics

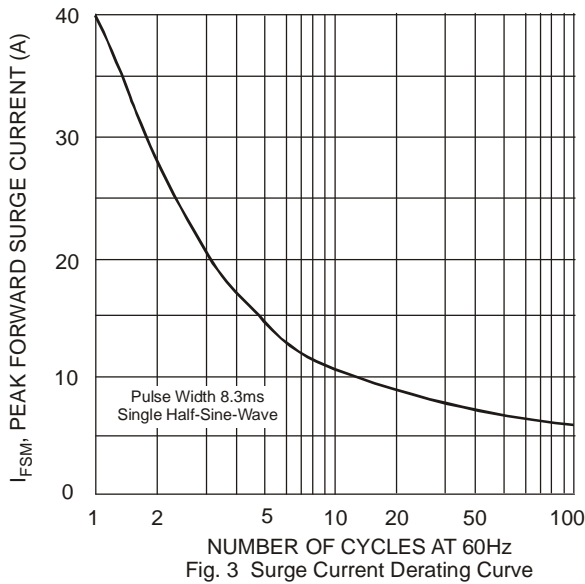


Fig. 3 Surge Current Derating Curve

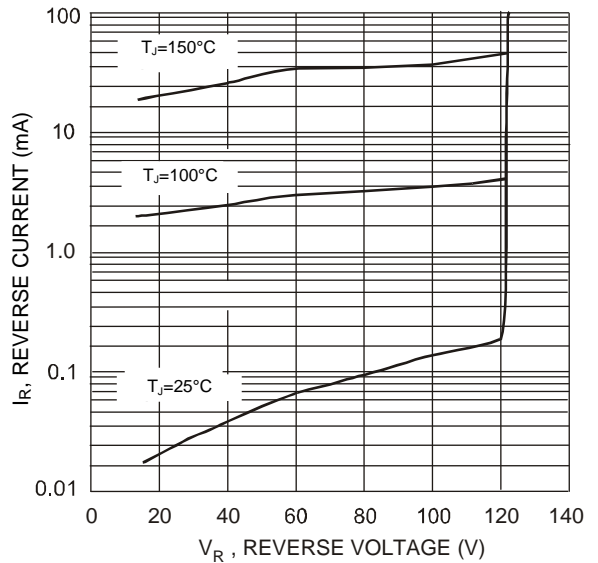
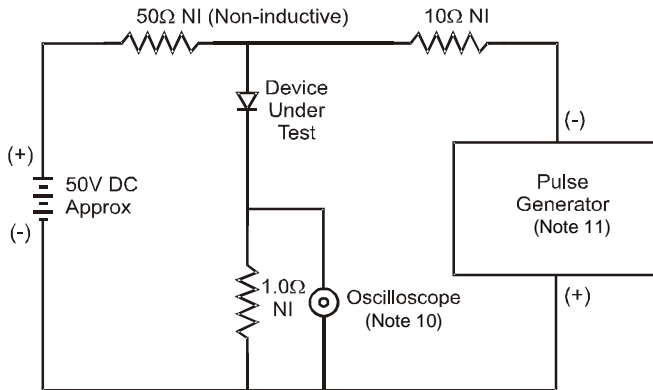
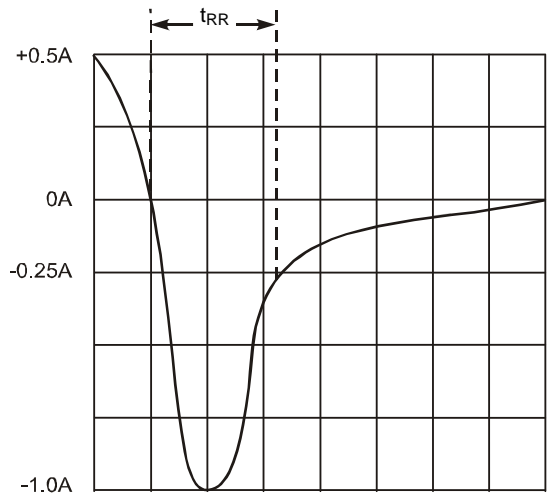


Fig. 4 Typical Reverse Characteristics



Notes:
10. Rise Time = 7.0ns max. Input Impedance = 1.0MΩ, 22pF.
11. Rise Time = 10ns max. Input Impedance = 50Ω.

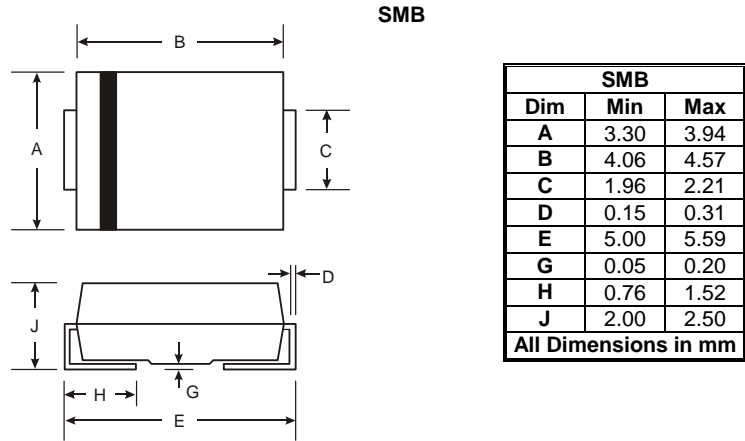


Set Time Base for 50/100 ns/cm

Fig. 5 Reverse Recovery Time Characteristic and Test Circuit

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.



Suggested Pad Layout

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