

# MC14013B

TRUTH TABLE

| Inputs                                                                            |      |       |     | Outputs |           |
|-----------------------------------------------------------------------------------|------|-------|-----|---------|-----------|
| Clock <sup>†</sup>                                                                | Data | Reset | Set | Q       | $\bar{Q}$ |
|  | 0    | 0     | 0   | 0       | 1         |
|  | 1    | 0     | 0   | 1       | 0         |
|  | X    | 0     | 0   | Q       | $\bar{Q}$ |
| X                                                                                 | X    | 1     | 0   | 0       | 1         |
| X                                                                                 | X    | 0     | 1   | 1       | 0         |
| X                                                                                 | X    | 1     | 1   | 1       | 1         |

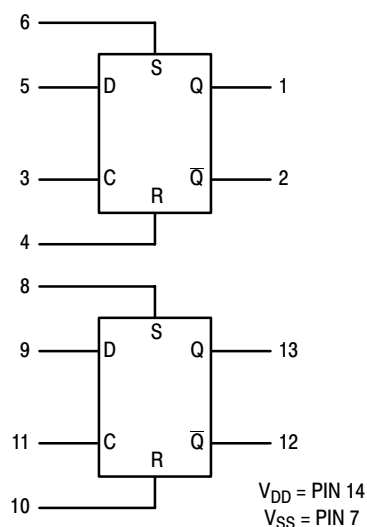
No  
Change

X = Don't Care  
† = Level Change

PIN ASSIGNMENT

|                 |   |   |    |                 |
|-----------------|---|---|----|-----------------|
| Q <sub>A</sub>  | 1 | • | 14 | V <sub>DD</sub> |
| $\bar{Q}_A$     | 2 |   | 13 | Q <sub>B</sub>  |
| C <sub>A</sub>  | 3 |   | 12 | $\bar{Q}_B$     |
| R <sub>A</sub>  | 4 |   | 11 | C <sub>B</sub>  |
| D <sub>A</sub>  | 5 |   | 10 | R <sub>B</sub>  |
| S <sub>A</sub>  | 6 |   | 9  | D <sub>B</sub>  |
| V <sub>SS</sub> | 7 |   | 8  | S <sub>B</sub>  |

BLOCK DIAGRAM



ORDERING INFORMATION

| Device        | Package             | Shipping <sup>†</sup>    |
|---------------|---------------------|--------------------------|
| MC14013BCP    | PDIP-14             | 25 Units / Rail          |
| MC14013BCPG   | PDIP-14 (Pb-Free)   |                          |
| MC14013BD     | SOIC-14             | 55 Units / Rail          |
| MC14013BDG    | SOIC-14 (Pb-Free)   |                          |
| MC14013BDR2   | SOIC-14             | 2500 Units / Tape & Reel |
| MC14013BDR2G  | SOIC-14 (Pb-Free)   |                          |
| MC14013BDTR2  | TSSOP-14*           |                          |
| MC14013BDTR2G | TSSOP-14*           |                          |
| MC14013BF     | SOEIAJ-14           | 50 Units / Rail          |
| MC14013BFG    | SOEIAJ-14 (Pb-Free) |                          |
| MC14013BFEL   | SOEIAJ-14           | 2000 Units / Tape & Reel |
| MC14013BFELG  | SOEIAJ-14 (Pb-Free) |                          |

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

\*This package is inherently Pb-Free.

# MC14013B

## ELECTRICAL CHARACTERISTICS (Voltages Referenced to V<sub>SS</sub>)

| Characteristic                                                                                                                                                                                                                                     | Symbol          | V <sub>DD</sub><br>Vdc | – 55° C                                                                                                                                                      |       | 25° C  |                    |       | 125° C |       | Unit |
|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------|------------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------|-------|--------|--------------------|-------|--------|-------|------|
|                                                                                                                                                                                                                                                    |                 |                        | Min                                                                                                                                                          | Max   | Min    | Typ <sup>(2)</sup> | Max   | Min    | Max   |      |
| Output Voltage<br>V <sub>in</sub> = V <sub>DD</sub> or 0<br><br>V <sub>in</sub> = 0 or V <sub>DD</sub>                                                                                                                                             | V <sub>OL</sub> | 5.0                    | –                                                                                                                                                            | 0.05  | –      | 0                  | 0.05  | –      | 0.05  | Vdc  |
|                                                                                                                                                                                                                                                    |                 | 10                     | –                                                                                                                                                            | 0.05  | –      | 0                  | 0.05  | –      | 0.05  |      |
|                                                                                                                                                                                                                                                    |                 | 15                     | –                                                                                                                                                            | 0.05  | –      | 0                  | 0.05  | –      | 0.05  |      |
|                                                                                                                                                                                                                                                    | V <sub>OH</sub> | 5.0                    | 4.95                                                                                                                                                         | –     | 4.95   | 5.0                | –     | 4.95   | –     | Vdc  |
|                                                                                                                                                                                                                                                    |                 | 10                     | 9.95                                                                                                                                                         | –     | 9.95   | 10                 | –     | 9.95   | –     |      |
|                                                                                                                                                                                                                                                    |                 | 15                     | 14.95                                                                                                                                                        | –     | 14.95  | 15                 | –     | 14.95  | –     |      |
| Input Voltage<br>(V <sub>O</sub> = 4.5 or 0.5 Vdc)<br>(V <sub>O</sub> = 9.0 or 1.0 Vdc)<br>(V <sub>O</sub> = 13.5 or 1.5 Vdc)<br><br>(V <sub>O</sub> = 0.5 or 4.5 Vdc)<br>(V <sub>O</sub> = 1.0 or 9.0 Vdc)<br>(V <sub>O</sub> = 1.5 or 13.5 Vdc)  | V <sub>IL</sub> | 5.0                    | –                                                                                                                                                            | 1.5   | –      | 2.25               | 1.5   | –      | 1.5   | Vdc  |
|                                                                                                                                                                                                                                                    |                 | 10                     | –                                                                                                                                                            | 3.0   | –      | 4.50               | 3.0   | –      | 3.0   |      |
|                                                                                                                                                                                                                                                    |                 | 15                     | –                                                                                                                                                            | 4.0   | –      | 6.75               | 4.0   | –      | 4.0   |      |
|                                                                                                                                                                                                                                                    | V <sub>IH</sub> | 5.0                    | 3.5                                                                                                                                                          | –     | 3.5    | 2.75               | –     | 3.5    | –     | Vdc  |
|                                                                                                                                                                                                                                                    |                 | 10                     | 7.0                                                                                                                                                          | –     | 7.0    | 5.50               | –     | 7.0    | –     |      |
|                                                                                                                                                                                                                                                    |                 | 15                     | 11                                                                                                                                                           | –     | 11     | 8.25               | –     | 11     | –     |      |
| Output Drive Current<br>(V <sub>OH</sub> = 2.5 Vdc)<br>(V <sub>OH</sub> = 4.6 Vdc)<br>(V <sub>OH</sub> = 9.5 Vdc)<br>(V <sub>OH</sub> = 13.5 Vdc)<br><br>(V <sub>OL</sub> = 0.4 Vdc)<br>(V <sub>OL</sub> = 0.5 Vdc)<br>(V <sub>OL</sub> = 1.5 Vdc) | I <sub>OH</sub> | 5.0                    | – 3.0                                                                                                                                                        | –     | – 2.4  | – 4.2              | –     | – 1.7  | –     | mAdc |
|                                                                                                                                                                                                                                                    |                 | 5.0                    | – 0.64                                                                                                                                                       | –     | – 0.51 | – 0.88             | –     | – 0.36 | –     |      |
|                                                                                                                                                                                                                                                    |                 | 10                     | – 1.6                                                                                                                                                        | –     | – 1.3  | – 2.25             | –     | – 0.9  | –     |      |
|                                                                                                                                                                                                                                                    |                 | 15                     | – 4.2                                                                                                                                                        | –     | – 3.4  | – 8.8              | –     | – 2.4  | –     |      |
|                                                                                                                                                                                                                                                    | I <sub>OL</sub> | 5.0                    | 0.64                                                                                                                                                         | –     | 0.51   | 0.88               | –     | 0.36   | –     | mAdc |
|                                                                                                                                                                                                                                                    |                 | 10                     | 1.6                                                                                                                                                          | –     | 1.3    | 2.25               | –     | 0.9    | –     |      |
|                                                                                                                                                                                                                                                    |                 | 15                     | 4.2                                                                                                                                                          | –     | 3.4    | 8.8                | –     | 2.4    | –     |      |
| Input Current                                                                                                                                                                                                                                      | I <sub>in</sub> | 15                     | –                                                                                                                                                            | ± 0.1 | —      | ± 0.00001          | ± 0.1 | –      | ± 1.0 | μAdc |
| Input Capacitance<br>(V <sub>in</sub> = 0)                                                                                                                                                                                                         | C <sub>in</sub> | –                      | –                                                                                                                                                            | –     | –      | 5.0                | 7.5   | –      | –     | pF   |
| Quiescent Current<br>(Per Package)                                                                                                                                                                                                                 | I <sub>DD</sub> | 5.0                    | –                                                                                                                                                            | 1.0   | –      | 0.002              | 1.0   | –      | 30    | μAdc |
|                                                                                                                                                                                                                                                    |                 | 10                     | –                                                                                                                                                            | 2.0   | –      | 0.004              | 2.0   | –      | 60    |      |
|                                                                                                                                                                                                                                                    |                 | 15                     | –                                                                                                                                                            | 4.0   | –      | 0.006              | 4.0   | –      | 120   |      |
| Total Supply Current <sup>(3) (4)</sup><br>(Dynamic plus Quiescent,<br>Per Package)<br>(C <sub>L</sub> = 50 pF on all outputs, all<br>buffers switching)                                                                                           | I <sub>T</sub>  | 5.0<br>10<br>15        | I <sub>T</sub> = (0.75 μA/kHz) f + I <sub>DD</sub><br>I <sub>T</sub> = (1.5 μA/kHz) f + I <sub>DD</sub><br>I <sub>T</sub> = (2.3 μA/kHz) f + I <sub>DD</sub> |       |        |                    |       |        |       | μAdc |

2. Data labelled "Typ" is not to be used for design purposes but is intended as an indication of the IC's potential performance.

3. The formulas given are for the typical characteristics only at 25° C.

4. To calculate total supply current at loads other than 50 pF:

$$I_T(C_L) = I_T(50 \text{ pF}) + (C_L - 50) Vfk$$

where: I<sub>T</sub> is in μA (per package), C<sub>L</sub> in pF, V = (V<sub>DD</sub> – V<sub>SS</sub>) in volts, f in kHz is input frequency, and k = 0.002.

# MC14013B

## SWITCHING CHARACTERISTICS <sup>(5)</sup> ( $C_L = 50 \text{ pF}$ , $T_A = 25^\circ\text{C}$ )

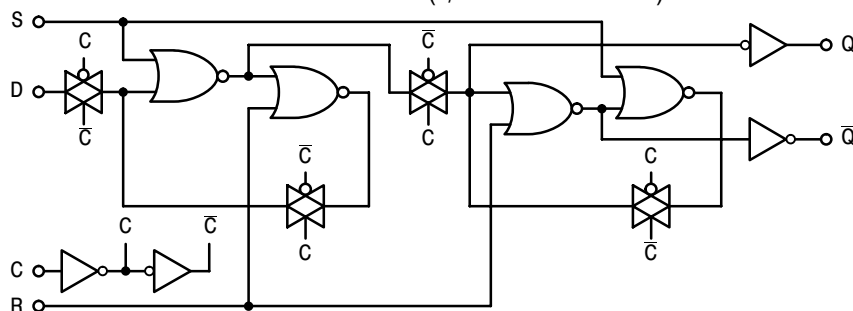
| Characteristic                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 | Symbol                   | $V_{DD}$                                                      | Min                                               | Typ <sup>(6)</sup>                                             | Max                                                                 | Unit          |
|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------------|---------------------------------------------------------------|---------------------------------------------------|----------------------------------------------------------------|---------------------------------------------------------------------|---------------|
| Output Rise and Fall Time<br>$t_{TLH}$ , $t_{THL} = (1.5 \text{ ns/pF}) C_L + 25 \text{ ns}$<br>$t_{TLH}$ , $t_{THL} = (0.75 \text{ ns/pF}) C_L + 12.5 \text{ ns}$<br>$t_{TLH}$ , $t_{THL} = (0.55 \text{ ns/pF}) C_L + 9.5 \text{ ns}$                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | $t_{TLH}$ ,<br>$t_{THL}$ | 5.0<br>10<br>15                                               | –<br>–<br>–                                       | 100<br>50<br>40                                                | 200<br>100<br>80                                                    | ns            |
| Propagation Delay Time<br>Clock to Q, $\bar{Q}$<br>$t_{PLH}$ , $t_{PHL} = (1.7 \text{ ns/pF}) C_L + 90 \text{ ns}$<br>$t_{PLH}$ , $t_{PHL} = (0.66 \text{ ns/pF}) C_L + 42 \text{ ns}$<br>$t_{PLH}$ , $t_{PHL} = (0.5 \text{ ns/pF}) C_L + 25 \text{ ns}$<br>Set to Q, $\bar{Q}$<br>$t_{PLH}$ , $t_{PHL} = (1.7 \text{ ns/pF}) C_L + 90 \text{ ns}$<br>$t_{PLH}$ , $t_{PHL} = (0.66 \text{ ns/pF}) C_L + 42 \text{ ns}$<br>$t_{PLH}$ , $t_{PHL} = (0.5 \text{ ns/pF}) C_L + 25 \text{ ns}$<br>Reset to Q, $\bar{Q}$<br>$t_{PLH}$ , $t_{PHL} = (1.7 \text{ ns/pF}) C_L + 265 \text{ ns}$<br>$t_{PLH}$ , $t_{PHL} = (0.66 \text{ ns/pF}) C_L + 67 \text{ ns}$<br>$t_{PLH}$ , $t_{PHL} = (0.5 \text{ ns/pF}) C_L + 50 \text{ ns}$ | $t_{PLH}$ ,<br>$t_{PHL}$ | 5.0<br>10<br>15<br><br>5.0<br>10<br>15<br><br>5.0<br>10<br>15 | –<br>–<br>–<br><br>–<br>–<br>–<br><br>–<br>–<br>– | 175<br>75<br>50<br><br>175<br>75<br>50<br><br>225<br>100<br>75 | 350<br>150<br>100<br><br>350<br>150<br>100<br><br>450<br>200<br>150 | ns            |
| Setup Times <sup>(7)</sup>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     | $t_{su}$                 | 5.0<br>10<br>15                                               | 40<br>20<br>15                                    | 20<br>10<br>7.5                                                | –<br>–<br>–                                                         | ns            |
| Hold Times <sup>(7)</sup>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      | $t_h$                    | 5.0<br>10<br>15                                               | 40<br>20<br>15                                    | 20<br>10<br>7.5                                                | –<br>–<br>–                                                         | ns            |
| Clock Pulse Width                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              | $t_{WL}$ , $t_{WH}$      | 5.0<br>10<br>15                                               | 250<br>100<br>70                                  | 125<br>50<br>35                                                | –<br>–<br>–                                                         | ns            |
| Clock Pulse Frequency                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          | $f_{cl}$                 | 5.0<br>10<br>15                                               | –<br>–<br>–                                       | 4.0<br>10<br>14                                                | 2.0<br>5.0<br>7.0                                                   | MHz           |
| Clock Pulse Rise and Fall Time                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 | $t_{TLH}$ ,<br>$t_{THL}$ | 5.0<br>10<br>15                                               | –<br>–<br>–                                       | –<br>–<br>–                                                    | 15<br>5.0<br>4.0                                                    | $\mu\text{s}$ |
| Set and Reset Pulse Width                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      | $t_{WL}$ , $t_{WH}$      | 5.0<br>10<br>15                                               | 250<br>100<br>70                                  | 125<br>50<br>35                                                | –<br>–<br>–                                                         | ns            |
| Removal Times<br>Set<br><br>Reset                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              | $t_{rem}$                | 5<br>10<br>15<br><br>5<br>10<br>15                            | 80<br>45<br>35<br><br>50<br>30<br>25              | 0<br>5<br>5<br><br>–35<br>–10<br>–5                            | –<br>–<br>–<br><br>–<br>–<br>–                                      | ns            |

5. The formulas given are for the typical characteristics only at  $25^\circ\text{C}$ .

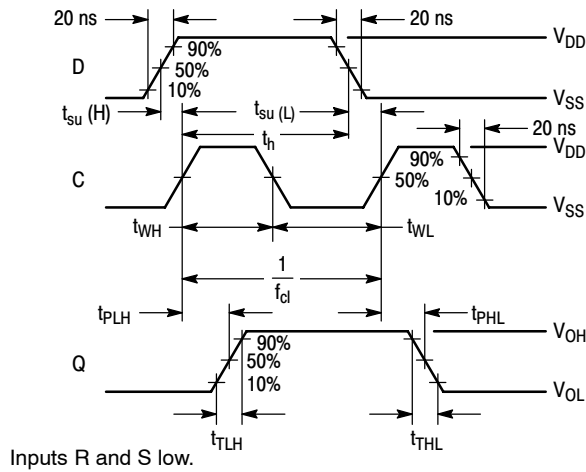
6. Data labelled "Typ" is not to be used for design purposes but is intended as an indication of the IC's potential performance.

7. Data must be valid for 250 ns with a 5 V supply, 100 ns with 10 V, and 70 ns with 15 V.

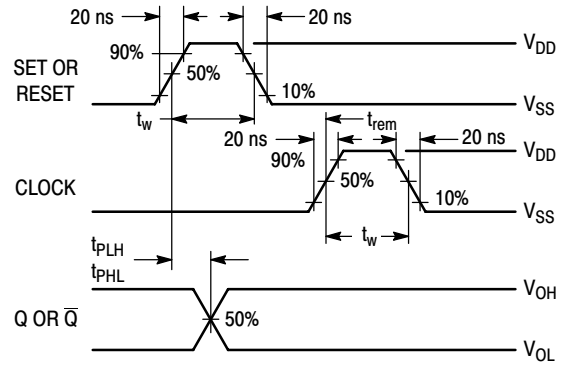
## LOGIC DIAGRAM (1/2 of Device Shown)



# MC14013B



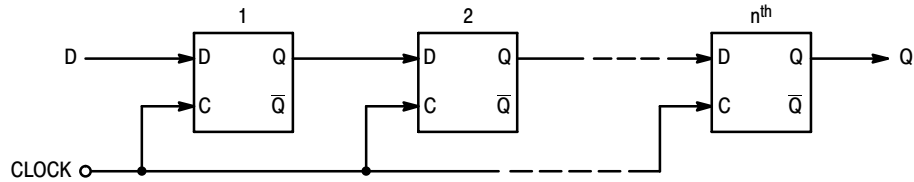
**Figure 1. Dynamic Signal Waveforms (Data, Clock, and Output)**



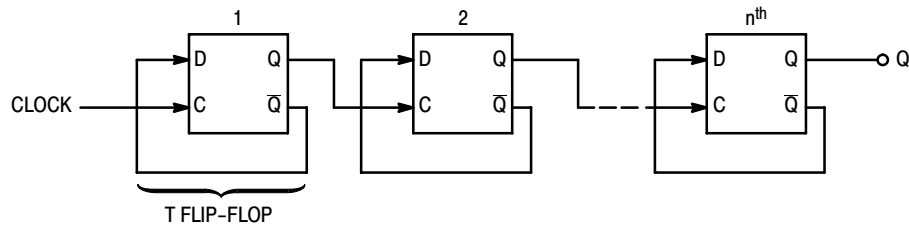
**Figure 2. Dynamic Signal Waveforms (Set, Reset, Clock, and Output)**

## TYPICAL APPLICATIONS

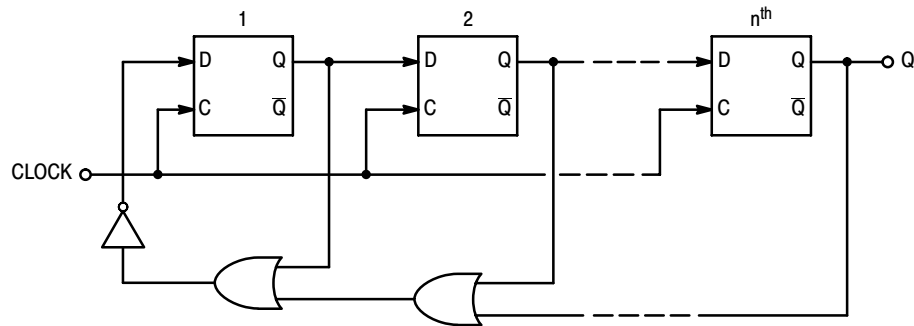
### n-STAGE SHIFT REGISTER



### BINARY RIPPLE UP-COUNTER (Divide-by- $2^n$ )



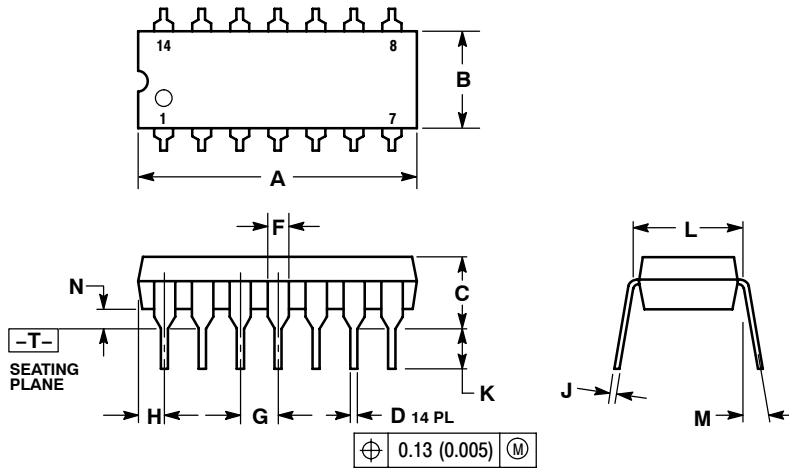
### MODIFIED RING COUNTER (Divide-by- $(n+1)$ )



# MC14013B

## PACKAGE DIMENSIONS

**PDIP-14**  
CASE 646-06  
ISSUE P



### NOTES:

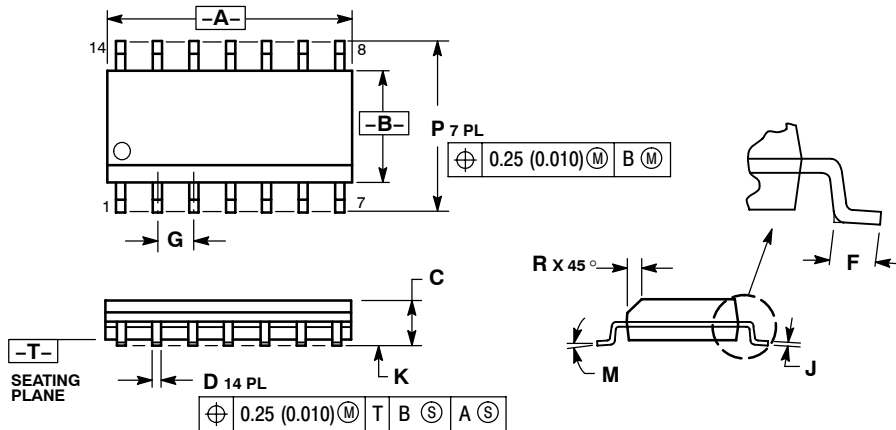
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. DIMENSION L TO CENTER OF LEADS WHEN FORMED PARALLEL.
4. DIMENSION B DOES NOT INCLUDE MOLD FLASH.
5. ROUNDED CORNERS OPTIONAL.

| DIM | INCHES    |       | MILLIMETERS |       |
|-----|-----------|-------|-------------|-------|
|     | MIN       | MAX   | MIN         | MAX   |
| A   | 0.715     | 0.770 | 18.16       | 19.56 |
| B   | 0.240     | 0.260 | 6.10        | 6.60  |
| C   | 0.145     | 0.185 | 3.69        | 4.69  |
| D   | 0.015     | 0.021 | 0.38        | 0.53  |
| F   | 0.040     | 0.070 | 1.02        | 1.78  |
| G   | 0.100 BSC |       | 2.54 BSC    |       |
| H   | 0.052     | 0.095 | 1.32        | 2.41  |
| J   | 0.008     | 0.015 | 0.20        | 0.38  |
| K   | 0.115     | 0.135 | 2.92        | 3.43  |
| L   | 0.290     | 0.310 | 7.37        | 7.87  |
| M   | ---       | 10°   | ---         | 10°   |
| N   | 0.015     | 0.039 | 0.38        | 1.01  |

# MC14013B

## PACKAGE DIMENSIONS

SOIC-14  
CASE 751A-03  
ISSUE H

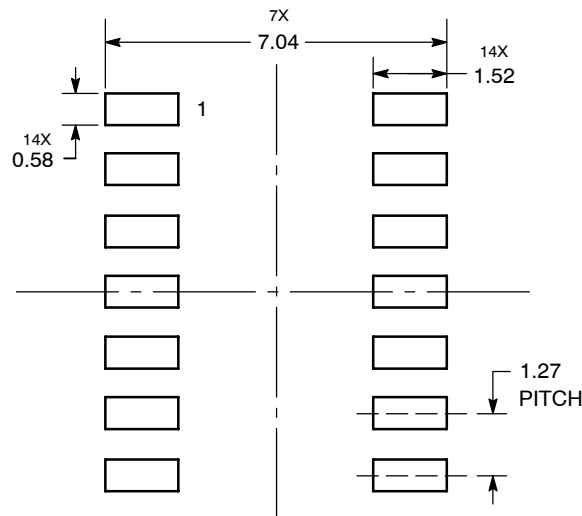


### NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETER.
3. DIMENSIONS A AND B DO NOT INCLUDE MOLD PROTRUSION.
4. MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE.
5. DIMENSION D DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 (0.005) TOTAL IN EXCESS OF THE D DIMENSION AT MAXIMUM MATERIAL CONDITION.

| DIM | MILLIMETERS |      | INCHES    |       |
|-----|-------------|------|-----------|-------|
|     | MIN         | MAX  | MIN       | MAX   |
| A   | 8.55        | 8.75 | 0.337     | 0.344 |
| B   | 3.80        | 4.00 | 0.150     | 0.157 |
| C   | 1.35        | 1.75 | 0.054     | 0.068 |
| D   | 0.35        | 0.49 | 0.014     | 0.019 |
| F   | 0.40        | 1.25 | 0.016     | 0.049 |
| G   | 1.27 BSC    |      | 0.050 BSC |       |
| J   | 0.19        | 0.25 | 0.008     | 0.009 |
| K   | 0.10        | 0.25 | 0.004     | 0.009 |
| M   | 0°          | 7°   | 0°        | 7°    |
| P   | 5.80        | 6.20 | 0.228     | 0.244 |
| R   | 0.25        | 0.50 | 0.010     | 0.019 |

### SOLDERING FOOTPRINT\*



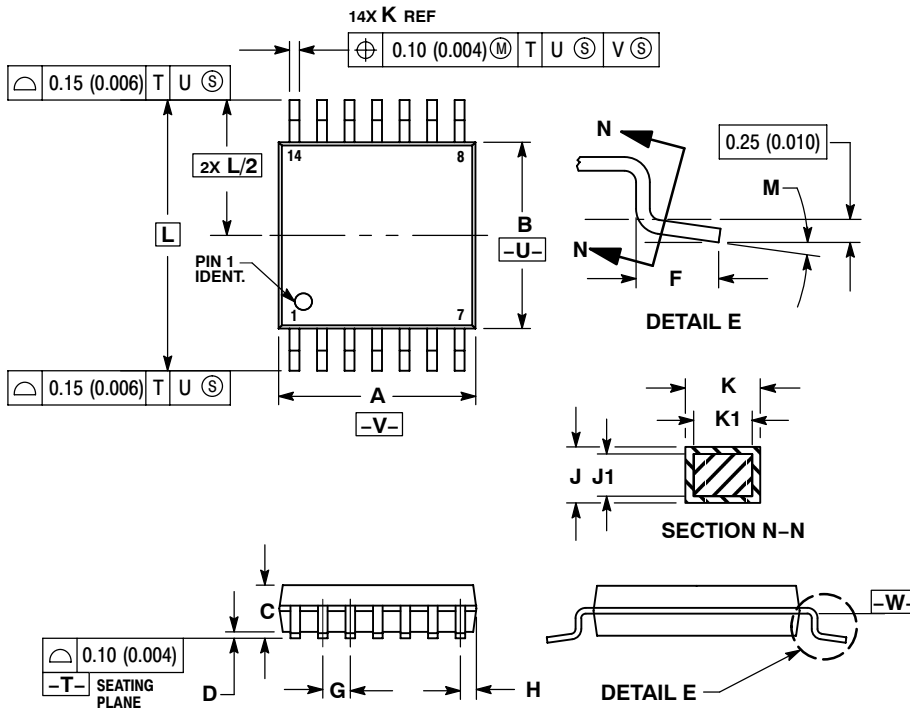
DIMENSIONS: MILLIMETERS

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

# MC14013B

## PACKAGE DIMENSIONS

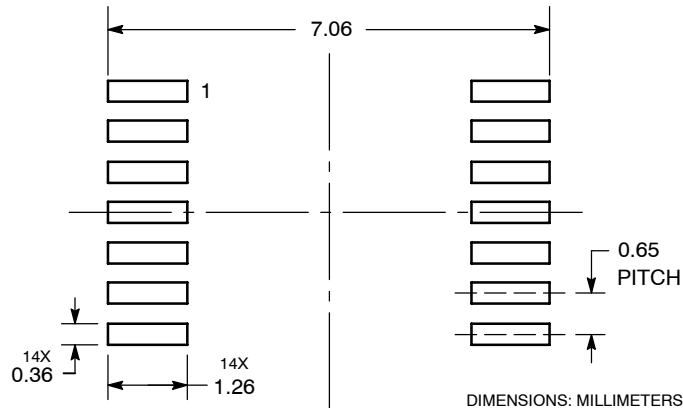
TSSOP-14  
CASE 948G-01  
ISSUE B



- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  2. CONTROLLING DIMENSION: MILLIMETER.
  3. DIMENSION A DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS. MOLD FLASH OR GATE BURRS SHALL NOT EXCEED 0.15 (0.006) PER SIDE.
  4. DIMENSION B DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSION. INTERLEAD FLASH OR PROTRUSION SHALL NOT EXCEED 0.25 (0.010) PER SIDE.
  5. DIMENSION K DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 (0.003) TOTAL IN EXCESS OF THE K DIMENSION AT MAXIMUM MATERIAL CONDITION.
  6. TERMINAL NUMBERS ARE SHOWN FOR REFERENCE ONLY.
  7. DIMENSION A AND B ARE TO BE DETERMINED AT DATUM PLANE -W-.

| DIM | MILLIMETERS |      | INCHES    |       |
|-----|-------------|------|-----------|-------|
|     | MIN         | MAX  | MIN       | MAX   |
| A   | 4.90        | 5.10 | 0.193     | 0.200 |
| B   | 4.30        | 4.50 | 0.169     | 0.177 |
| C   | ---         | 1.20 | ---       | 0.047 |
| D   | 0.05        | 0.15 | 0.002     | 0.006 |
| F   | 0.50        | 0.75 | 0.020     | 0.030 |
| G   | 0.65 BSC    |      | 0.026 BSC |       |
| H   | 0.50        | 0.60 | 0.020     | 0.024 |
| J   | 0.09        | 0.20 | 0.004     | 0.008 |
| J1  | 0.09        | 0.16 | 0.004     | 0.006 |
| K   | 0.19        | 0.30 | 0.007     | 0.012 |
| K1  | 0.19        | 0.25 | 0.007     | 0.010 |
| L   | 6.40 BSC    |      | 0.252 BSC |       |
| M   | 0°          | 8°   | 0°        | 8°    |

### SOLDERING FOOTPRINT\*

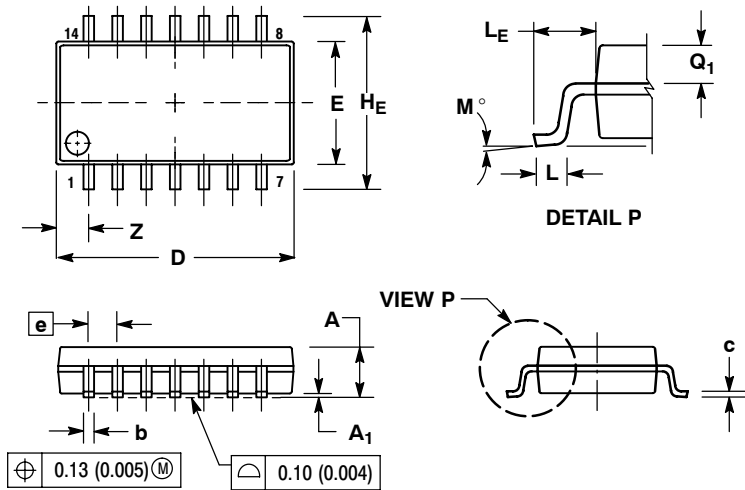


\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

# MC14013B

## PACKAGE DIMENSIONS

SOEIAJ-14  
CASE 965-01  
ISSUE A



### NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETER.
3. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS AND ARE MEASURED AT THE PARTING LINE. MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED 0.15 (0.006) PER SIDE.
4. TERMINAL NUMBERS ARE SHOWN FOR REFERENCE ONLY.
5. THE LEAD WIDTH DIMENSION (b) DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 (0.003) TOTAL IN EXCESS OF THE LEAD WIDTH DIMENSION AT MAXIMUM MATERIAL CONDITION. DAMBAR CANNOT BE LOCATED ON THE LOWER RADIUS OR THE FOOT. MINIMUM SPACE BETWEEN PROTRUSIONS AND ADJACENT LEAD TO BE 0.46 (0.018).

| DIM            | MILLIMETERS |       | INCHES    |       |
|----------------|-------------|-------|-----------|-------|
|                | MIN         | MAX   | MIN       | MAX   |
| A              | ---         | 2.05  | ---       | 0.081 |
| A <sub>1</sub> | 0.05        | 0.20  | 0.002     | 0.008 |
| b              | 0.35        | 0.50  | 0.014     | 0.020 |
| c              | 0.10        | 0.20  | 0.004     | 0.008 |
| D              | 9.90        | 10.50 | 0.390     | 0.413 |
| E              | 5.10        | 5.45  | 0.201     | 0.215 |
| e              | 1.27 BSC    |       | 0.050 BSC |       |
| H <sub>E</sub> | 7.40        | 8.20  | 0.291     | 0.323 |
| 0.50           | 0.50        | 0.85  | 0.020     | 0.033 |
| L <sub>E</sub> | 1.10        | 1.50  | 0.043     | 0.059 |
| M              | 0°          | 10°   | 0°        | 10°   |
| Q <sub>1</sub> | 0.70        | 0.90  | 0.028     | 0.035 |
| Z              | ---         | 1.42  | ---       | 0.056 |

ON Semiconductor and  are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

### PUBLICATION ORDERING INFORMATION

#### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor  
P.O. Box 5163, Denver, Colorado 80217 USA  
Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada  
Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada  
Email: [orderlit@onsemi.com](mailto:orderlit@onsemi.com)

N. American Technical Support: 800-282-9855 Toll Free  
USA/Canada

Europe, Middle East and Africa Technical Support:  
Phone: 421 33 790 2910

Japan Customer Focus Center  
Phone: 81-3-5773-3850

ON Semiconductor Website: [www.onsemi.com](http://www.onsemi.com)

Order Literature: <http://www.onsemi.com/orderlit>

For additional information, please contact your local  
Sales Representative

MC14013B/D