



_						
	herr	ทวเ	םם	CIC	ton	\sim

Parameter	Symbol	Conditions	Max. Value	Unit
Characteristic				
IGBT thermal resistance,	R_{thJC}		0.35	K/W
junction – case				
Diode thermal resistance,	R_{thJCD}		0.6	
junction – case				
Thermal resistance,	R_{thJA}		40	
junction – ambient				

Electrical Characteristic, at $T_j = 25$ °C, unless otherwise specified

Devemeter	Cumbal	Conditions	Value			Unit
Parameter	Symbol	Conditions	min.	Тур.	max.	Oilit
Static Characteristic						
Collector-emitter breakdown voltage	$V_{(BR)CES}$	$V_{\rm GE} = 0 \text{V}, I_{\rm C} = 0.2 \text{mA}$	600	-	-	V
Collector-emitter saturation voltage	$V_{CE(sat)}$	$V_{\rm GE} = 15 \rm V, \ I_{\rm C} = 75 \rm A$				
		<i>T</i> _j =25°C	-	1.5	2.0	
		<i>T</i> _j =175°C	-	1.9	-	
Diode forward voltage	V_{F}	$V_{GE} = 0 \text{ V}, I_{F} = 75 \text{ A}$				
		<i>T</i> _j =25°C	-	1.65	2.0	
		<i>T</i> _j =175°C	-	1.6	-	
Gate-emitter threshold voltage	$V_{\rm GE(th)}$	$I_{\rm C}$ =1.2mA, $V_{\rm CE}$ = $V_{\rm GE}$	4.1	4.9	5.7	
Zero gate voltage collector current	I _{CES}	V _{CE} =600V, V _{GE} =0V				μΑ
		<i>T</i> _j =25°C	-	-	40	
		<i>T</i> _j =175°C	-	-	5000	
Gate-emitter leakage current	I _{GES}	$V_{\text{CE}}=0\text{V}, V_{\text{GE}}=20\text{V}$	-	-	100	nA
Transconductance	g_{fs}	$V_{CE} = 20 \text{V}, I_{C} = 75 \text{A}$	-	41	-	S
Integrated gate resistor	R _{Gint}			-		Ω

Dynamic Characteristic

Input capacitance	Ciss	V _{CE} =25V,	-	4620	-	pF
Output capacitance	Coss	$V_{GE}=0V$,	-	288	-	
Reverse transfer capacitance	Crss	f=1MHz	-	137	-	
Gate charge	Q _{Gate}	$V_{\rm CC} = 480 \text{V}, I_{\rm C} = 75 \text{A}$	-	470	-	nC
		V _{GE} =15V				
Internal emitter inductance	LE		-	13	-	nΗ
measured 5mm (0.197 in.) from case						
Short circuit collector current	$I_{C(SC)}$	$V_{\text{GE}}=15\text{V}, t_{\text{SC}}\leq 5\mu\text{s}$	-	690	-	Α
Allowed number of short circuits: <1000; time between short circuits: >1s.		$V_{CC} = 400 \text{ V},$ $T_{j} \le 150^{\circ} \text{ C}$				





Switching Characteristic, Inductive Load, at T_j =25 °C

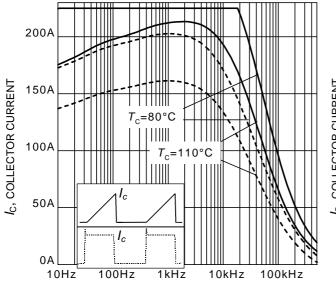
Davamatav	Cumbal	Conditions	Value			I I m i 4
Parameter	Symbol	Conditions	min.	typ.	max.	Unit
IGBT Characteristic						
Turn-on delay time	$t_{d(on)}$	T _j =25°C,	-	33	-	ns
Rise time	t_{r}	$V_{CC}=400V, I_{C}=75A, V_{GE}=0/15V,$	-	36	-	
Turn-off delay time	$t_{d(off)}$	$r_{\rm G}$ =5 Ω , L_{σ} =100nH,	-	330	-	
Fall time	t_{f}	C_{σ} =39pF	-	35	-	
Turn-on energy	Eon	L_{σ} , C_{σ} from Fig. E Energy losses include	-	2.0	-	mJ
Turn-off energy	E _{off}	"tail" and diode reverse	-	2.5	-	
Total switching energy	Ets	recovery.	-	4.5	-	
Anti-Parallel Diode Characteristic						•
Diode reverse recovery time	t_{rr}	<i>T</i> _j =25°C,	-	121	-	ns
Diode reverse recovery charge	Q_{rr}	V_{R} =400V, I_{F} =75A,	-	2.4	-	μC
Diode peak reverse recovery current	I _{rrm}	<i>di_F/dt</i> =1460A/μs	-	38.5	-	Α
Diode peak rate of fall of reverse recovery current during $t_{\rm b}$	di _{rr} /dt		-	921	-	A/μs

Switching Characteristic, Inductive Load, at T_j =175 °C

Donomoton	Cumbal	Conditions	Value			Unit
Parameter	Symbol	Conditions	min.	typ.	max.	Joint
IGBT Characteristic						
Turn-on delay time	$t_{d(on)}$	T _j =175°C,	-	32	-	ns
Rise time	t_{r}	$V_{CC}=400V, I_{C}=75A, V_{GE}=0/15V,$	-	37	-	
Turn-off delay time	$t_{d(off)}$	$r_{\rm G}=5\Omega$, $L_{\sigma}=100$ nH,	-	363	-	
Fall time	t_{f}	C_{σ} =39pF	-	38	-	
Turn-on energy	Eon	L_{σ} , C_{σ} from Fig. E Energy losses include	-	2.9	-	mJ
Turn-off energy	E_{off}	"tail" and diode reverse	-	2.9	-	
Total switching energy	E _{ts}	recovery.	-	5.8	-	
Anti-Parallel Diode Characteristic						
Diode reverse recovery time	t_{rr}	<i>T</i> _j =175°C	-	182	-	ns
Diode reverse recovery charge	Q _{rr}	V_{R} =400V, I_{F} =75A,	-	5.8	-	μC
Diode peak reverse recovery current	I _{rrm}	$di_F/dt=1460A/\mu s$	-	56.2	-	Α
Diode peak rate of fall of reverse recovery current during t_b	di _{rr} /dt		-	1013	-	A/μs







f, SWITCHING FREQUENCY

Figure 1. Collector current as a function of switching frequency $(T_i \le 175^{\circ}\text{C}, D = 0.5, V_{\text{CE}} = 400\text{V},$

 $V_{\rm GE} = 0/15 \text{V}, r_{\rm G} = 5\Omega$

 $V_{\rm CE}$, COLLECTOR-EMITTER VOLTAGE

Figure 2. Safe operating area $(D=0, T_C=25^{\circ}\text{C}, T_j \le 175^{\circ}\text{C}; V_{GE}=0/15\text{V})$

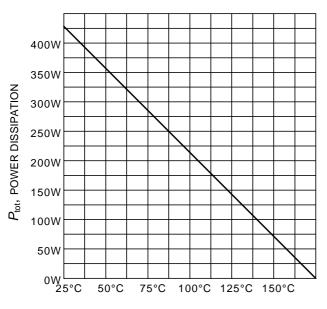
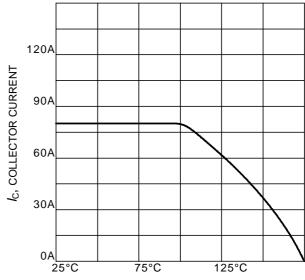




Figure 3. Power dissipation as a function of case temperature $(T_i \le 175^{\circ}\text{C})$



 $T_{\rm C}$, CASE TEMPERATURE

Figure 4. DC Collector current as a function of case temperature $(V_{GE} \ge 15 \text{V}, \ T_i \le 175 ^{\circ}\text{C})$





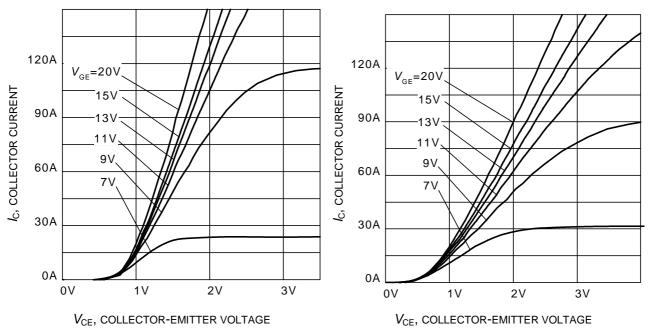


Figure 5. Typical output characteristic $(T_i = 25^{\circ}\text{C})$

Figure 6. Typical output characteristic $(T_i = 175^{\circ}\text{C})$

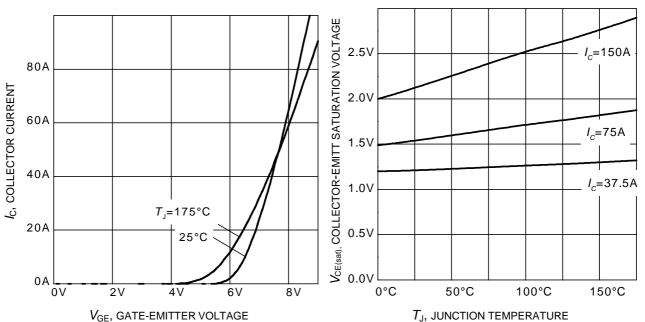
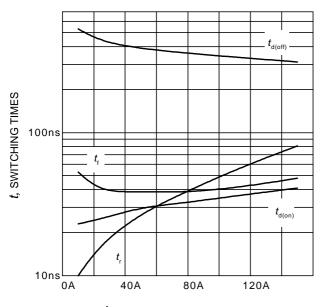


Figure 7. Typical transfer characteristic (V_{CE}=20V)

Figure 8. Typical collector-emitter saturation voltage as a function of junction temperature $(V_{GE} = 15V)$

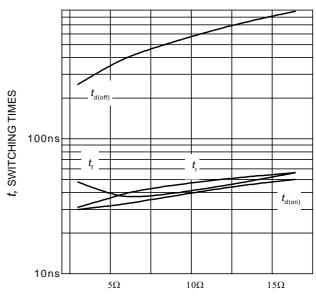






I_C, COLLECTOR CURRENT

Figure 9. Typical switching times as a function of collector current (inductive load, T_J =175°C, V_{CE} = 400V, V_{GE} = 0/15V, r_G = 5 Ω , Dynamic test circuit in Figure E)



R_G, GATE RESISTOR

Figure 10. Typical switching times as a function of gate resistor (inductive load, T_J = 175°C, V_{CE} = 400V, V_{GE} = 0/15V, I_C = 75A, Dynamic test circuit in Figure E)

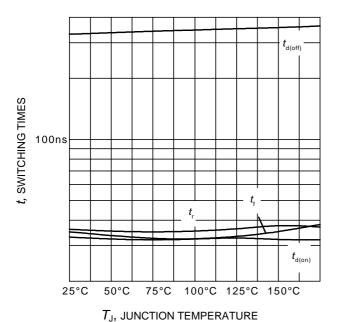
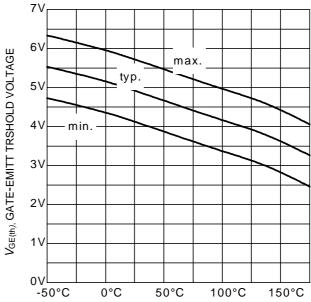


Figure 11. Typical switching times as a function of junction temperature

(inductive load, $V_{\rm CE}$ = 400V, $V_{\rm GE}$ = 0/15V, $I_{\rm C}$ = 75A, $r_{\rm G}$ =5 Ω , Dynamic test circuit in Figure E)



 $T_{
m J}$, JUNCTION TEMPERATURE

Figure 12. Gate-emitter threshold voltage as a function of junction temperature $(I_C = 1.2 \text{mA})$





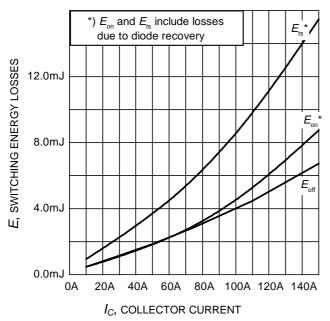


Figure 13. Typical switching energy losses as a function of collector current (inductive load, $T_J = 175^{\circ}\text{C}$, $V_{\text{CE}} = 400\text{V}$, $V_{\text{GE}} = 0/15\text{V}$, $r_{\text{G}} = 5\Omega$, Dynamic test circuit in Figure E)

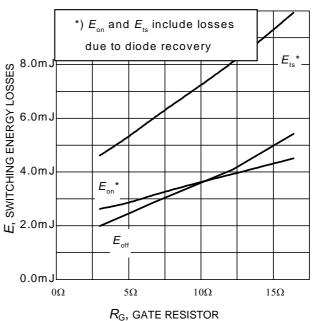


Figure 14. Typical switching energy losses as a function of gate resistor (inductive load, $T_J = 175$ °C, $V_{CE} = 400$ V, $V_{GE} = 0/15$ V, $I_C = 75$ A, Dynamic test circuit in Figure E)

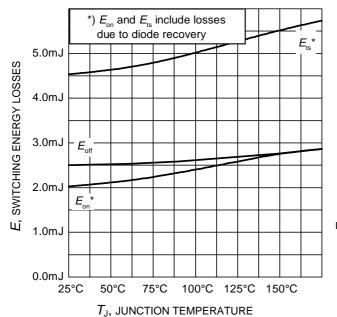
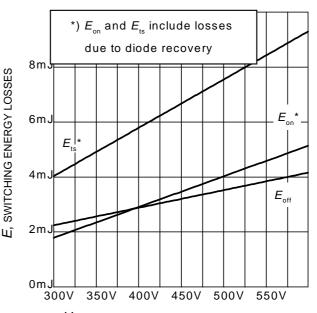


Figure 15. Typical switching energy losses as a function of junction temperature (inductive load, Voc = 400V

(inductive load, $V_{\rm CE}$ = 400V, $V_{\rm GE}$ = 0/15V, $I_{\rm C}$ = 75A, $r_{\rm G}$ = 5 Ω , Dynamic test circuit in Figure E)



 V_{CE} , COLLECTOR-EMITTER VOLTAGE

Figure 16. Typical switching energy losses as a function of collector emitter voltage

(inductive load, T_J = 175°C, V_{GE} = 0/15V, I_C = 75A, r_G = 5 Ω , Dynamic test circuit in Figure E)





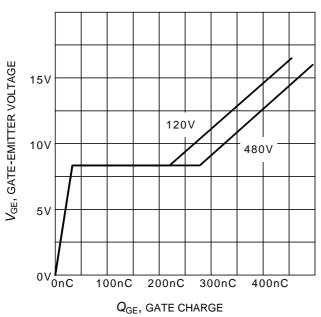
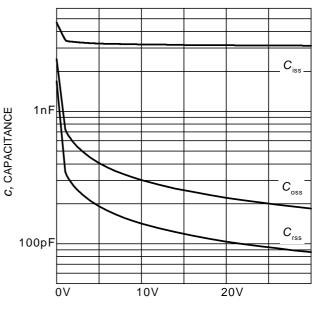
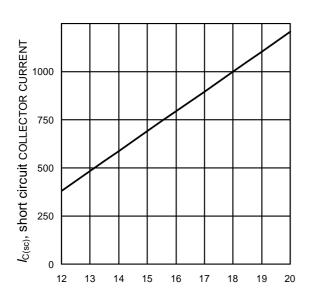


Figure 17. Typical gate charge $(I_C=75 \text{ A})$

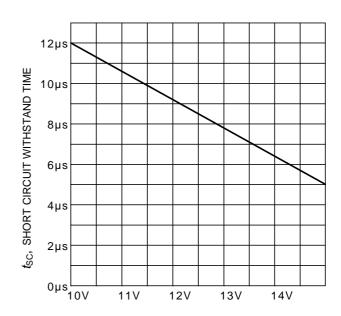


 $V_{\rm CE}$, COLLECTOR-EMITTER VOLTAGE

Figure 18. Typical capacitance as a function of collector-emitter voltage $(V_{GE}=0V, f=1 \text{ MHz})$



 $V_{\rm GE}$, GATE-EMITTER VOLTAGE Figure 19. Typical short circuit collector current as a function of gateemitter voltage ($V_{\rm CE} \le 400 \, {\rm V}$, $T_{\rm i} \le 150 \, {\rm ^{\circ}C}$)



 V_{GE} , gate- emitter voltage

Figure 20. Short circuit withstand time as a function of gate-emitter voltage (V_{CE} =400V, start at T_{J} =25°C, T_{Jmax} <150°C)





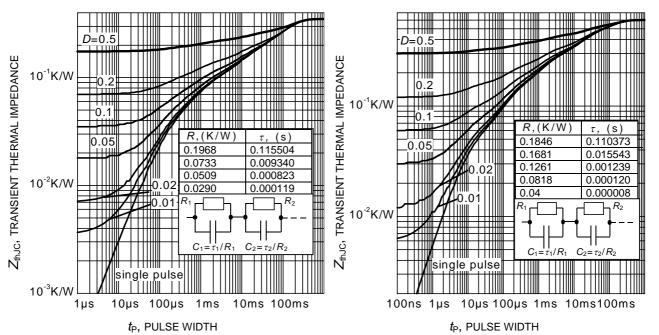


Figure 21. IGBT transient thermal impedance $(D = t_0 / T)$

Figure 22. Diode transient thermal impedance as a function of pulse width $(D=t_P/T)$

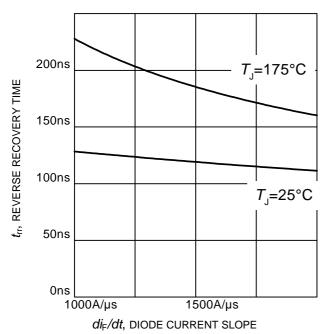
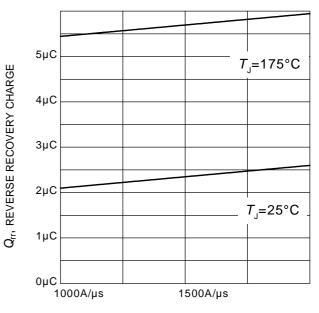


Figure 23. Typical reverse recovery time as a function of diode current slope $(V_R=400V, I_F=75A,$ Dynamic test circuit in Figure E)



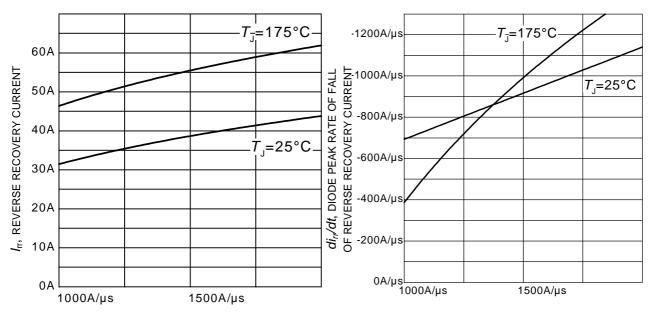
di_F/dt, DIODE CURRENT SLOPE

Figure 24. Typical reverse recovery charge as a function of diode current slope

 $(V_R = 400V, I_F = 75A,$ Dynamic test circuit in Figure E)







di_F/dt, DIODE CURRENT SLOPE

Figure 25. Typical reverse recovery current as a function of diode current slope

($V_R = 400V$, $I_F = 75A$, Dynamic test circuit in Figure E) di_F/dt, DIODE CURRENT SLOPE

Figure 26. Typical diode peak rate of fall of reverse recovery current as a function of diode current slope (V_R =400V, I_F =75A, Dynamic test circuit in Figure E)

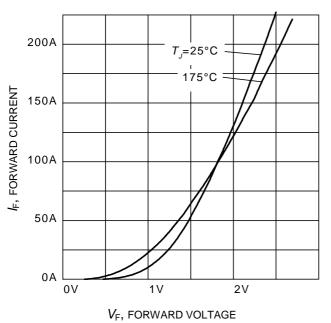
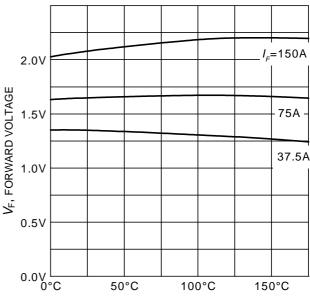


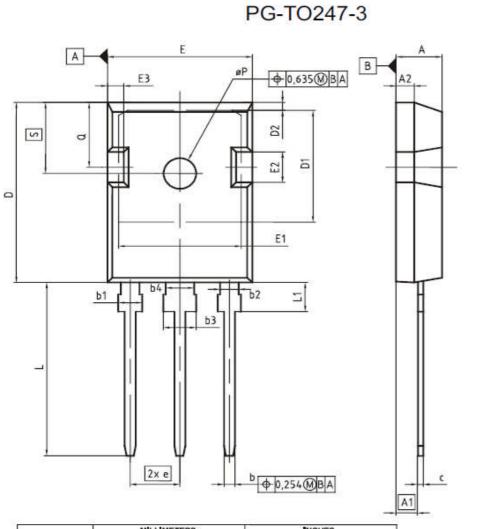
Figure 27. Typical diode forward current as a function of forward voltage



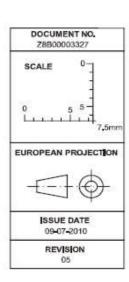
 $T_{\rm J}$, JUNCTION TEMPERATURE

Figure 28. Typical diode forward voltage as a function of junction temperature





DEM.	MILLIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
A	4.83	5,21	0.190	0,205
A1	2.27	2,54	0.089	0,100
A2	1.85	2,16	0.073	0,085
ь	1.07	1,33	0,042	0,052
b1	1,90	2.41	0,075	0,095
b2	1.90	2.16	0,075	0,085
b3	2,87	3.38	0.113	0.133
b4	2,87	3.13	0,113	0.123
c	0,55	0.68	0,022	0,027
D	20,80	21,10	0,819	0,831
D1	16,25	17,65	0,640	0,695
D2	0.95	1.35	0.037	0.053
E	15.70	16.13	0.618	0,635
E1	13.10	14.15	0,516	0,557
E2	3,68	5.10	0.145	0,201
E3	1.00	2,60	0.039	0.102
e	5.	44 (BSC)	0.2	214 (BSC)
N		3		3
L.	19,80	20,32	0,780	0.800
L1	4.10	4.47	0.161	0.176
øΡ	3,50	3.70	0.138	0.146
Q	5.49	6,00	0,216	0,236
s	6,04	6,30	0,238	0,248







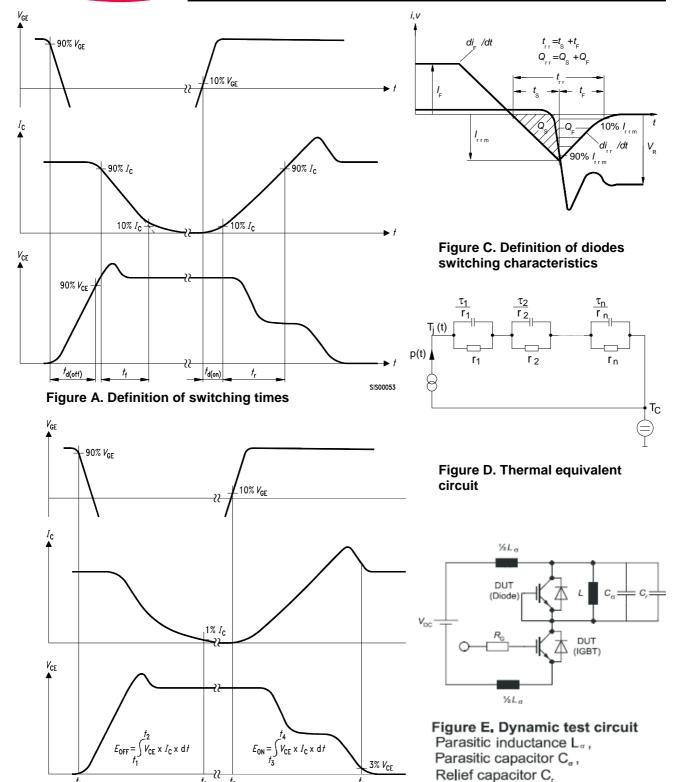


Figure B. Definition of switching losses

(only for ZVT switching)





Published by Infineon Technologies AG 81726 Munich, Germany © 2013 Infineon Technologies AG All Rights Reserved.

Legal Disclaimer

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. With respect to any examples or hints given herein, any typical values stated herein and/or any information regarding the application of the device, Infineon Technologies hereby disclaims any and all warranties and liabilities of any kind, including without limitation, warranties of non-infringement of intellectual property rights of any third party.

Information

For further information on technology, delivery terms and conditions and prices, please contact the nearest Infineon Technologies Office (www.infineon.com).

Warnings

Due to technical requirements, components may contain dangerous substances. For information on the types in question, please contact the nearest Infineon Technologies Office.

The Infineon Technologies component described in this Data Sheet may be used in life-support devices or systems and/or automotive, aviation and aerospace applications or systems only with the express written approval of Infineon Technologies, if a failure of such components can reasonably be expected to cause the failure of that life-support, automotive, aviation and aerospace device or system or to affect the safety or effectiveness of that device or system. Life support devices or systems are intended to be implanted in the human body or to support and/or maintain and sustain and/or protect human life. If they fail, it is reasonable to assume that the health of the user or other persons may be endangered.