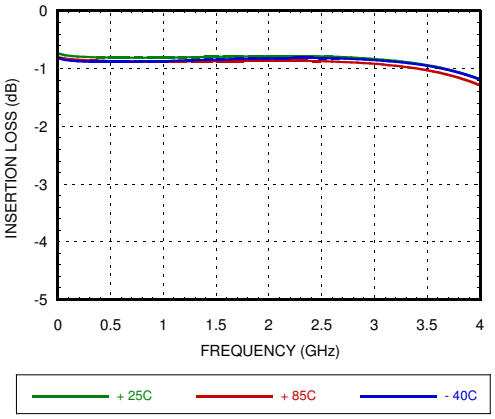
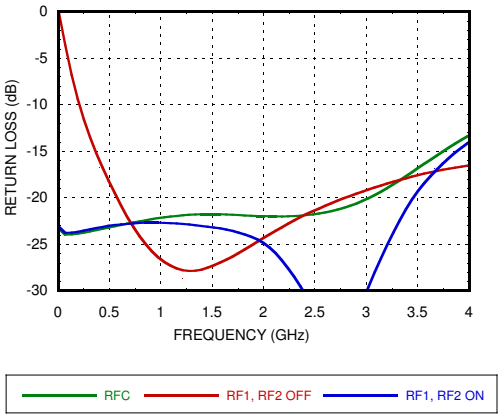
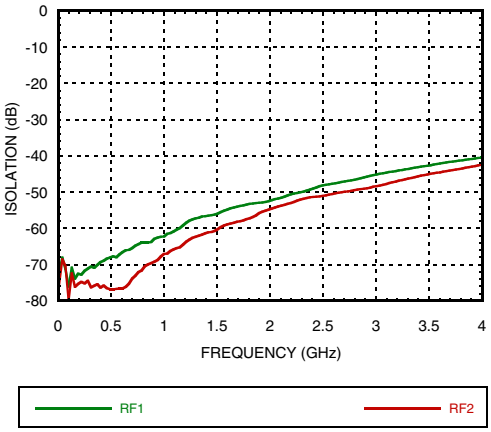
Insertion Loss



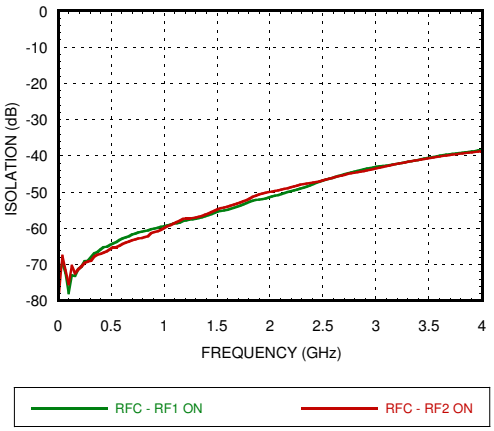
Return Loss



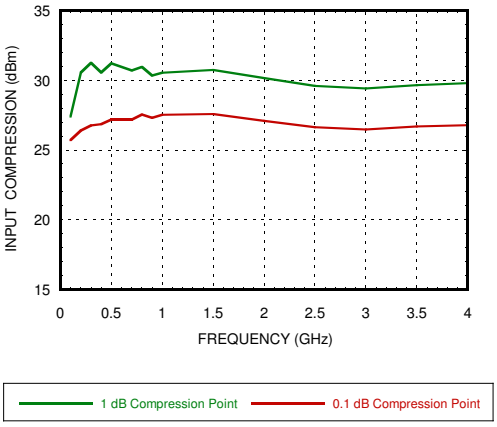
Isolation Between Ports RFC and RF1 / RF2



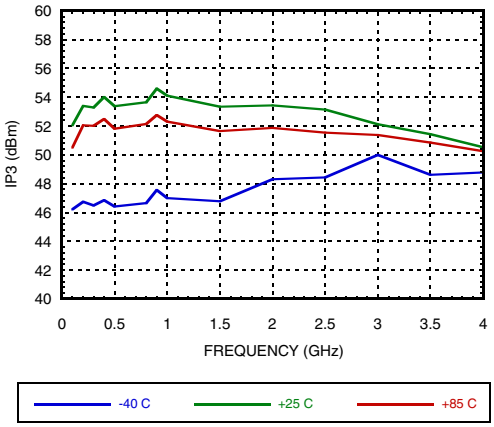
Isolation Between Ports RF1 and RF2



0.1 and 1 dB Input Compression Point



Input Third Order Intercept Point



RoHS✓ EARTH FRIENDLY

SPDT NON-REFLECTIVE SWITCH, DC - 4 GHz

Absolute Maximum Ratings

Control Voltage Range	-0.5 to +7.5 Vdc
RF Input Power Vctl = 0/+5V	+31 dBm
RF1, RF2 Termination	+26 dBm
Junction Temperature	150 °C
Insertion Loss Path - (channel to ground) Continuous Pdiss (T = 85 °C) (derate 13 mW/°C above 85 °C) Thermal Resistance	 0.86 W 75 °C/W
Termination Path - (channel to ground) Continuous Pdiss (T = 85 °C) (derate 6.5 mW/°C above 85 °C) Thermal Resistance	 0.42 W 153 °C/W
Storage Temperature	-65 to +150 °C
Operating Temperature	-40 to +85 °C
ESD Sensitivity (HBM)	Class 1A

Control Voltages

*Control Input Tolerances are ± 0.2 Vdc

State	Bias Condition*
Low	0 Vdc @ 5 µA Typical
High	+5.0 Vdc @ 5 µA Typical

Truth Table

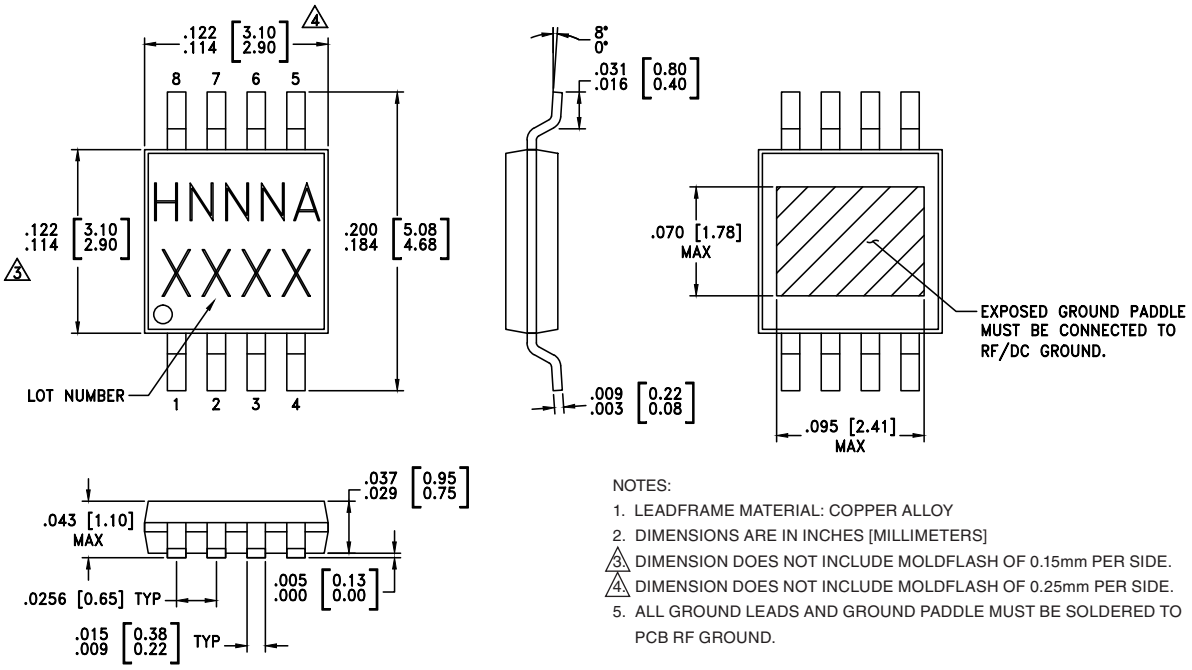
Control Input		Signal Path State
A	B	RFC to:
Low	High	RF1
High	Low	RF2

DC blocks are required at ports RFC, RF1, RF2.

Do not operate continuously at RF power input greater than 1 dB compression and do not "***Hot Switch***" power levels greater than +24 dBm (control = 0/+5 Vdc).

ELECTROSTATIC SENSITIVE DEVICE
OBSERVE HANDLING PRECAUTIONS

Outline Drawing



NOTES:

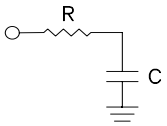
1. LEADFRAME MATERIAL: COPPER ALLOY
2. DIMENSIONS ARE IN INCHES [MILLIMETERS]
3. DIMENSION DOES NOT INCLUDE MOLDFLASH OF 0.15mm PER SIDE.
4. DIMENSION DOES NOT INCLUDE MOLDFLASH OF 0.25mm PER SIDE.
5. ALL GROUND LEADS AND GROUND PADDLE MUST BE SOLDERED TO PCB RF GROUND.

Package Information

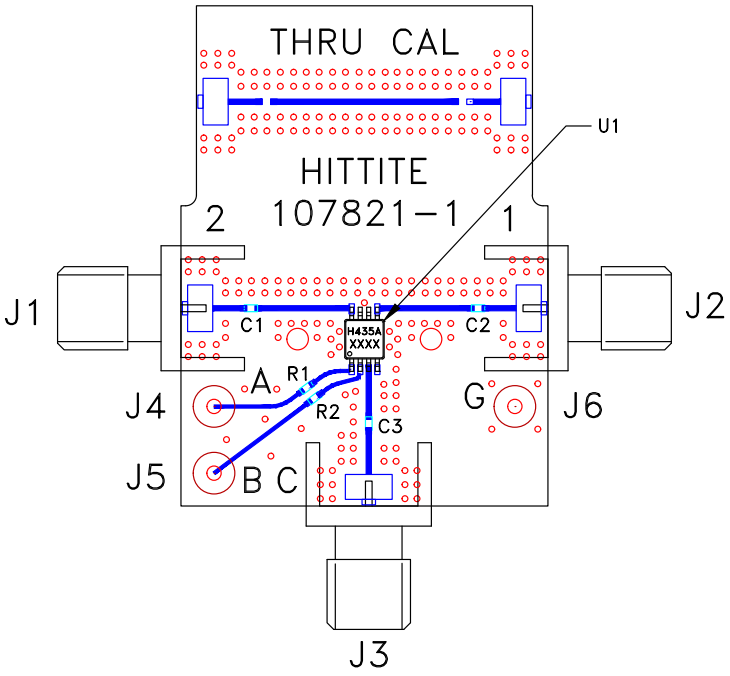
Part Number	Package Body Material	Lead Finish	MSL Rating	Package Marking [3]
HMC435AMS8G	Low Stress Injection Molded Plastic	Sn/Pb Solder	MSL3 [1]	H435A XXXX
HMC435AMS8GE	RoHS-compliant Low Stress Injection Molded Plastic	100% matte Sn	MSL3 [2]	H435A XXXX

[1] Max peak reflow temperature of 235 °C
[2] Max peak reflow temperature of 260 °C
[3] 4-Digit lot number XXXX

Pin Descriptions

Pin Number	Function	Description	Interface Schematic
1	A	See truth and control voltage tables.	
2	B	See truth and control voltage tables.	
3, 5, 8	RFC, RF1, RF2	These pins are DC coupled and matched to 50 Ohms. Blocking capacitors are required.	
4	N/C	This pin is not connected internally; however, all data shown herein was measured with this pin connected to RF/DC ground externally.	
6, 7	GND	Package bottom has exposed metal paddle that must be connected to PCB RF ground as well.	

Evaluation PCB



List of Materials for Evaluation PCB EVAL 105143-HMC435AMS8G[1]

Item	Description
J1 - J3	PCB Mount SMA RF Connector
J4 - J6	DC Pin
C1 - C3	100 pF Capacitor, 0402 Pkg.
R1 - R2	100 Ohm Resistor, 0402 Pkg.
U1	HMC435AMS8G(E) SPDT Switch
PCB [2]	107821 Evaluation PCB

[1] Reference this number when ordering complete evaluation PCB

[2] Circuit Board Material: Rogers 4350

The circuit board used in the application should be generated with proper RF circuit design techniques. Signal lines at the RF port should have 50 Ohm impedance and the package ground leads and backside ground slug should be connected directly to the ground plane similar to that shown above. The evaluation circuit board shown above is available from Analog Devices, upon request.