

December 2014

# FCP190N60 / FCPF190N60 N-Channel SuperFET<sup>®</sup> II MOSFET

**600 V, 20.2 A, 199 m**Ω

#### **Features**

- 650 V @ T<sub>J</sub> = 150°C
- Typ.  $R_{DS(on)}$  = 170 m $\Omega$
- Ultra Low Gate Charge (Typ. Q<sub>q</sub> = 57 nC)
- Low Effective Output Capacitance (Typ. C<sub>oss(eff.)</sub> = 160 pF)
- · 100% Avalanche Tested
- · RoHS Compliant

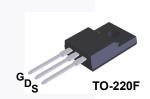
## **Applications**

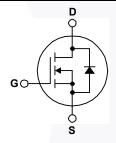
- · LCD / LED / PDP TV Lighting
- · Solar Inverter
- · AC-DC Power Supply

## **Description**

SuperFET® II MOSFET is Fairchild Semiconductor's brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This technology is tailored to minimize conduction loss, provide superior switching performance, dv/dt rate and higher avalanche energy. Consequently, SuperFET II MOSFET is very suitable for the switching power applications such as PFC, server/telecom power, FPD TV power, ATX power and industrial power applications.







## Absolute Maximum Ratings T<sub>C</sub> = 25°C unless otherwise noted.

Symbol	Parameter			FCP190N60	FCPF190N60	Unit	
$V_{DSS}$	Drain to Source Voltage			6	V		
V	Cata to Source Voltage	- DC		±	20	V	
$V_{GSS}$	Gate to Source Voltage	- AC	(f > 1 Hz)	±30		V	
	Drain Current	- Continuous (T <sub>C</sub> = 25°C)		20.2	20.2*	^	
ID	Drain Current	- Continuous (T <sub>C</sub> = 100°C)		12.7	12.7*	Α	
I <sub>DM</sub>	Drain Current	- Pulsed	(Note 1)	60.6	60.6*	Α	
E <sub>AS</sub>	Single Pulsed Avalanche Energy (Note 2)		400		mJ		
I <sub>AR</sub>	Avalanche Current		(Note 1)	4.0		Α	
E <sub>AR</sub>	Repetitive Avalanche Energy (Note 1)		(Note 1)	2.1		mJ	
dv/dt	MOSFET dv/dt			100		1//20	
uv/ut	Peak Diode Recovery dv/dt		(Note 3)	20		V/ns	
В	Dower Discipation	(T <sub>C</sub> = 25°C)		208	39	W	
$P_{D}$	Power Dissipation - Derate Above 25°C			1.67	0.31	W/oC	
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range		-55 to +150		οС		
TL	Maximum Lead Temperature	for Soldering, 1/8" from Case for	5 Seconds	3	00	οС	

<sup>\*</sup>Drain current limited by maximum junction temperature.

#### **Thermal Characteristics**

Symbol	Parameter	FCP190N60	FCPF190N60	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	0.6	3.2	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	62.5	-0/00

©2012 Fairchild Semiconductor Corporation FCP190N60 / FCPF190N60 Rev. C18

## **Package Marking and Ordering Information**

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FCP190N60	FCP190N60	TO-220	Tube	N/A	N/A	50 units
FCPF190N60	FCPF190N60	TO-220F	Tube	N/A	N/A	50 units

**Test Conditions** 

Min.

Тур.

Max.

Unit

## **Electrical Characteristics** $T_C = 25^{\circ}C$ unless otherwise noted. Parameter

Off Chara	acteristics					
D\/	Drain to Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 10 \text{ mA}, T_J = 25^{\circ}\text{C}$	600	-	-	V
BV <sub>DSS</sub>	Drain to Source Breakdown voltage	$V_{GS} = 0 \text{ V}, I_D = 10 \text{ mA}, T_J = 150^{\circ}\text{C}$	650	-	-	V
ΔBV <sub>DSS</sub> / ΔT <sub>J</sub>	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> = 10 mA, Referenced to 25°C	-	0.67	-	V/°C
BV <sub>DS</sub>	Drain to Source Avalanche Breakdown Voltage	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 20 A	-	700	-	V
1	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 600 V, V <sub>GS</sub> = 0 V	-	-	1	
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{DS} = 480 \text{ V}, T_{C} = 125^{\circ}\text{C}$	-	1.3	-	μΑ
I <sub>GSS</sub>	Gate to Body Leakage Current	V <sub>GS</sub> = ±20 V, V <sub>DS</sub> = 0 V	-	-	±100	nA

#### On Characteristics

Symbol

V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 250 \mu A$	2.5	-	3.5	V
R <sub>DS(on)</sub>	Static Drain to Source On Resistance	$V_{GS} = 10 \text{ V}, I_D = 10 \text{ A}$	-	0.17	0.199	Ω
9 <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> = 20 V, I <sub>D</sub> = 10 A	-	21	-	S

## **Dynamic Characteristics**

C <sub>iss</sub>	Input Capacitance	V - 25 V V - 0 V	- 1	2220	2950	pF
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V f = 1 MHz	-	1630	2165	pF
C <sub>rss</sub>	Reverse Transfer Capacitance	1 - 1 1/11/12	-	85	128	pF
C <sub>oss</sub>	Output Capacitance	$V_{DS}$ = 380 V, $V_{GS}$ = 0 V, f = 1 MHz	-	42	-	pF
C <sub>oss(eff.)</sub>	Effective Output Capacitance	$V_{DS} = 0 \text{ V to } 480 \text{ V}, V_{GS} = 0 \text{ V}$	-	160	-	pF
Q <sub>g(tot)</sub>	Total Gate Charge at 10V	V <sub>DS</sub> = 380 V, I <sub>D</sub> = 10 A,	-	57	74	nC
Q <sub>gs</sub>	Gate to Source Gate Charge	V <sub>GS</sub> = 10 V	- /	9	-	nC
Q <sub>gd</sub>	Gate to Drain "Miller" Charge	(Note 4)	- /	21	-	nC
ESR	Equivalent Series Resistance	f = 1 MHz	-/-	1	- ,	Ω

#### **Switching Characteristics**

t <sub>d(on)</sub>	Turn-On Delay Time		-	20	50	ns
t <sub>r</sub>		$V_{DD} = 380 \text{ V}, I_D = 10 \text{ A},$	-	10	30	ns
t <sub>d(off)</sub>	Turn-Off Delay Time	$V_{GS}$ = 10 V, $R_{G}$ = 4.7 $\Omega$	-	64	138	ns
t <sub>f</sub>	Turn-Off Fall Time	(Note 4)	-	5	20	ns

#### **Drain-Source Diode Characteristics**

$I_S$	Maximum Continuous Drain to Source Diode Forward Current		-	-	20.2	Α
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	60.6	Α
$V_{SD}$	Drain to Source Diode Forward Voltage V <sub>GS</sub> = 0 V, I <sub>SD</sub> = 10 A		-	-	1.2	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>GS</sub> = 0 V, I <sub>SD</sub> = 10 A,	-	320	-	ns
Q <sub>rr</sub>	Reverse Recovery Charge	$dI_F/dt = 100 A/\mu s$	-	5.1	-	μС

- 1. Repetitive rating: pulse-width limited by maximum junction temperature.
- 2. I<sub>AS</sub> = 4 A, V<sub>DD</sub> = 50 V, R<sub>G</sub> = 25  $\Omega$ , starting T<sub>J</sub> = 25°C.
- 3.  $I_{SD} \le 10$  A, di/dt  $\le 200$  A/ $\mu$ s,  $V_{DD} \le BV_{DSS}$ , starting  $T_J$  = 25°C.
- 4. Essentially independent of operating temperature typical characteristics.

©2012 Fairchild Semiconductor Corporation FCP190N60 / FCPF190N60 Rev. C18

## **Typical Performance Characteristics**

Figure 1. On-Region Characteristics

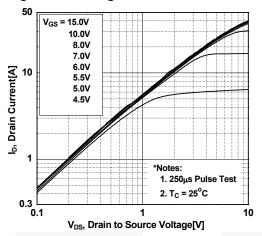


Figure 3. On-Resistance Variation vs.

Drain Current and Gate Voltage

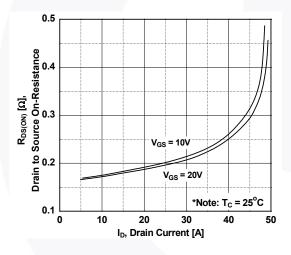


Figure 5. Capacitance Characteristics

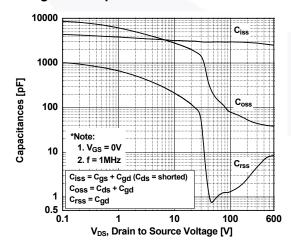


Figure 2. Transfer Characteristics

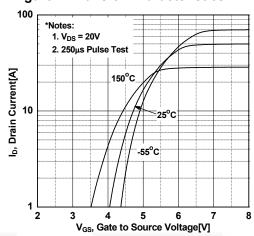
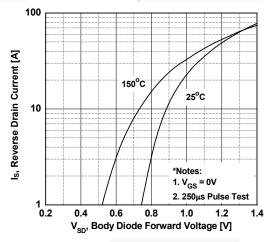
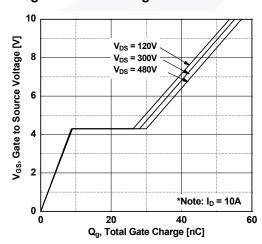


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature



**Figure 6. Gate Charge Characteristics** 



#### **Typical Performance Characteristics** (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

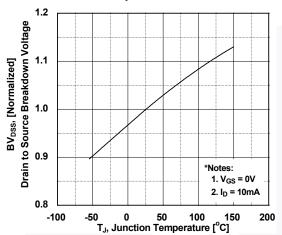


Figure 9. Maximum Safe Operating Area for FCP190N60

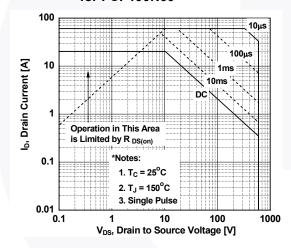


Figure 11. Maximum Drain Current vs. Case Temperature

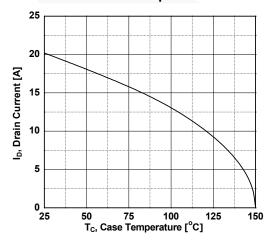


Figure 8. On-Resistance Variation vs. Temperature

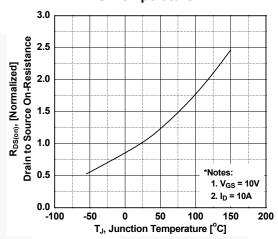


Figure 10. Maximum Safe Operating Area for FCPF190N60

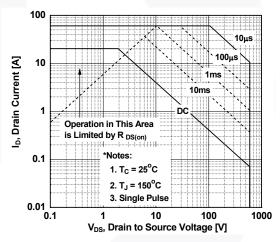
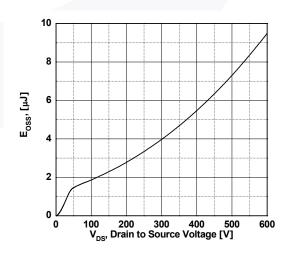


Figure 12. Eoss vs. Drain to Source Voltage



## **Typical Performance Characteristics** (Continued)

Figure 13. Transient Thermal Response Curve for FCP190N60

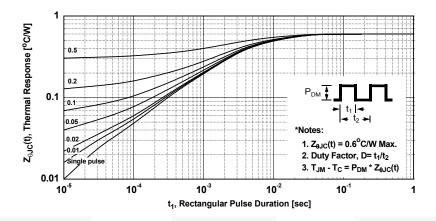
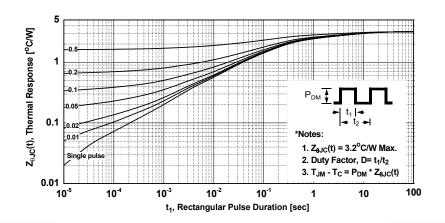


Figure 14. Transient Thermal Response Curve for FCPF190N60



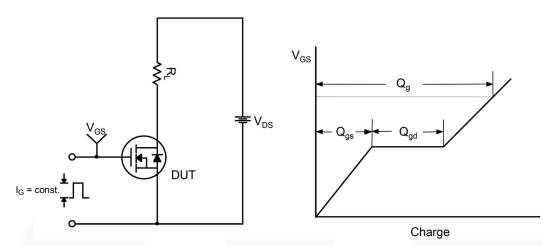


Figure 15. Gate Charge Test Circuit & Waveform

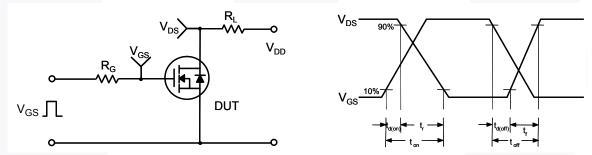


Figure 16. Resistive Switching Test Circuit & Waveforms

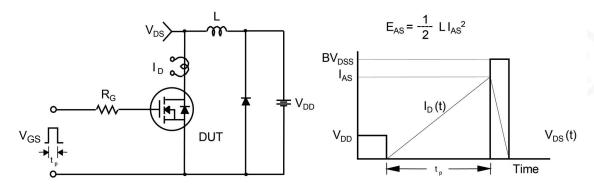


Figure 17. Unclamped Inductive Switching Test Circuit & Waveforms

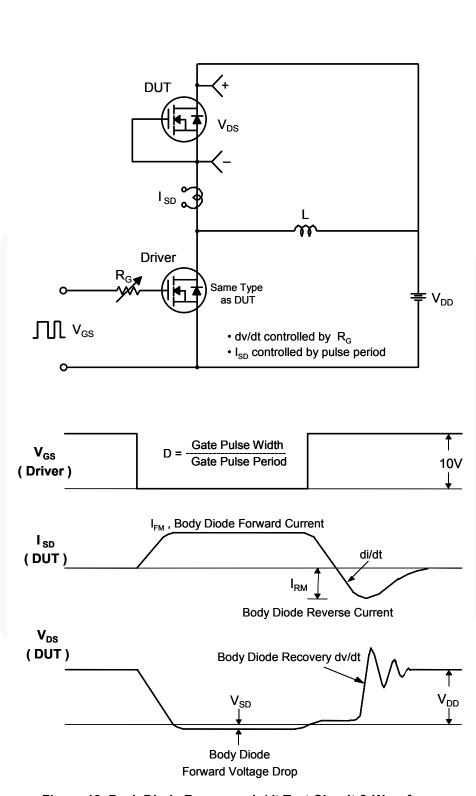
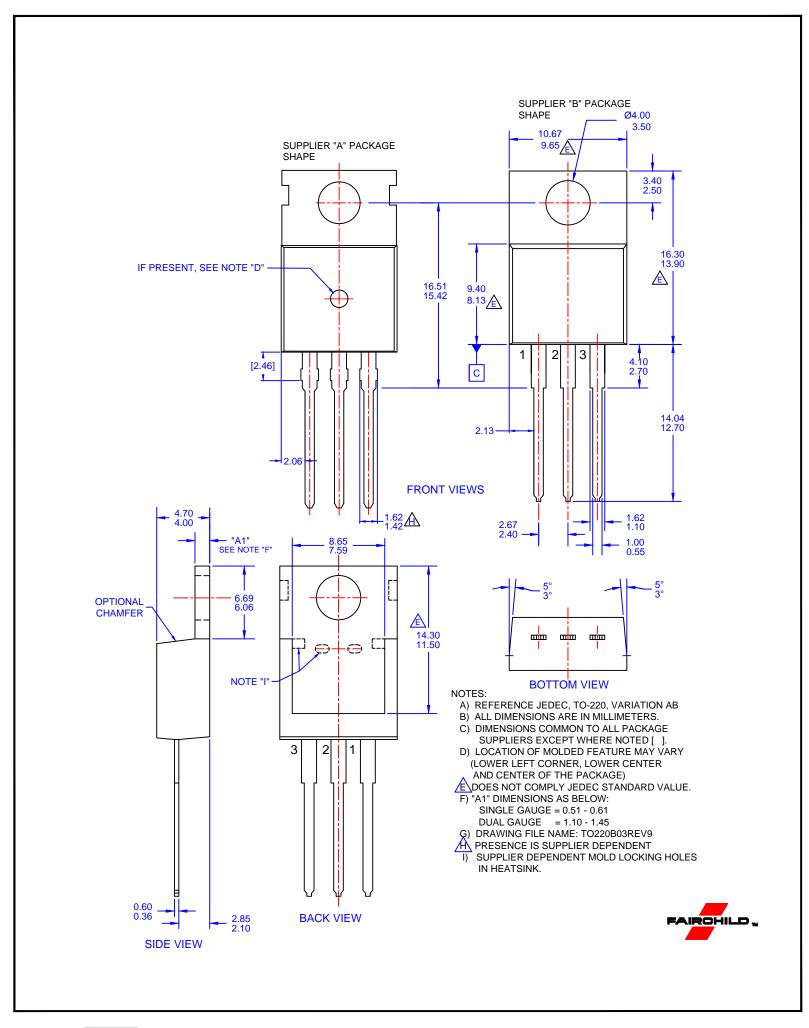
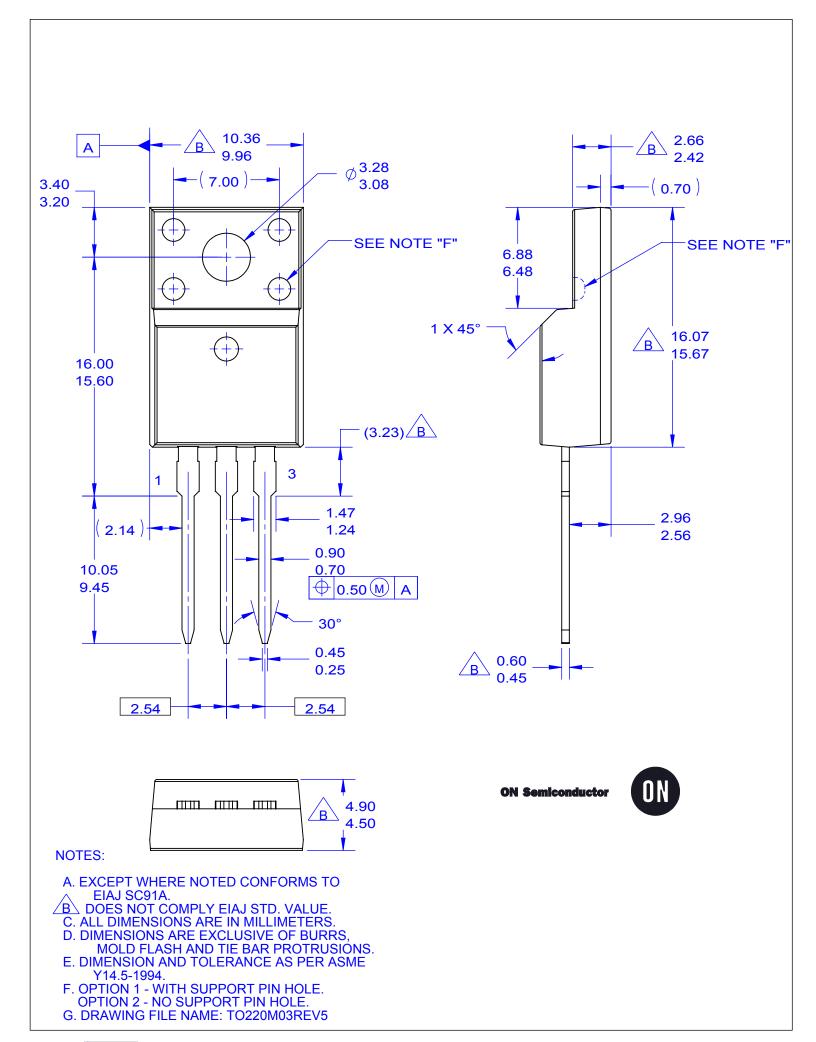


Figure 18. Peak Diode Recovery dv/dt Test Circuit & Waveforms





ON Semiconductor and III) are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at <a href="www.onsemi.com/site/pdf/Patent-Marking.pdf">www.onsemi.com/site/pdf/Patent-Marking.pdf</a>. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages.

Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that ON Semiconductor was negligent regarding the design or manufacture of the part. ON Semiconductor is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

#### **PUBLICATION ORDERING INFORMATION**

#### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor 19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free Europe, Middle East and Africa Technical Support:

Phone: 421 33 790 2910 Japan Customer Focus Center Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative

© Semiconductor Components Industries, LLC

www.onsemi.com