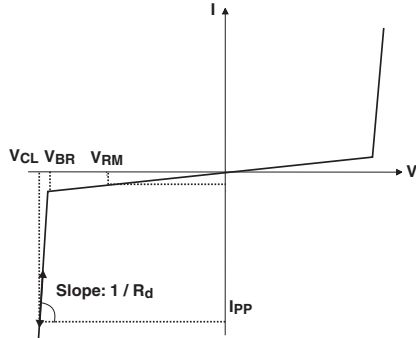


# 1 Characteristics

**Table 1. Absolute ratings (limiting values)**

Symbol	Parameter	Value	Unit
$V_{PP}$	MIL STD 883G-Method 3015-7	$\pm 25$	kV
	ESD discharge IEC 61000-4-2 air discharge	$\pm 15$	
	IEC 61000-4-2 contact discharge	$\pm 8$	
$P_{PP}$	Peak pulse power (8/20 $\mu$ s)	50	W
$T_j$	Junction temperature	125	$^{\circ}\text{C}$
$T_{stg}$	Storage temperature range	-55 to +150	$^{\circ}\text{C}$
$T_L$	Lead solder temperature (10 seconds duration)	260	$^{\circ}\text{C}$
$T_{op}$	Operating temperature range	-40 to +125	$^{\circ}\text{C}$

**Table 2. Electrical characteristics ( $T_{amb} = 25^{\circ}\text{C}$ )**

Symbol	Parameter							
V <sub>BR</sub>	Breakdown voltage							
I <sub>RM</sub>	Leakage current @ V <sub>RM</sub>							
V <sub>RM</sub>	Stand-off voltage							
V <sub>CL</sub>	Clamping voltage							
R <sub>d</sub>	Dynamic impedance							
I <sub>PP</sub>	Peak pulse current							
C	Capacitance							
Order code	V <sub>BR</sub> @ I <sub>R</sub>			I <sub>RM</sub> @ V <sub>RM</sub>		R <sub>d</sub>	αT	C
	min.	max.		max.		typ. <sup>(1)</sup>	max. <sup>(2)</sup>	max. 0 V bias
	V	V	mA	μA	V	W	10 <sup>-4</sup> /°C	pF
ESDA14V2-4BF3	14.2	18	1	0.5 0.1	12 3	3.2	10	15

1. Square pulse,  $I_{PP} = 3\text{ A}$ ,  $t_p = 2.5\text{ }\mu\text{s}$ .

2.  $\Delta V_{BR} = \alpha T * (T_{amb} - 25^{\circ}\text{C}) * V_{BR}(25^{\circ}\text{C})$

Figure 3. Clamping voltage versus peak pulse current ( $T_j$  initial = 25 °C) (rectangular waveform,  $t_p = 2.5 \mu s$ )

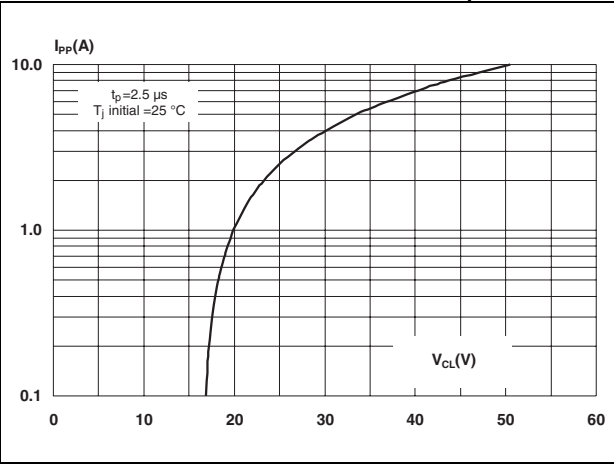


Figure 4. Junction capacitance versus reverse applied voltage (typical values)

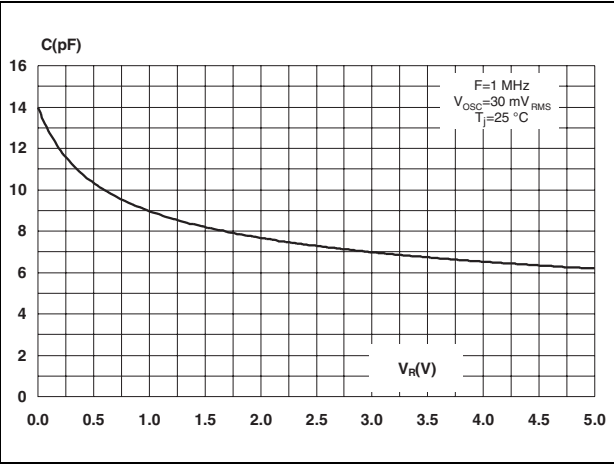


Figure 5. Relative variation of leakage current versus junction temperature (typical values)

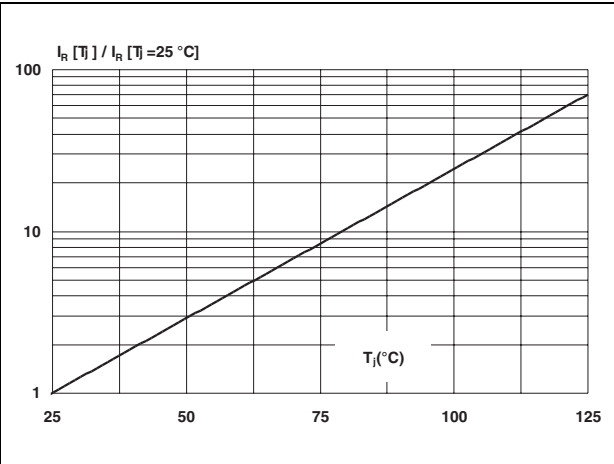


Figure 6. ESD response to IEC 61000-4-2 (+15 kV air discharge)

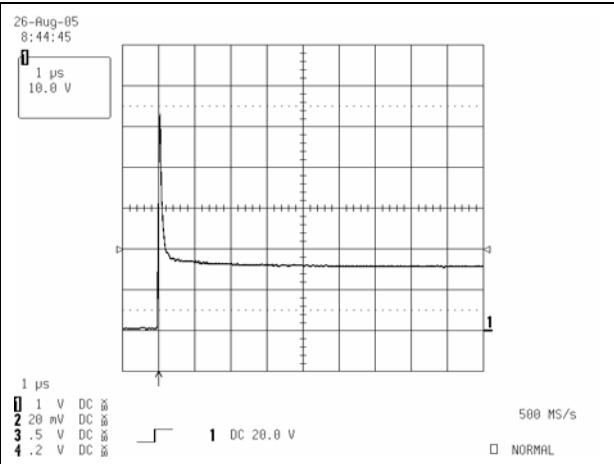


Figure 7. ESD response to IEC 61000-4-2 (-15 kV air discharge)

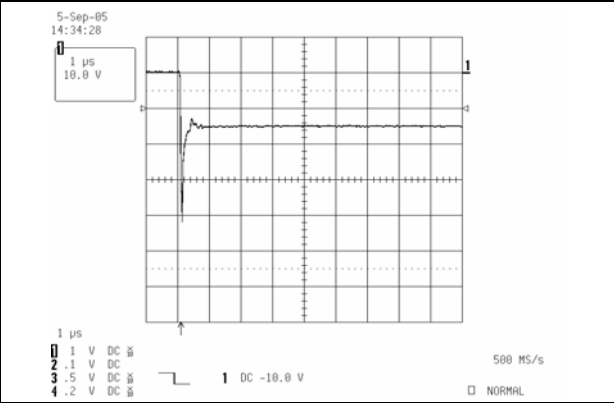


Figure 8. Analog crosstalk measurements

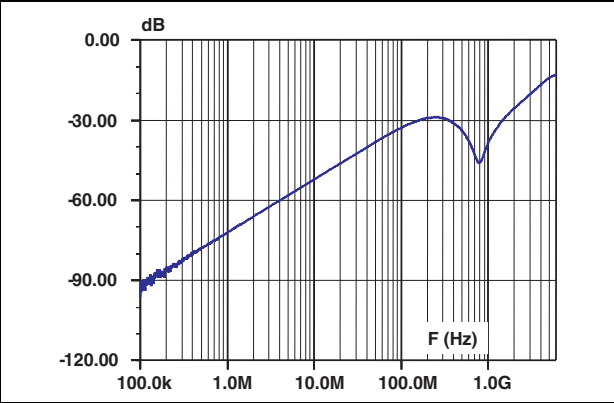
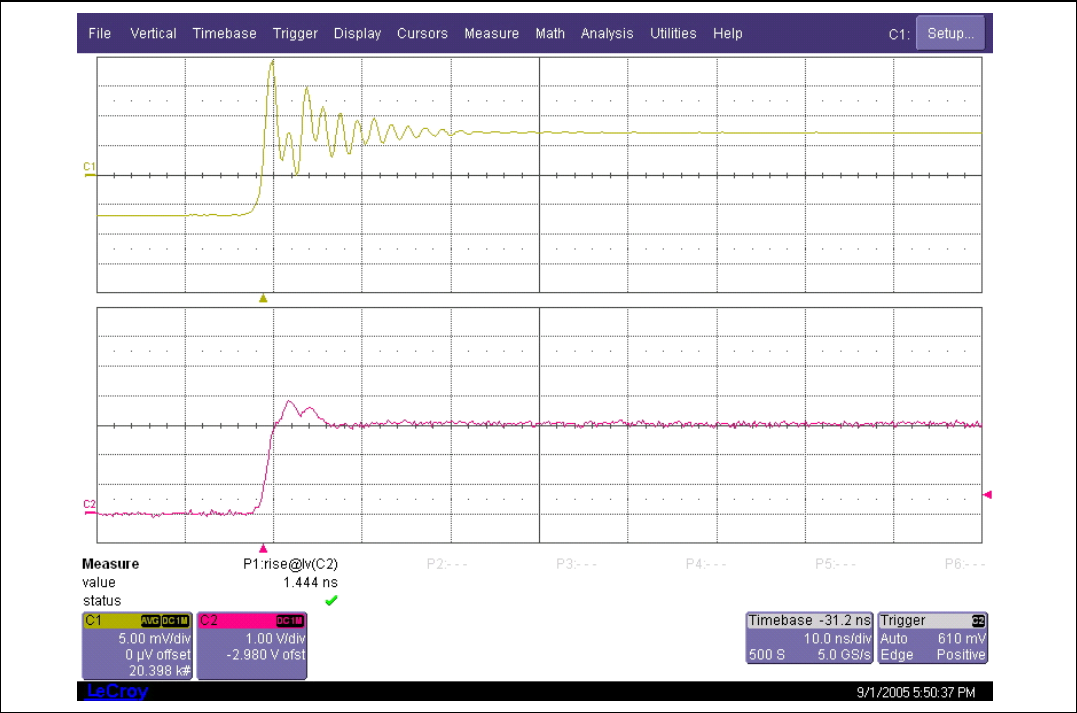
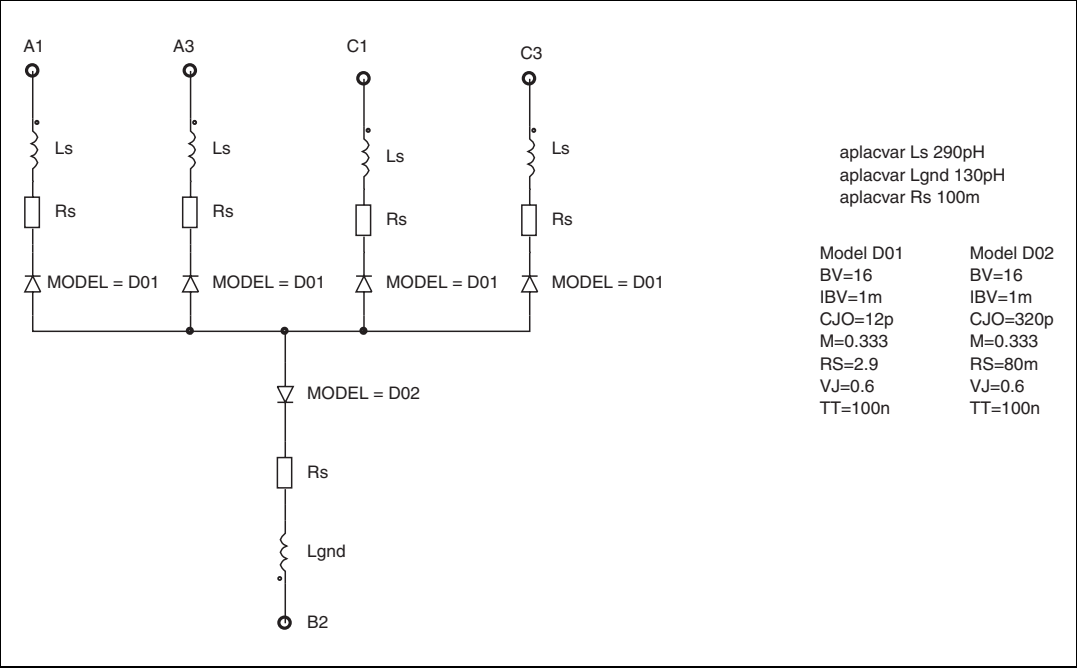


Figure 9. Digital crosstalk measurements



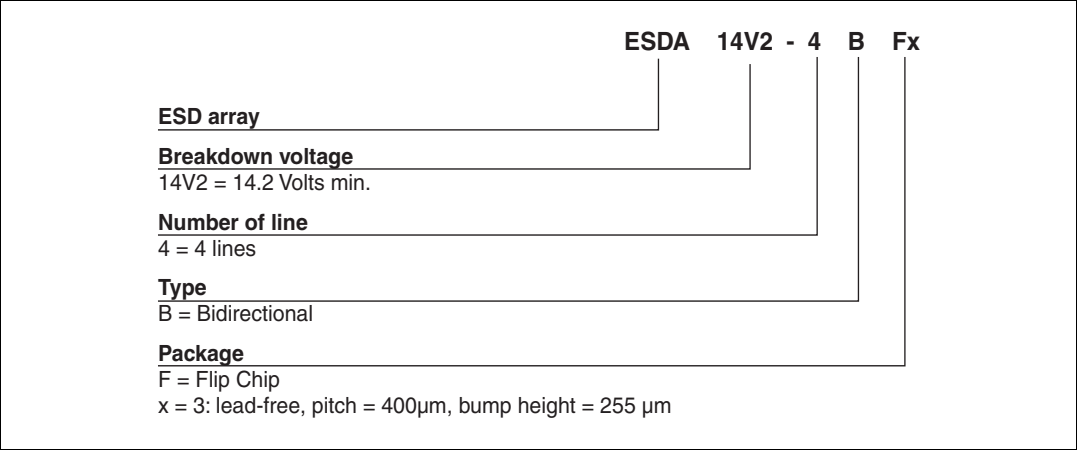
## 2 Application information

Figure 10. Aplac model



3 Ordering information scheme

Figure 11. Ordering information scheme



4 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: [www.st.com](http://www.st.com). ECOPACK® is an ST trademark.

Figure 12. Package dimensions

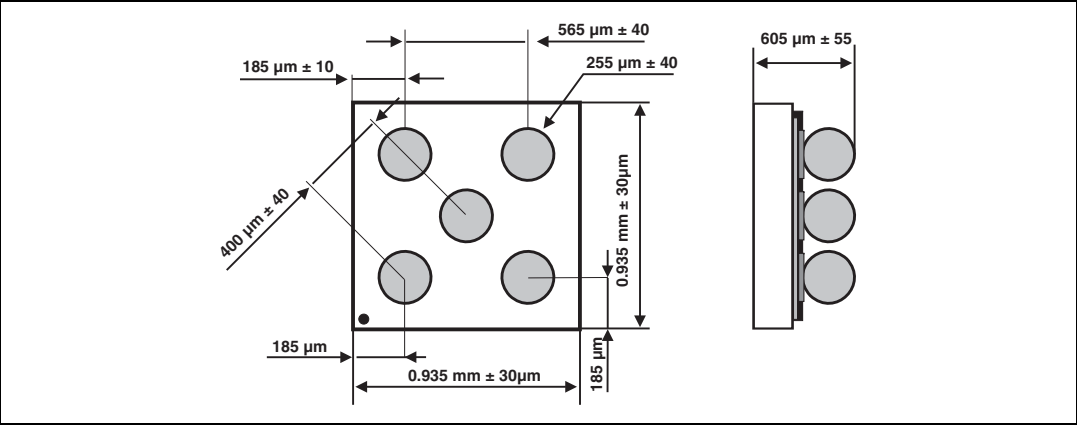


Figure 13. Footprint

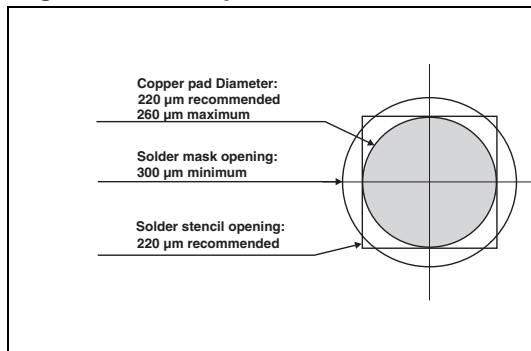


Figure 14. Marking

Dot  
xx = marking  
z = manufacturing location  
yww = datecode  
(y = year  
ww = week)

■ ECOPACK grade

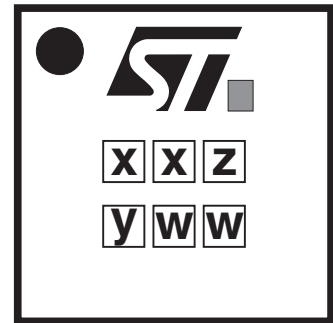
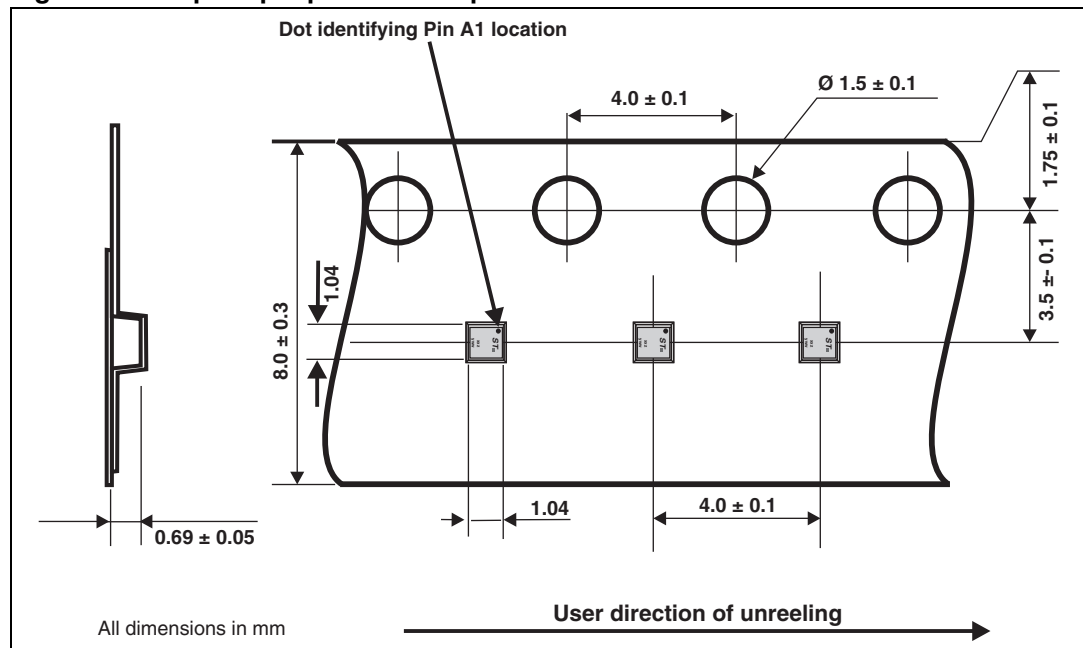


Figure 15. Flip Chip tape and reel specifications



Note:

More information is available in the application notes:

AN2348: "400  $\mu\text{m}$  Flip Chip: Package description and recommendations for use"

AN1751: EMI Filters: Recommendations and measurements

## 5 Ordering information

Table 3. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
ESDA14V2-4BF3	EF	Flip Chip	1.10 mg	5000	Tape and reel 7"

## 6 Revision history

Table 4. Document revision history

Date	Revision	Changes
19-Sep-2005	1	Initial release.
15-Dec-2005	2	Dimension from center bump to corner bump changed in Figure 9 to indicate diagonal instead of perpendicular measurement. No values changed. ECOPACK statement added. Updated ordering information.
18-Apr-2008	3	Updated ECOPACK statement. Updated <a href="#">Figure 11</a> , <a href="#">Figure 12</a> and <a href="#">Figure 15</a> . Reformatted to current standards.
28-Jan-2010	4	Added ST logo and ECOPACK grade to package and marking illustrations.

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