

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage		V _{DSS}	-20	V	
Gate-Source Voltage		V _{GSS}	±12	V	
Drain Current (Note 6) Continuous	T _A = +25°C T _A = +70°C	ID	-4.6 -3.7	A	
Pulsed Drain Current (Note 7)		I _{DM}	-18	A	
Body-Diode Continuous Current (Note 6)		ls	2.0	A	

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 6)	PD	1.25	W
Thermal Resistance, Junction to Ambient (Note 6); Steady-State	R _{θJA}	100	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

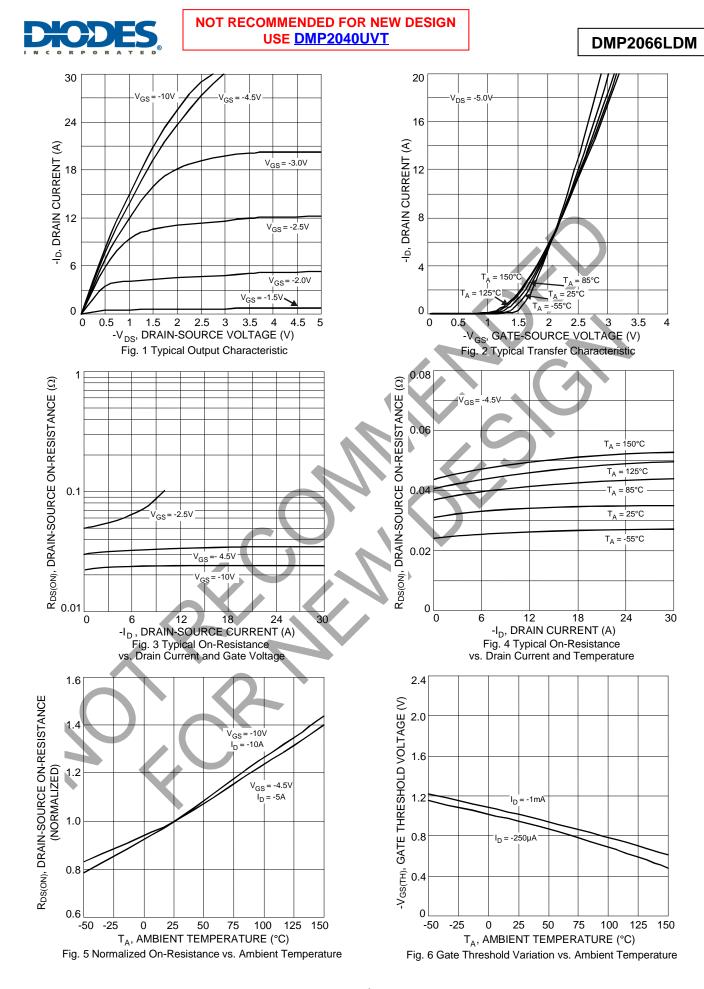
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Cymphol	Min	Tim	Max	Unit	Test Condition	
STATIC PARAMETERS	Symbol	MIN	Тур	Wax	Unit	Test Condition	
		00			N		
Drain-Source Breakdown Voltage	BV _{DSS}	-20			V	$I_{D} = -250 \mu A, V_{GS} = 0 V$	
Zero Gate Voltage Drain Current $T_J = +25^{\circ}C$	IDSS			-1	μΑ	$V_{DS} = -20V, V_{GS} = 0V$	
Gate-Body Leakage Current	lgss			±100	nA	$V_{DS} = 0V, V_{GS} = \pm 12V$	
Gate Threshold Voltage	V _{GS(TH)}	-0.6	-0.96	-1.2	V	$V_{DS} = V_{GS}, I_D = -250 \mu A$	
On State Drain Current (Note 8)	I _{D(ON)}	-15		—	Α	$V_{GS} = -4.5V, V_{DS} = -5V$	
Statia Drain Source On Begisteneg (Note 9)			29	40		V _{GS} = -4.5V, I _D = -4.6A	
Static Drain-Source On-Resistance (Note 8)	R _{DS(ON)}		55	70	mΩ	V _{GS} = -2.5V, I _D = -3.8A	
Forward Transconductance (Note 8)	g _{FS}	. –	9	_	S	$V_{DS} = -10V, I_D = -4.6A$	
Diode Forward Voltage (Note 8)	V _{SD}	-0.5	-0.72	-1.4	V	$I_{S} = -2.1A, V_{GS} = 0V$	
Maximum Body-Diode Continuous Current (Note 6)	ls			-1.7	А	_	
DYNAMIC PARAMETERS (Note 9)							
Input Capacitance	Ciss	_	820	_	pF	$V_{DS} = -15V, V_{GS} = 0V$ f = 1.0MHz	
Output Capacitance	Coss	_	200		pF		
Reverse Transfer Capacitance	Crss	_	160	_	pF		
Gate Resistance	R _G	_	2.5		Ω	$V_{DS} = 0V, V_{GS} = 0V$ f = 1.0MHz	
SWITCHING CHARACTERISTICS							
Total Gate Charge	Q _G	_	10.1		nC	V _{DS} = -10V, V _{GS} = -4.5V, I _D = -4.5A	
Gate-Source Charge	Q _{GS}	_	1.5				
Gate-Drain Charge	Q _{GD}		4.3	_			
Turn-On Delay Time	t _{D(ON)}		4.4			$V_{DS} = -10V, V_{GS} = -4.5V,$ $I_D = -1A, R_G = 6.0\Omega$	
Rise Time	t _R	_	9.9		20		
Turn-Off Delay Time	t _{D(OFF)}	_	28.0	_	ns		
Fall Time	t _F	_	23.4				

6. Device mounted on 1"x1", FR-4 PC board with 2 oz. Copper and test pulse width t \leq 10s. 7. Repetitive Rating, pulse width limited by junction temperature. Notes:

8. Test pulse width $t = 300 \mu s$.

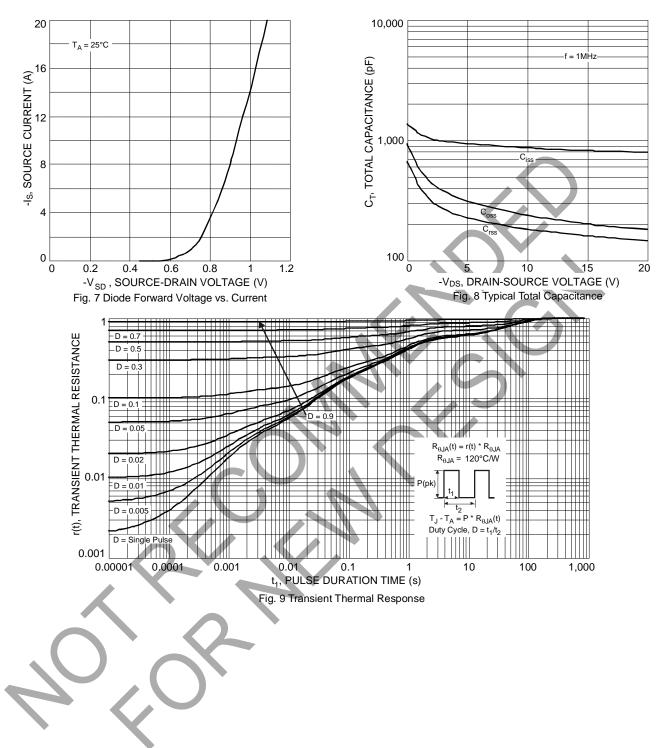
9. Guaranteed by design. Not subject to production testing.





NOT RECOMMENDED FOR NEW DESIGN USE <u>DMP2040UVT</u>

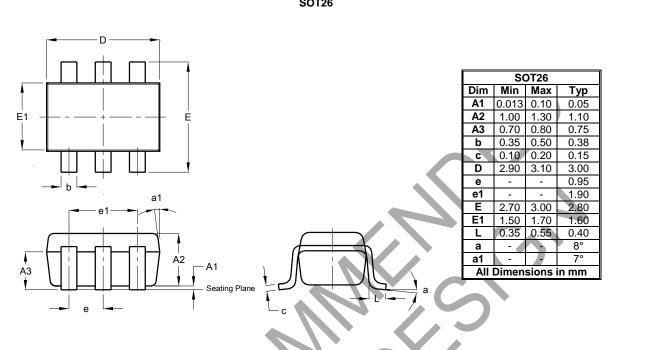
DMP2066LDM





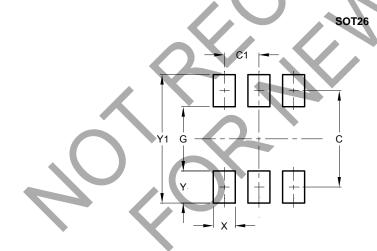
Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	2.40
C1	0.95
G	1.60
Х	0.55
Y	0.80
Y1	3.20



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