Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V_{DSS}	-12	V
Gate-Source Voltage			V_{GSS}	-6	V
Continuous Drain Current (Note 5) V_{GS} = -4.5V	Steady State	T_A = +25°C T_A = +70°C	I_D	-7.6 -6.0	A
Continuous Drain Current (Note 6) V_{GS} = -4.5V	Steady State	T_A = +25°C T_A = +70°C	I_D	-5.5 -4.3	A
Pulsed Drain Current (Pulse duration 10µs, duty cycle ≤1%)			I_{DM}	-60	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 5)	P_D	1.0	W
Total Power Dissipation (Note 6)	P_D	1.8	W
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	126.8	°C/W
Thermal Resistance, Junction to Ambient (Note 6)	$R_{\theta JA}$	69	°C/W
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV_{DSS}	-12	—	—	V	V_{GS} = 0V, I_D = -250µA
Zero Gate Voltage Drain Current @T_C = +25°C	I_{DSS}	—	—	-1	µA	V_{DS} = -9.6V, V_{GS} = 0V
Gate-Source Leakage	I_{GSS}	—	—	-100	nA	V_{GS} = -6.0V, V_{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(th)}$	-0.4	-0.8	-1.3	V	V_{DS} = V_{GS}, I_D = -250µA
Static Drain-Source On-Resistance	$R_{DS\ (ON)}$	—	12	18	mΩ	V_{GS} = -4.5V, I_D = -2A
			15	22		V_{GS} = -2.5V, I_D = -2A
Forward Transfer Admittance	$\|Y_{fs}\|$	—	5.5	—	S	V_{DS} = -6V, I_D = -2A
Diode Forward Voltage (Note 6)	V_{SD}	—	-0.7	-1	V	V_{GS} = 0V, I_S = -2A
Reverse Recovery Charge	Q_{rr}	—	30.2	—	nC	V_{dd} = -5V, I_F = -2A, di/dt = 200A/µs
Reverse Recovery Time	t_{rr}	—	71.4	—	ns	
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C_{iss}	—	457	—	pF	V_{DS} = -6V, V_{GS} = 0V, f = 1.0MHz
Output Capacitance	C_{oss}	—	272	—	pF	
Reverse Transfer Capacitance	C_{rss}	—	120	—	pF	
Series Gate Resistance	R_G	—	21.23	—	Ω	V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz
Total Gate Charge (4.5V)	Q_g	—	4.9	—	nC	V_{GS} = -4.5V, V_{DS} = -6V, I_D = -2A
Gate-Source Charge	Q_{gs}	—	0.6	—	nC	
Gate-Drain Charge	Q_{gd}	—	1.1	—	nC	
Turn-On Delay Time	$t_{D(on)}$	—	4.45	—	ns	V_{DD} = -6V, V_{GS} = -4.5V, I_{DS} = -2A, R_G = 2Ω,
Turn-On Rise Time	t_r	—	12	—	ns	
Turn-Off Delay Time	$t_{D(off)}$	—	100	—	ns	
Turn-Off Fall Time	t_f	—	93	—	ns	

Notes:
5. Device mounted on FR-4 PCB with minimum recommended pad layout.
6. Device mounted on FR4 material with 1-inch^2 (6.45-cm^2), 2-oz (0.071-mm thick) Cu.
7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to production testing.

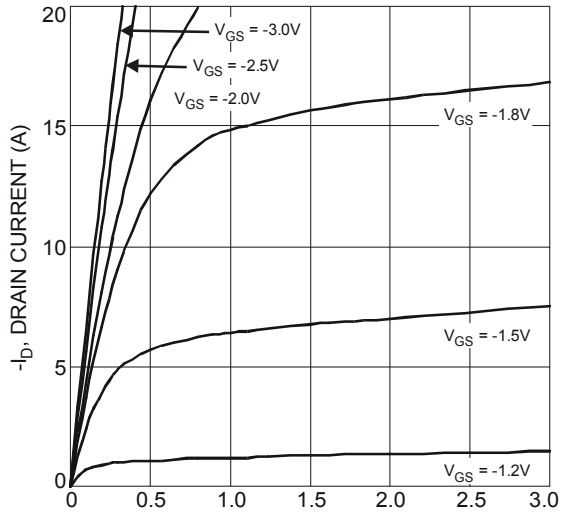


Figure 1 Typical Output Characteristics

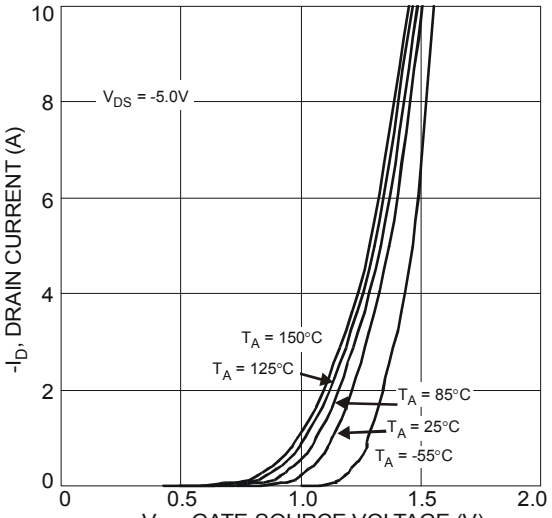


Figure 2 Typical Transfer Characteristics

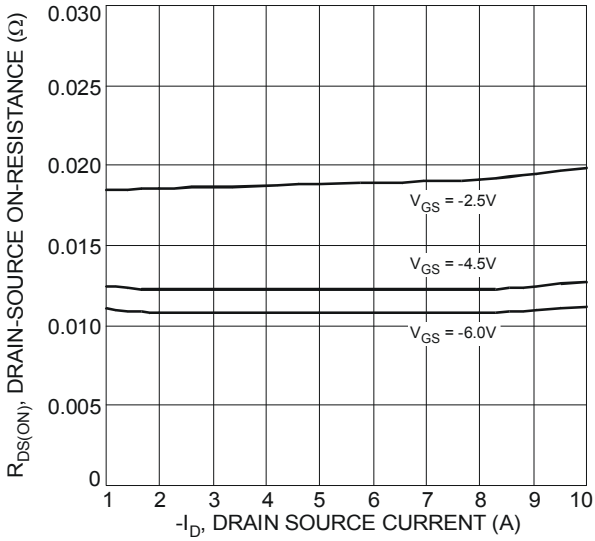


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

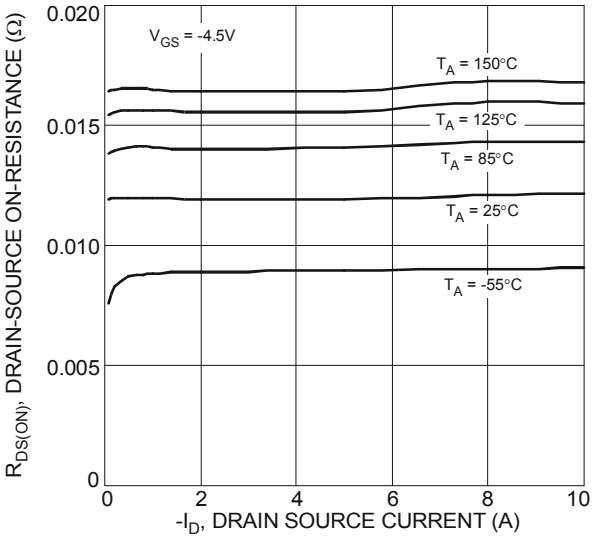


Figure 4 Typical On-Resistance vs. Drain Current and Temperature

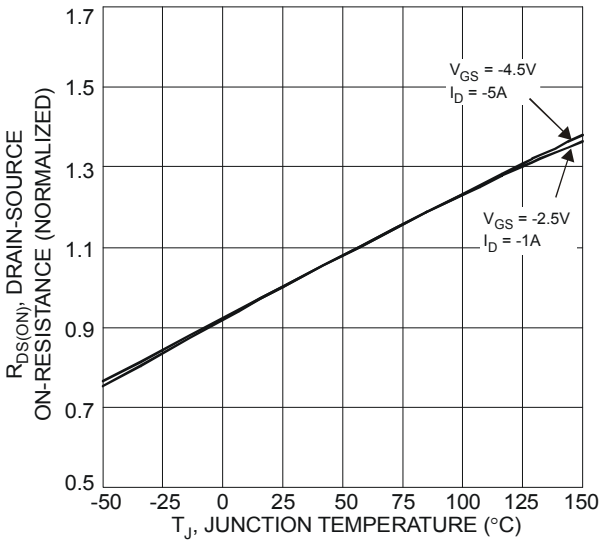


Figure 5 On-Resistance Variation with Temperature

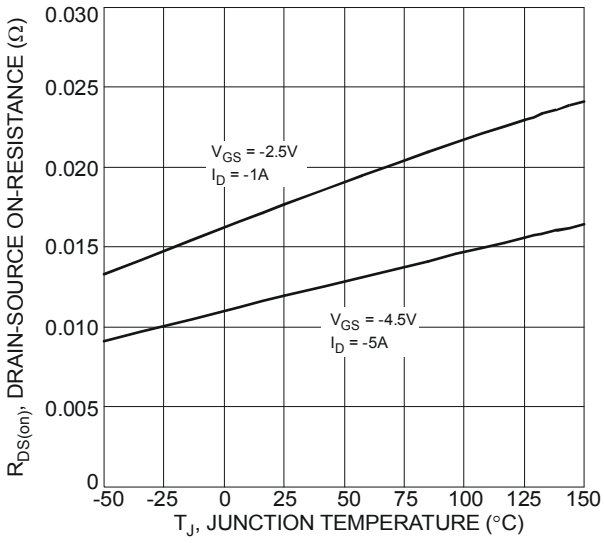


Figure 6 On-Resistance Variation with Temperature

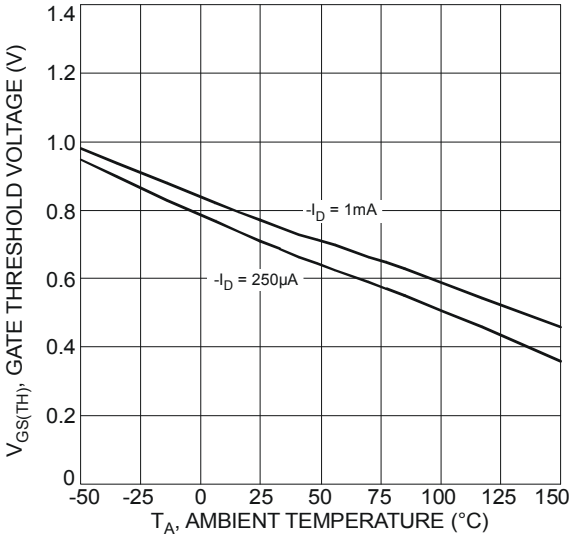


Figure 7 Gate Threshold Variation vs. Ambient Temperature

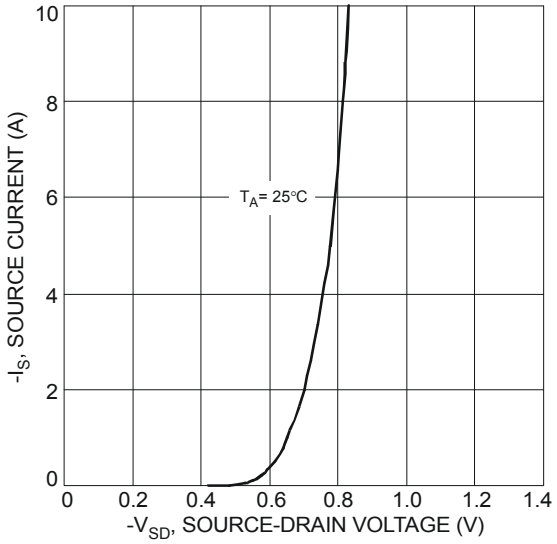


Figure 8 Diode Forward Voltage vs. Current

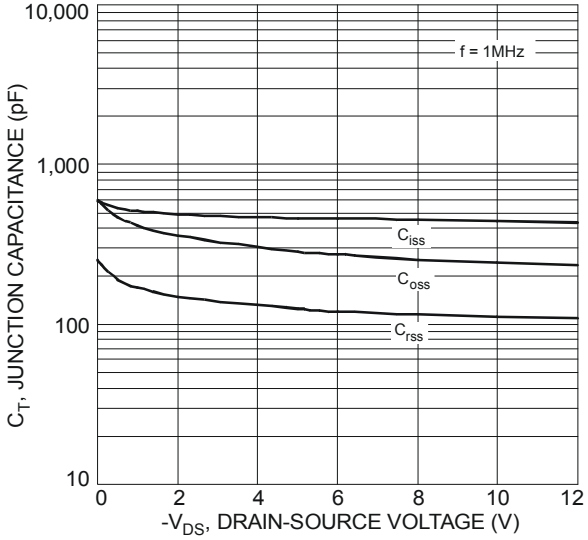


Figure 9 Typical Junction Capacitance

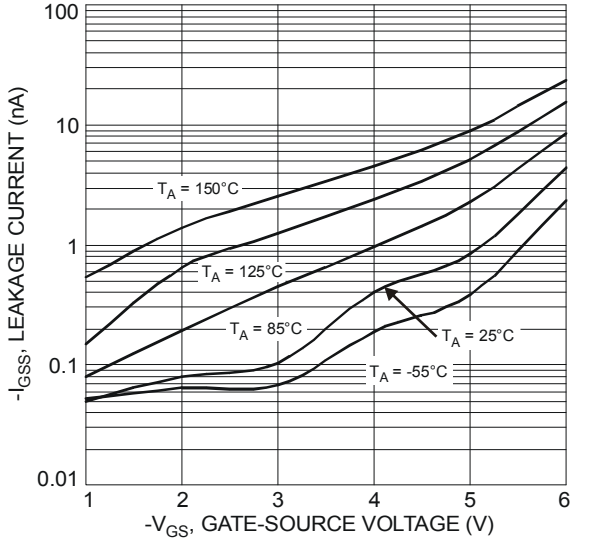


Figure 10 Typical Gate-Source Leakage Current vs. Voltage

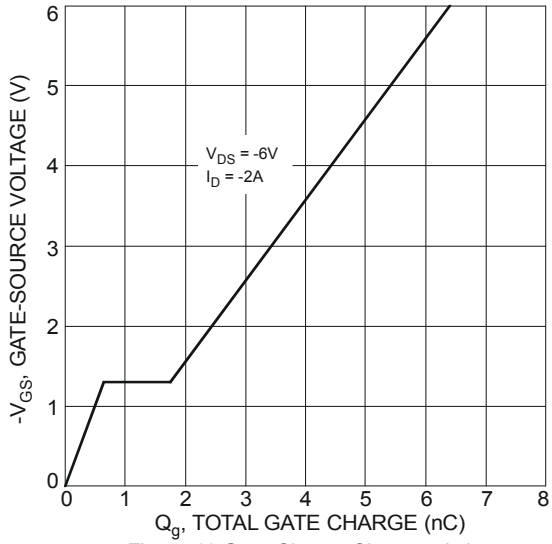


Figure 11 Gate-Charge Characteristics

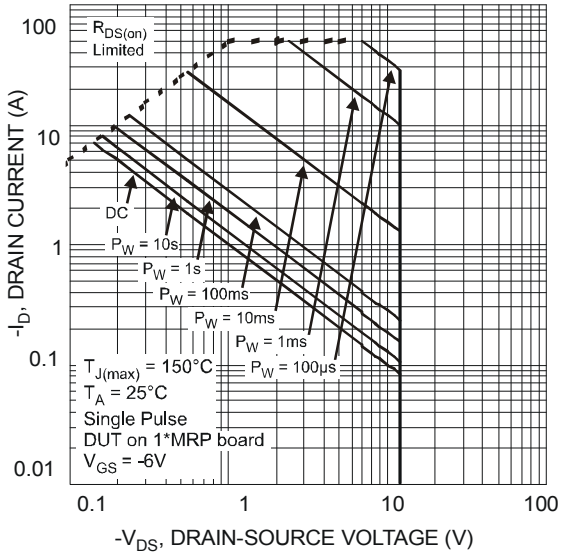


Figure 12 SOA, Safe Operation Area

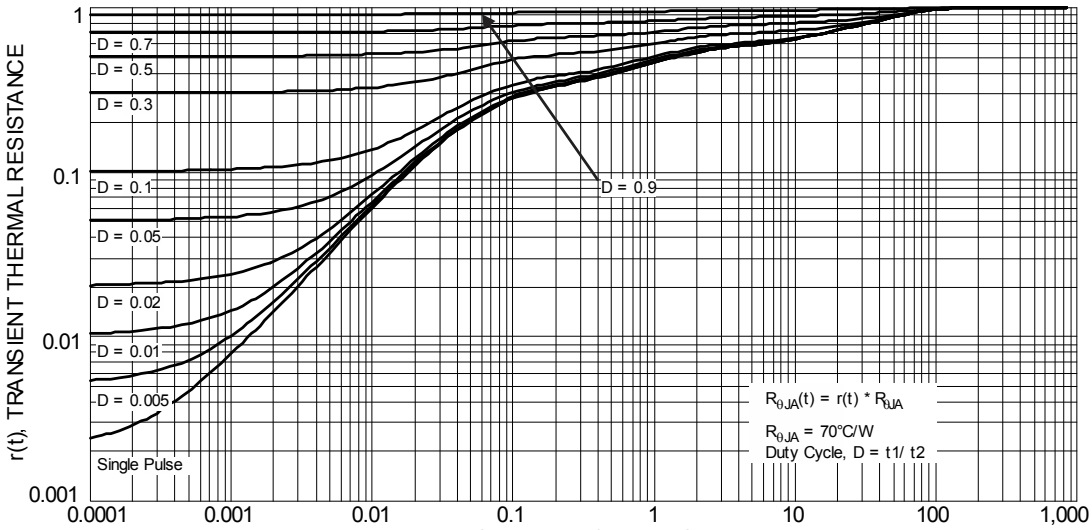
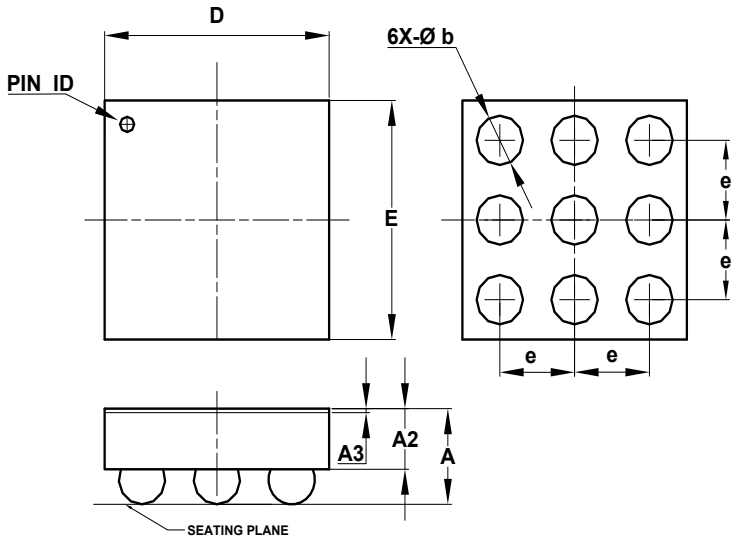


Figure 13 Transient Thermal Resistance

Package Outline Dimensions

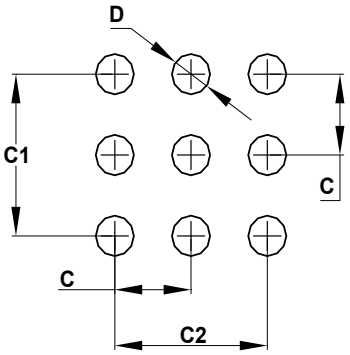
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



U-WLB1515-9			
Dim	**Min**	**Max**	**Typ**
A	-	0.62	-
A2	-	0.36	0.36
A3	0.020	0.030	0.025
b	0.27	0.37	0.32
D	1.47	1.51	1.49
E	1.47	1.51	1.49
e	-	-	0.50
All Dimensions in mm			

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
C	0.50
C1	1.00
C2	1.00
D	0.25

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