

## **Maximum Ratings** (@T<sub>C</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			$V_{DSS}$	100	V
Gate-Source Voltage			$V_{GSS}$	±20	V
Continuous Drain Current, V <sub>GS</sub> = 10V	Steady State	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	I <sub>D</sub>	40 25	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%) (Note 6)			I <sub>DM</sub>	54	Α
Maximum Continuous Body Diode Forward Current (Note 7)			Is	3.9	Α
Avalanche Current (Note 9) L=0.1mH			I <sub>AS</sub>	26	Α
Avalanche Energy (Note 9) L=0.1mH			E <sub>AS</sub>	35	mJ

## **Thermal Characteristics**

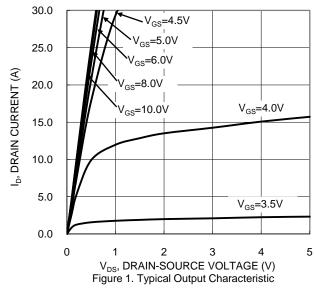
Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 6)		$P_D$	1.6	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady state	$R_{ heta JA}$	97	°C/W
Total Power Dissipation (Note 7)		$P_D$	2.9	W
Thermal Resistance, Junction to Ambient (Note 7)	Steady state	$R_{ heta JA}$	52	°C/W
Thermal Resistance, Junction to Case		$R_{\theta JC}$	1.8	C/VV
Operating and Storage Temperature Range		$T_{J_i}T_{STG}$	-55 to +175	°C

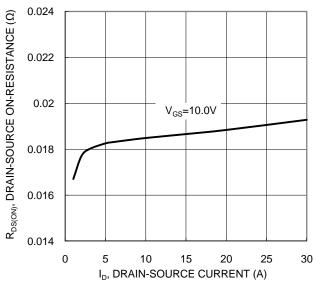
# **Electrical Characteristics** (@ $T_C = +25$ °C, unless otherwise specified.)

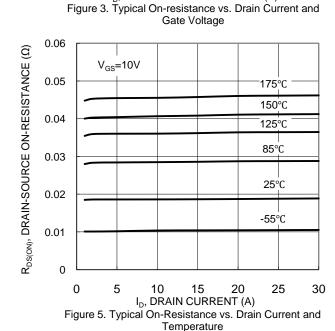
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	100	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	-	_	1.0	μΑ	$V_{DS} = 100V, V_{GS} = 0V$
Gate-Source Leakage	Igss	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)	ON CHARACTERISTICS (Note 8)					
Gate Threshold Voltage	V <sub>GS(TH)</sub>	2.0	2.5	4.0	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	1	19	28	mΩ	$V_{GS} = 10V, I_D = 20A$
Diode Forward Voltage	$V_{SD}$		0.7	1.2	V	$V_{GS} = 0V, I_{S} = 1.0A$
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	Ciss	_	2245	_		$V_{DS} = 50V, V_{GS} = 0V$ f = 1.0MHz
Output Capacitance	Coss	_	173	_	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>	_	68	_		
Gate Resistance	R <sub>G</sub>	_	1.9	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$
Total Gate Charge (V <sub>GS</sub> = 10V)	$Q_g$	_	36	_		V <sub>DD</sub> = 50V, I <sub>D</sub> = 20A
Total Gate Charge (V <sub>GS</sub> = 6.0V)	$Q_g$	_	22	_	nC	
Gate-Source Charge	$Q_{gs}$	_	7.3	_	IIC	
Gate-Drain Charge	$Q_{gd}$	_	9.2	_		
Turn-On Delay Time	t <sub>D(ON)</sub>	_	6.4	_		$V_{GS} = 10V, V_{DS} = 50V,$ $R_{G} = 3.0\Omega, I_{D} = 20A$
Turn-On Rise Time	t <sub>R</sub>	_	5.8	_		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	17.8	_	ns	
Turn-Off Fall Time	t <sub>F</sub>	_	4.8	_		
Reverse Recovery Time	t <sub>RR</sub>		35	_	ns	$I_F = 20A$ , $di/dt = 100A/\mu s$
Reverse Recovery Charge	Q <sub>RR</sub>	_	47	_	nC	$I_F = 20A$ , $di/dt = 100A/\mu s$

6. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
7. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
8. Short duration pulse test used to minimize self-heating effect.
9. Guaranteed by design. Not subject to product testing. Notes:



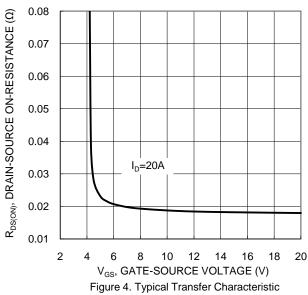






 $V_{DS}=5V$ 25 ID, DRAIN CURRENT (A) 20 15 85°C 150° 10 25°C 175 5 -55°C 0 2 3 3.5 5  $V_{GS}$ , GATE-SOURCE VOLTAGE (V) Figure 2. Typical Transfer Characteristic

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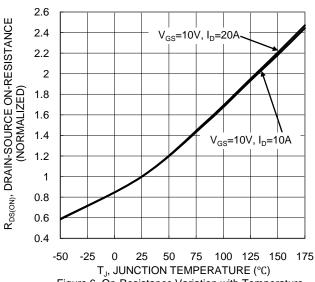


Figure 6. On-Resistance Variation with Temperature

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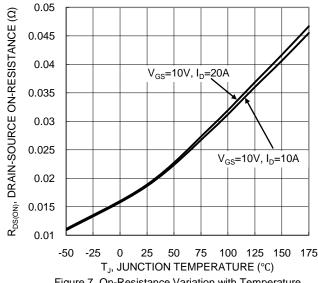
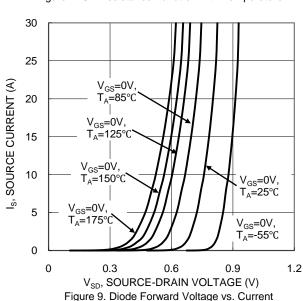
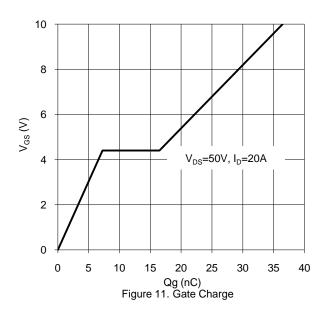


Figure 7. On-Resistance Variation with Temperature





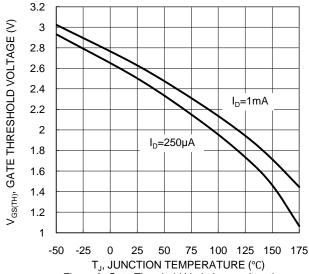
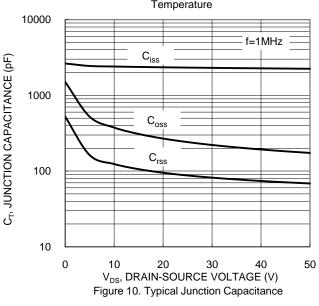
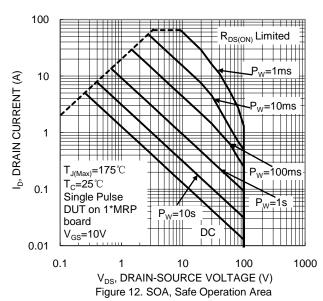


Figure 8. Gate Threshold Variation vs. Junction Temperature







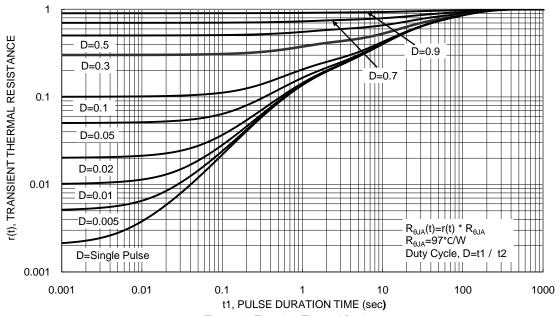


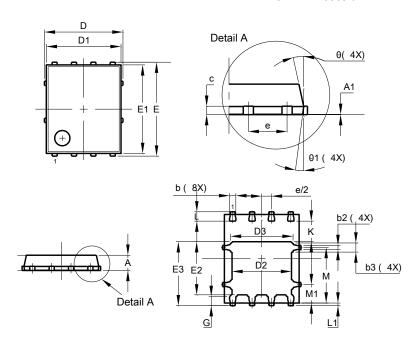
Figure 13. Transient Thermal Resistance



## **Package Outline Dimensions**

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

### POWERDI®5060-8

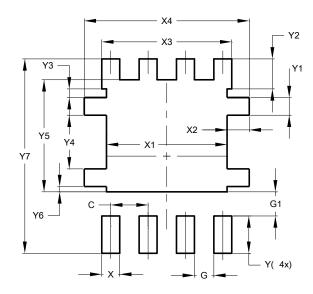


POWERDI®5060-8						
Dim	Min	Тур				
Α	0.90	1.10	1.00			
A1	0.00	0.05	_			
b	0.33	0.51	0.41			
b2	0.200	0.350	0.273			
b3	0.40	0.80	0.60			
C D	0.230	0.330	0.277			
	5.15 BSC					
D1	4.70	5.10	4.90			
D2	3.70	4.10	3.90			
D3	3.90 4.30 4.4		4.10			
Е	6.15 BSC					
E1	5.60	6.00	5.80			
E2	3.28	3.68	3.48			
E3	3.99	4.39	4.19			
е	1.27 BSC					
G	0.51	0.71	0.61			
K	0.51	-	_			
L	0.51	0.71	0.61			
L1	0.100	0.200	0.175			
M	3.235	4.035	3.635			
M1	1.00	1.40	1.21			
Θ	10°	12º	11º			
Θ1	6º	80	7º			
All Dimensions in mm						

# **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

### POWERDI®5060-8



Dimensions	Value (in mm)
С	1.270
G	0.660
G1	0.820
Х	0.610
X1	4.100
X2	0.755
Х3	4.420
X4	5.610
Y	1.270
Y1	0.600
Y2	1.020
Y3	0.295
Y4	1.825
Y5	3.810
Y6	0.180
Y7	6.610



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