

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	60	V
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current (Note 5) V _{GS} = 4V	Steady State	T _A = +25°C T _A = +70°C	I _D	407 325	mA
Pulsed Drain Current (Note 6)			I _{DM}	1	A

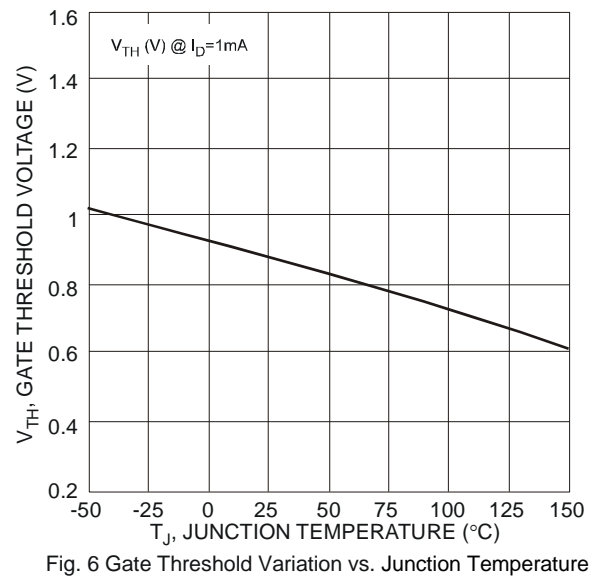
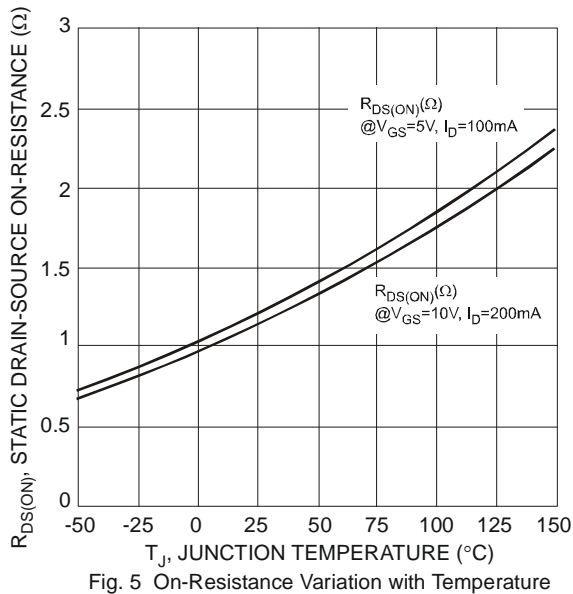
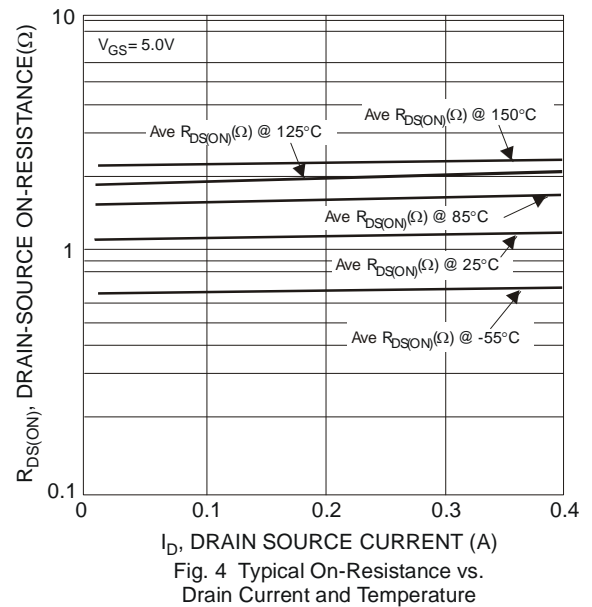
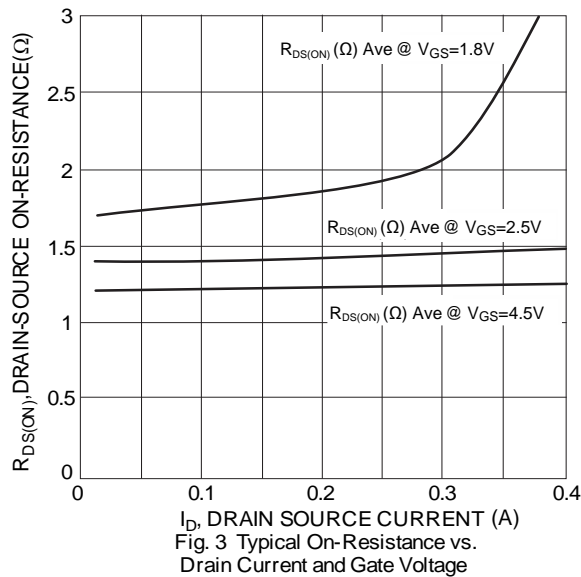
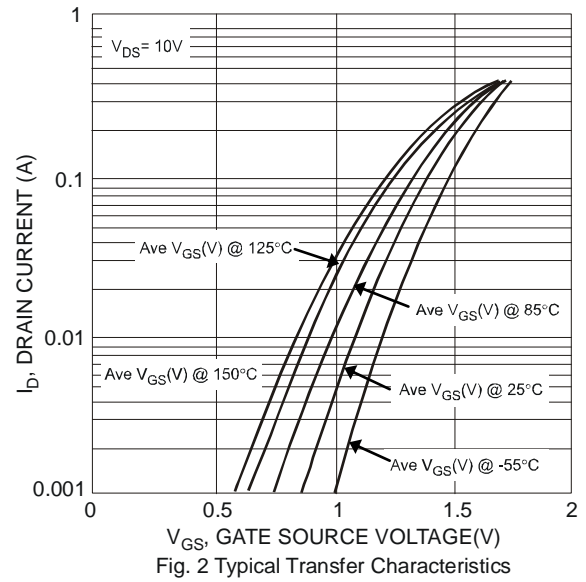
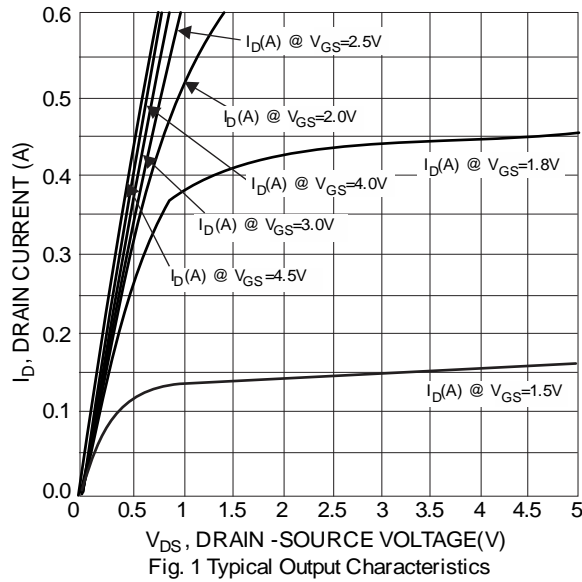
Thermal Characteristics

Characteristic	Symbol	Max	Unit
Power Dissipation (Note 5)	P _D	0.5	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 5)	R _{θJA}	251	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@ T_A = +25°C, unless otherwise stated.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	60	—	—	V	V _{GS} = 0V, I _D = 250μA
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	—	—	1.0	μA	V _{DS} = 60V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±5V, V _{DS} = 0V
		—	—	±500	nA	V _{GS} = ±10V, V _{DS} = 0V
		—	—	±2.0	μA	V _{GS} = ±15V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	0.6	—	1.0	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	1.3	2	Ω	V _{GS} = 4V, I _D = 100mA
		—	1.5	2.5		V _{GS} = 2.5V, I _D = 50mA
		—	1.9	3		V _{GS} = 1.8V, I _D = 50mA
Diode Forward Voltage	V _{SD}	—	0.9	1.3	V	V _{GS} = 0V, I _S = 115mA
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	—	32	64	pF	V _{DS} = 25V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	C _{oss}	—	4.4	9		
Reverse Transfer Capacitance	C _{rss}	—	2.9	6		
Gate Resistance	R _g	—	126	250	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge	Q _g	—	0.45	0.9	nC	V _{GS} = 4.5V, V _{DS} = 10V, I _D = 250mA
Gate-Source Charge	Q _{gs}	—	0.08	0.2		
Gate-Drain Charge	Q _{gd}	—	0.08	0.2		
Turn-On Delay Time	t _{D(ON)}	—	3.4	10	ns	V _{GS} = 10V, V _{DS} = 30V, R _L = 150Ω, R _G = 25Ω, I _D = 200mA
Turn-On Rise Time	t _R	—	3.4	10	ns	
Turn-Off Delay Time	t _{D(OFF)}	—	26.4	45	ns	
Turn-Off Fall Time	t _F	—	16.3	30	ns	

- Notes:
- Device mounted on FR-4 PCB with minimum recommended pad layout, single sided.
 - Repetitive rating, pulse width limited by junction temperature.
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to production testing.



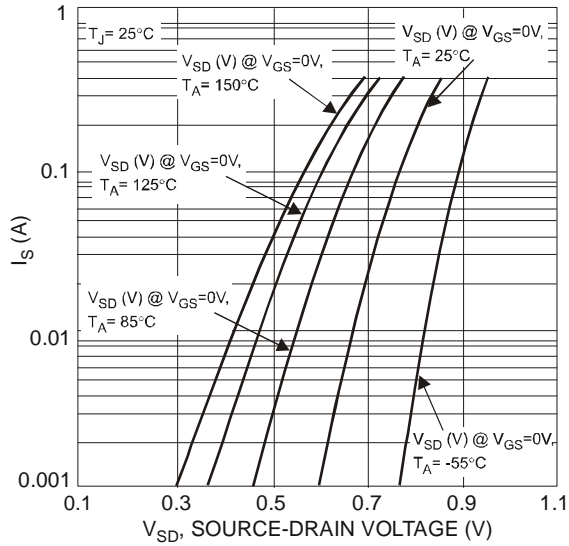


Fig. 7 Diodes Forward Voltage vs. Current

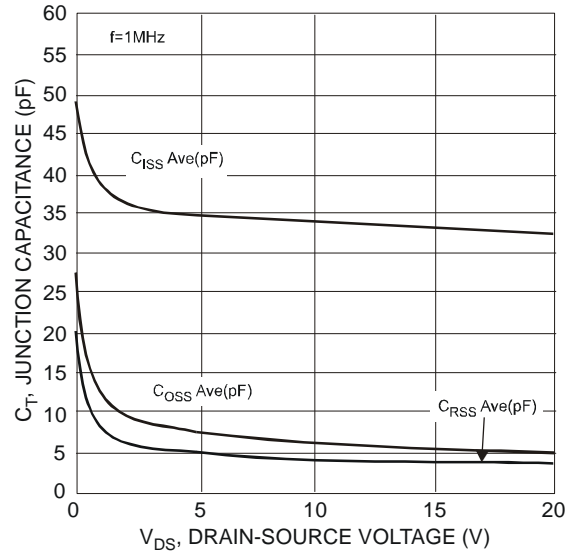


Fig. 8 Typical Junction Capacitance

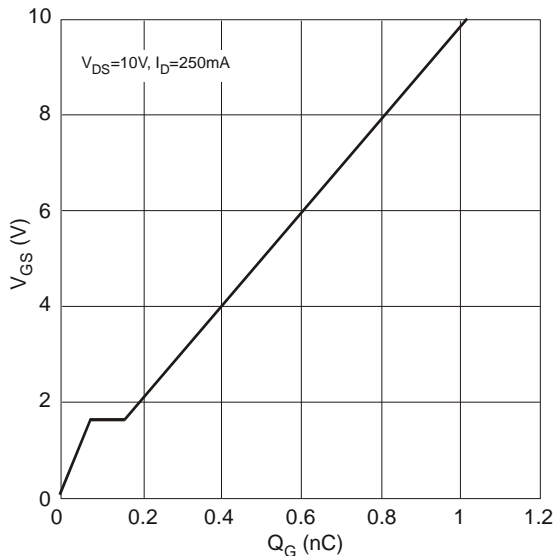


Fig. 9 Gate Charge Characteristics

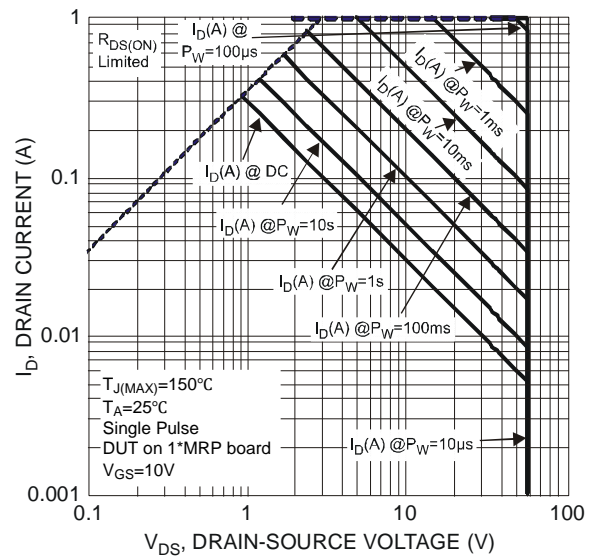


Fig. 10 SOA, Safe Operation Area

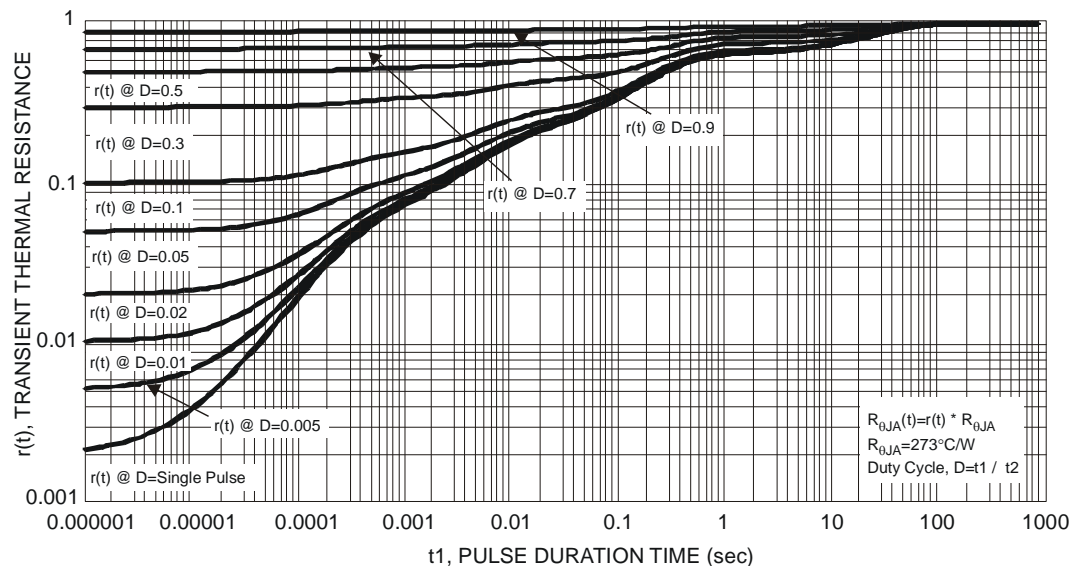
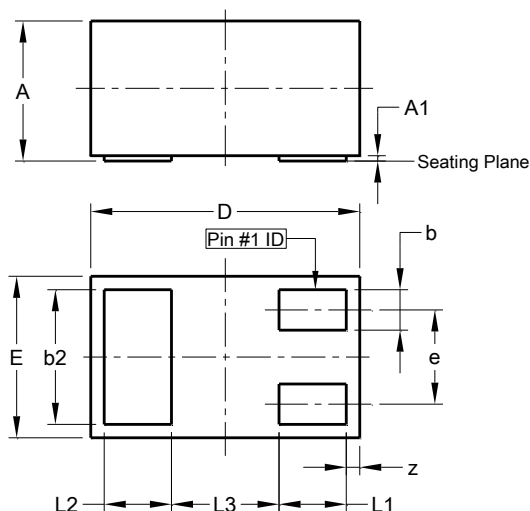


Fig. 11 Transient Thermal Resistance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

X1-DFN1006-3

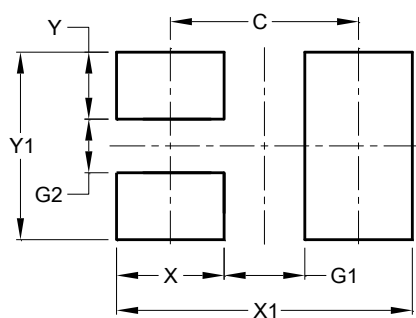


X1-DFN1006-3			
Dim	Min	Max	Typ
A	0.47	0.53	0.50
A1	0.00	0.05	0.03
b	0.10	0.20	0.15
b2	0.45	0.55	0.50
D	0.95	1.075	1.00
E	0.55	0.675	0.60
e	-	-	0.35
L1	0.20	0.30	0.25
L2	0.20	0.30	0.25
L3	-	-	0.40
z	0.02	0.08	0.05
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

X1-DFN1006-3



Dimensions	Value (in mm)
C	0.70
G1	0.30
G2	0.20
X	0.40
X1	1.10
Y	0.25
Y1	0.70

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