

Characteristic		Symbol	Value	Units
Drain-Source Voltage		V _{DSS}	30	V
Gate-Source Voltage		V _{GSS}	±20	V
Continuous Drain Current (Note 5) V _{GS} = 10V	T _A = +25°C T _A = +70°C	I _D	5.6 4.1	А
	T _C = +25°C T _C = +70°C	I _D	15.4 12.1	А
Maximum Continuous Body Diode Forward Current (Note 5)		I _S	1.5	Α
Pulsed Drain Current (10µs pulse, duty cycle = 1%)		I _{DM}	25	Α

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Total Power Dissipation (Note 5)	Ta = +25°C	P _D	1.8	- W
Total Power Dissipation (Note 5)	Ta = +70°C		1.1	
Thermal Resistance, Junction to Ambient (Note 5)		$R_{ heta JA}$	69	°C/W
Total Power Dissipation (Note 5)		P _D	14	W
Thermal Resistance, Junction to Case (Note 5)		$R_{ heta JC}$	8.7	°C/W
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

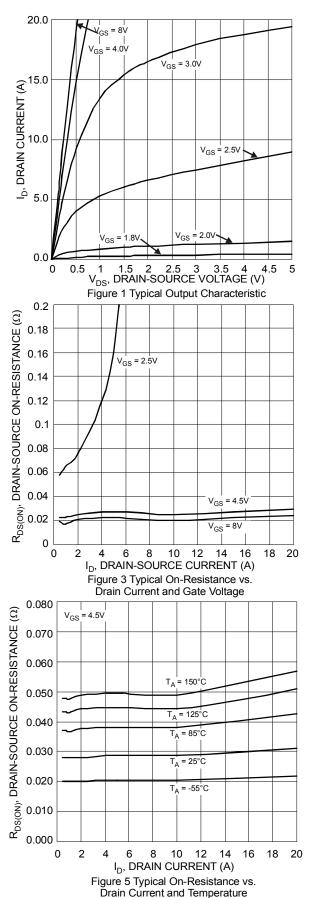
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

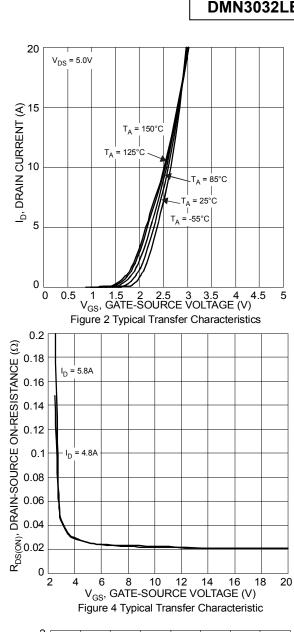
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)						
Drain-Source Breakdown Voltage	BV _{DSS}	30	1		V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current	I _{DSS}	_	1	1	μA	V _{DS} = 30V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 6)						
Gate Threshold Voltage	V _{GS(th)}	1	1	2	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
Static Drain-Source On-Resistance	Б	_	22	29	mΩ	V _{GS} = 10V, I _D = 3.2A
Static Dialii-Source Oil-Resistance	R _{DS (ON)}	_	27	35		V _{GS} = 4.5V, I _D = 2.8A
Forward Transfer Admittance	Y _{fs}	_	7	_	S	V _{DS} = 5V, I _D = 5.8A
Diode Forward Voltage	V_{SD}	_	0.7	1.5	V	V _{GS} = 0V, I _S = 1A
DYNAMIC CHARACTERISTICS (Note 7)						
Input Capacitance	C _{iss}	_	498		pF	V_{DS} = 15V, V_{GS} = 0V f = 1MHz
Output Capacitance	Coss	_	52	1		
Reverse Transfer Capacitance	C _{rss}	_	45	_		
Gate Resistnace	Rg	_	2.2	_	Ω	V_{DS} = 0V, V_{GS} = 0V, f = 1MHz
Total Gate Charge	Qg	_	11.3	_		V _{DS} = 15V, V _{GS} = 10V, I _D = 5.8A
Gate-Source Charge	Q _{gs}	_	1.4	_	nC	
Gate-Drain Charge	Q_{gd}	_	2.1	_		
Turn-On Delay Time	t _{D(on)}	_	2.3	_		V _{DS} = 15V, V _{GS} = 10V,
Turn-On Rise Time	t _r	_	3.9	_		
Turn-Off Delay Time	t _{D(off)}	_	10	_	ns	$R_L = 2.6\Omega$, $R_G = 3\Omega$
Turn-Off Fall Time	t _f	_	1.9	_		

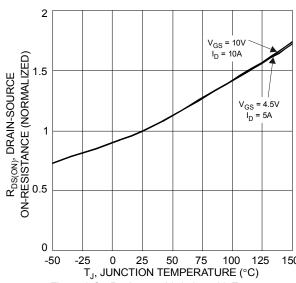
5. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal vias to bottom layer 1inch square copper plate 6 .Short duration pulse test used to minimize self-heating effect. Notes:

^{7.} Guaranteed by design. Not subject to production testing.

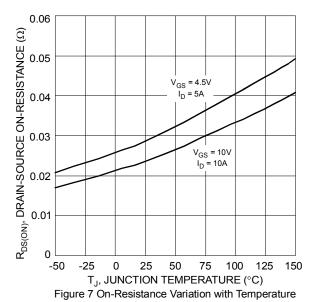


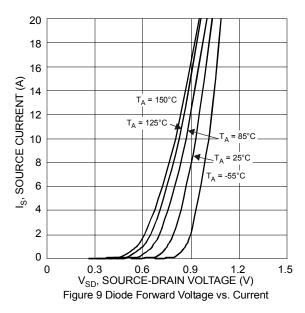


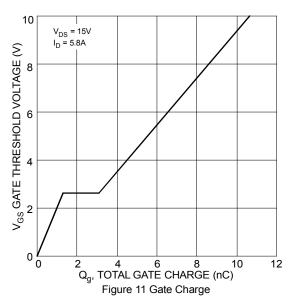












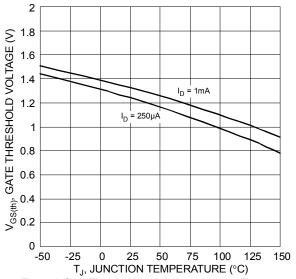
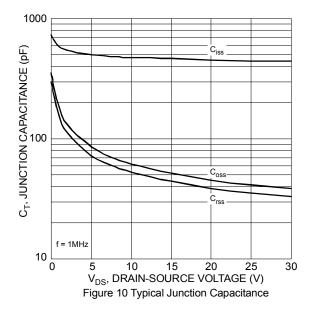
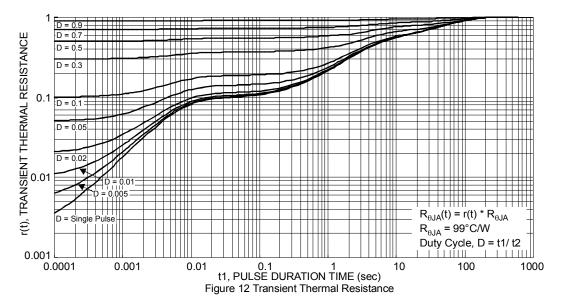


Figure 8 Gate Threshold Variation vs. Ambient Temperature

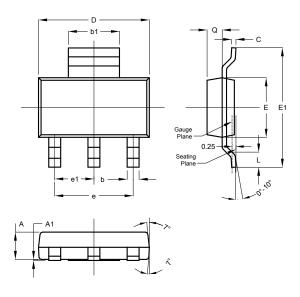






Package Outline Dimensions

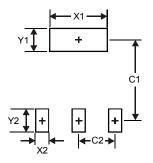
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



SOT223					
Dim	Min	Max	Тур		
Α	1.55	1.65	1.60		
A1	0.010	0.15	0.05		
b	0.60	0.80	0.70		
b1	2.90	3.10	3.00		
С	0.20	0.30	0.25		
D	6.45	6.55	6.50		
Е	3.45	3.55	3.50		
E1	6.90	7.10	7.00		
е	-	-	4.60		
e1	-	-	2.30		
L	0.85	1.05	0.95		
Q	0.84	0.94	0.89		
All Dimensions in mm					

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
X1	3.3
X2	1.2
Y1	1.6
Y2	1.6
C1	6.4
C2	2.3



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