

# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V <sub>DSS</sub>	20	V
Gate-Source Voltage			V <sub>GSS</sub>	±12	V
Continuous Drain Current (Note 7) $V_{GS}$ = 4.5V	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	ID	2.8 2.2	А
Maximum Continuous Body Diode Forward Current (Note 7)			Is	1.1	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I <sub>DM</sub>	12	А

## **Thermal Characteristics**

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 6)		PD	0.66	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R <sub>0JA</sub>	192	°C/W
Total Power Dissipation (Note 7)		PD	1.1	W
Thermal Resistance, Junction to Ambient (Note 7)	Steady State	R <sub>0JA</sub>	115	°C/W
Operating and Storage Temperature Range		T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	°C

## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)			71				
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	20		_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current $T_J = +25^{\circ}C$	I <sub>DSS</sub>	_	_	10	μA	$V_{DS} = 16V, V_{GS} = 0V$	
Gate-Source Leakage	IGSS	_	-	±10	μA	$V_{GS} = \pm 10V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	0.3	0.6	1.0	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Static Drain-Source On-Resistance		_	61	90	mΩ	$V_{GS} = 4.5V, I_D = 3.6A$	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>		80	120		$V_{GS} = 2.5V, I_D = 3.1A$	
Diode Forward Voltage	$V_{SD}$	—	0.7	1.2	V	$V_{GS} = 0V, I_{S} = 1.0A$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	Ciss	—	130	—	pF		
Output Capacitance	Coss	_	26	—	pF	<sup>−</sup> V <sub>DS</sub> = 10V, V <sub>GS</sub> = 0V − f = 1.0MHz	
Reverse Transfer Capacitance	Crss	_	18	_	pF		
Gate Resistance	Rg	—	2.7	—	Ω	$V_{DS}$ = 0V, $V_{GS}$ = 0V, f = 1MHz	
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Qg	—	1.4	—	nC	V <sub>DS</sub> = 10V, I <sub>D</sub> = 3.6A	
Total Gate Charge (V <sub>GS</sub> = 10V)	Qg	_	2.8	_	nC		
Gate-Source Charge	Q <sub>gs</sub>	_	0.1	_	nC		
Gate-Drain Charge	Q <sub>gd</sub>	_	0.5	_	nC		
Turn-On Delay Time	t <sub>D(ON)</sub>	_	0.6		ns	$V_{DS} = 10V, V_{GS} = 4.5V,$ $R_g = 1\Omega, R_L = 2.78\Omega$	
Turn-On Rise Time	t <sub>R</sub>	_	2.7	—	ns		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	—	4.2	—	ns		
Turn-Off Fall Time	t <sub>F</sub>	_	1.7	—	ns		
Reverse Recovery Time	t <sub>RR</sub>	—	5.3	—	ns	I <sub>F</sub> = 3.6A, di/dt = 100A/µs	
Reverse Recovery Charge	Q <sub>RR</sub>	_	0.5		nC	I <sub>F</sub> = 3.6A, di/dt = 100A/µs	

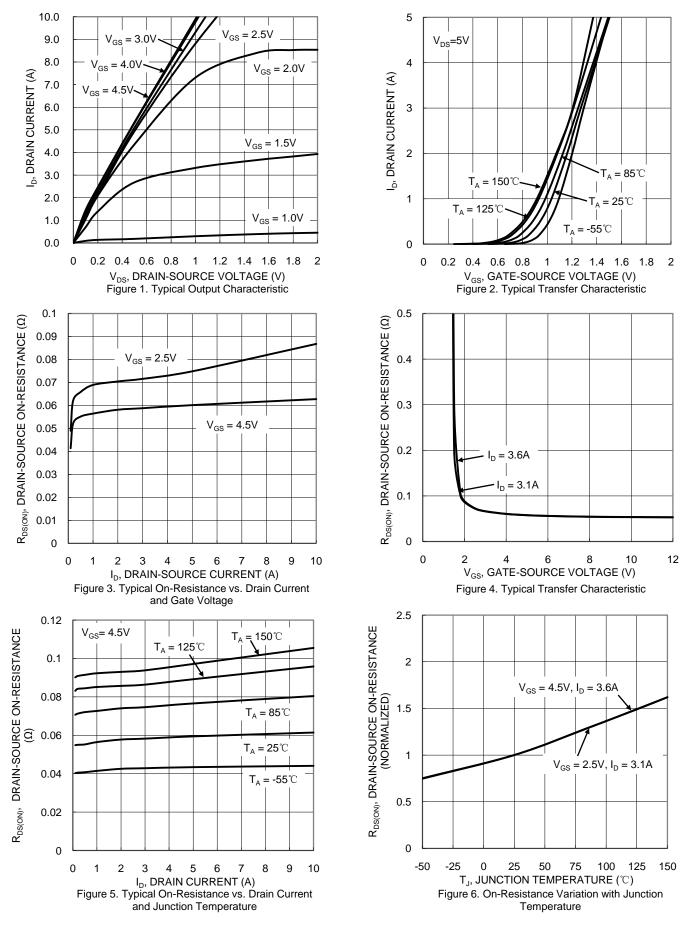
6. Device mounted on FR-4 PCB with minimum recommended pad layout. Notes:

Device mounted on 1" x 1" FR-4 PCB with high-coverage 2oz copper, single sided.
Short duration pulse test used to minimize self-heating effect.

9. Guaranteed by design. Not subject to product testing.



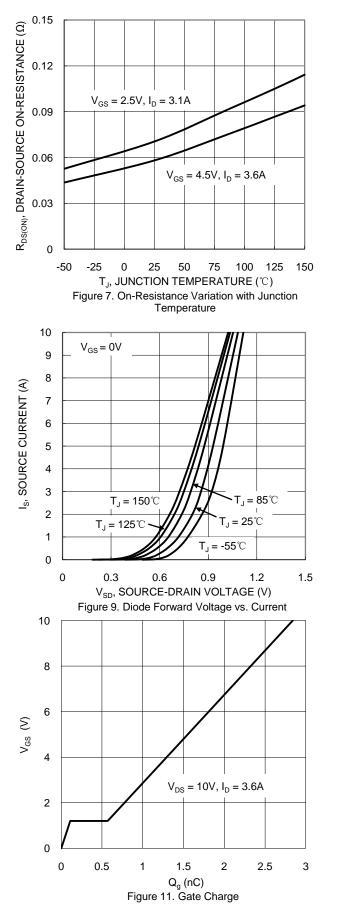
### DMG2302UKQ

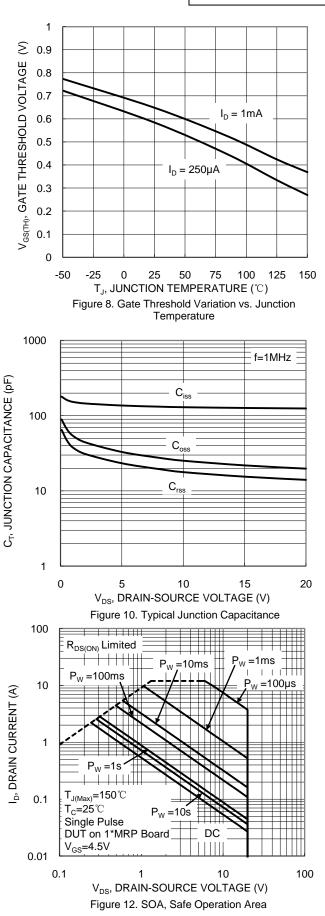


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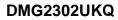
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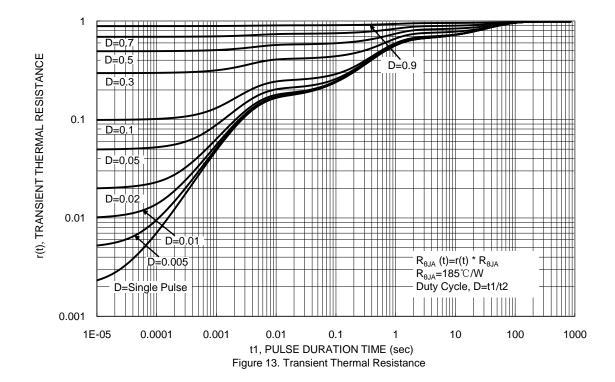




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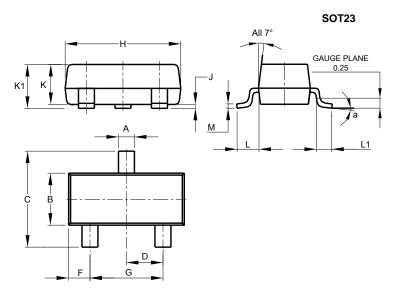






### **Package Outline Dimensions**

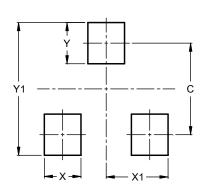
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT23						
Dim	Min	Max	Тур			
Α	0.37	0.51	0.40			
В	1.20	1.40	1.30			
С	2.30	2.50	2.40			
D	0.89	1.03	0.915			
F	0.45	0.60	0.535			
G	1.78	2.05	1.83			
Н	2.80	3.00	2.90			
J	0.013	0.10	0.05			
К	0.890	1.00	0.975			
K1	0.903	1.10	1.025			
L	0.45	0.61	0.55			
L1	0.25	0.55	0.40			
М	0.085	0.150	0.110			
а	0°	8°				
All Dimensions in mm						

## Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



#### SOT23

Dimensions	Value (in mm)			
С	2.0			
Х	0.8			
X1	1.35			
Y	0.9			
Y1	2.9			



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