## 60EPU02, 60APU02

## Vishay Semiconductors

# Ultrafast Soft Recovery Diode, 60 A FRED Pt®



<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS	
Reverse recovery time	t <sub>rr</sub>	$I_F = 1.0 \text{ A}, dI_F/dt = 200 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	-	35		
		T <sub>J</sub> = 25 °C		-	28	-	ns	
		T <sub>J</sub> = 125 °C	$I_F = 60 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_R = 160 \text{ V}$	-	50	-		
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C		-	4	-	А	
		T <sub>J</sub> = 125 °C		=	8	-		
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C		=	59	-	nC	
		T <sub>J</sub> = 125 °C		-	220	-		

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Thermal resistance, junction to case	R <sub>thJC</sub>		-	-	0.70	K/W
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.2	-	r\/vv
Weight			-	5.5	-	g
			-	0.2	-	OZ.
Mounting torque			-	-	1.2	N · m
Marking device		Case style TO-247AC modified	60EPU02			
		Case style TO-247AC	60APU02			



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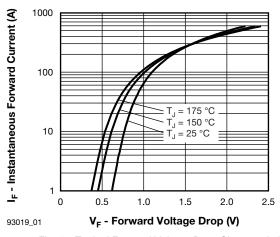


Fig. 1 - Typical Forward Voltage Drop Characteristics

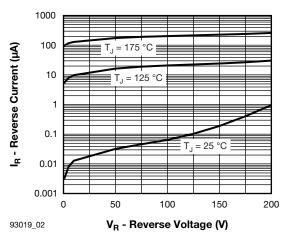


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

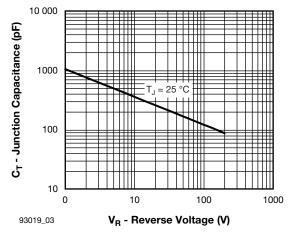


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

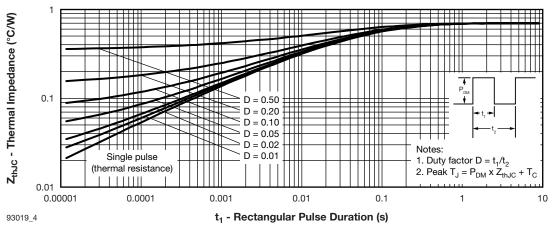


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

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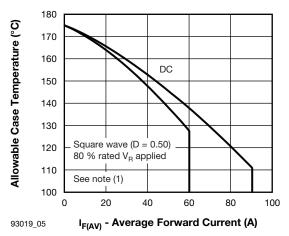


Fig. 5 - Maximum Allowable Case Temperature vs.

Average Forward Current

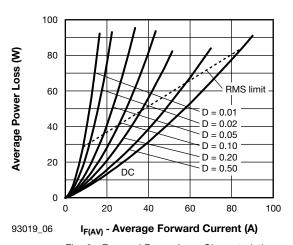


Fig. 6 - Forward Power Loss Characteristics

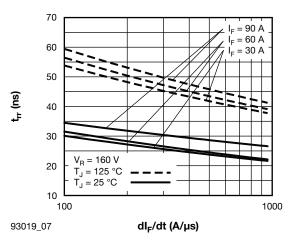


Fig. 7 - Typical Reverse Recovery Time vs.  $dI_F/dt$ 

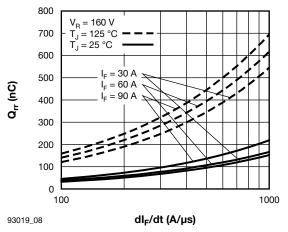


Fig. 8 - Typical Stored Charge vs. dI<sub>F</sub>/dt

#### Note

 $^{(1)}$  Formula used: T<sub>C</sub> = T<sub>J</sub> - (Pd + Pd<sub>REV</sub>) x R<sub>th,JC</sub>; Pd = Forward power loss = I<sub>F(AV)</sub> x V<sub>FM</sub> at (I<sub>F(AV)</sub>/D) (see fig. 6); Pd<sub>REV</sub> = Inverse power loss = V<sub>R1</sub> x I<sub>R</sub> (1 - D); I<sub>R</sub> at V<sub>R1</sub> = 80 % rated V<sub>R</sub>



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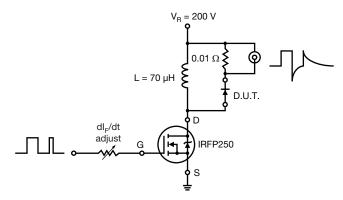
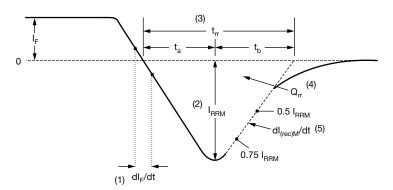


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dl<sub>F</sub>/dt rate of change of current through zero crossing
- (2)  $I_{RRM}$  peak reverse recovery current
- (3) t<sub>rr</sub> reverse recovery time measured from zero crossing point of negative going I<sub>F</sub> to point where a line passing through 0.75 I<sub>RBM</sub> and 0.50 I<sub>RBM</sub> extrapolated to zero current.
- (4)  $\mathbf{Q}_{rr}$  area under curve defined by  $\mathbf{t}_{rr}$  and  $\mathbf{I}_{\text{RRM}}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5)  $dl_{(rec)M}/dt$  - peak rate of change of current during  $t_b$  portion of  $t_{rr}$ 

Fig. 10 - Reverse Recovery Waveform and Definitions

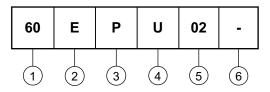
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#### **ORDERING INFORMATION TABLE**

**Device code** 



1 - Current rating (60 = 60 A)

2 - Circuit configuration:

E = Single diode

A = Single diode, 3 pins

Package:

P = TO-247AC (modified)

4 - Type of silicon:

U = Ultrafast recovery

5 - Voltage rating (02 = 200 V)

6 - None = Standard production

• PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS						
Dimensions	TO-247AC modified	www.vishay.com/doc?95253				
	TO-247AC	www.vishay.com/doc?95223				
Part marking information	TO-247AC modified	www.vishay.com/doc?95255				
	TO-247AC	www.vishay.com/doc?95226				
SPICE model		www.vishay.com/doc?95416				



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