

SMT Power Inductors

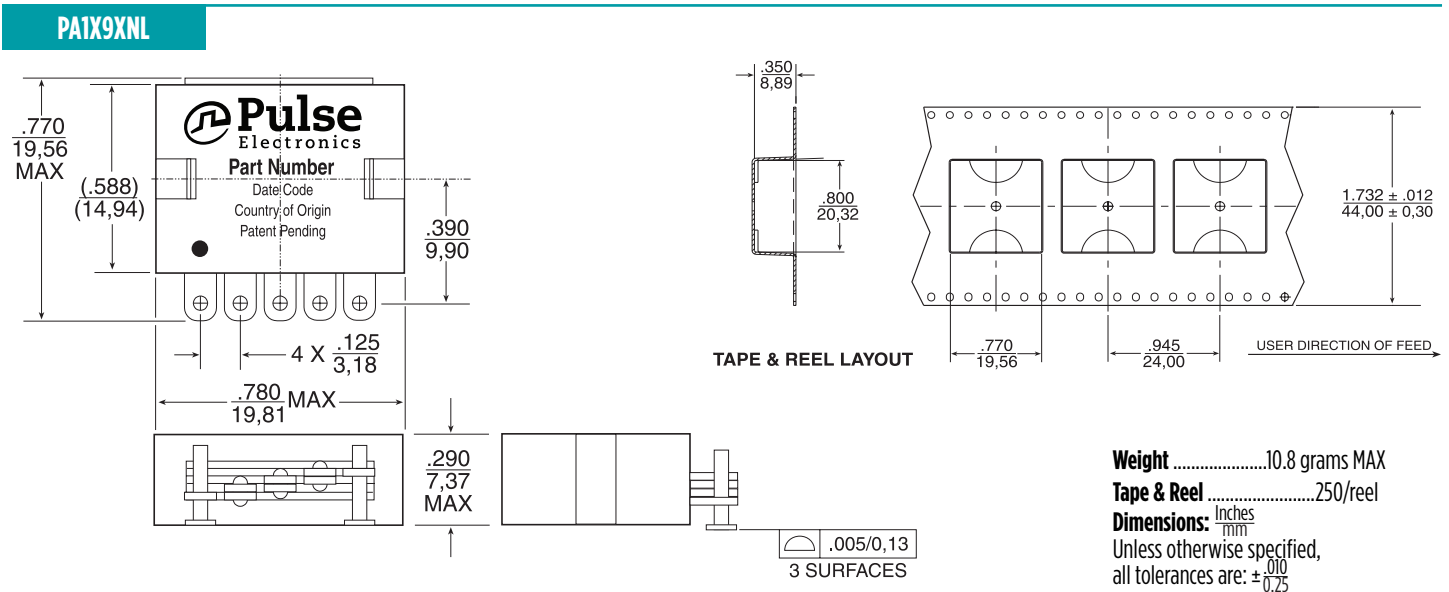
Planar - PA1X9XNL Series

Notes:

1. The rated current as listed is either 85% of the saturation current or the heating current, depending on which value is lower.
2. The saturation current is the current which causes the inductance to drop by 15% at the stated ambient temperatures (25°C and 100°C). This current is determined by placing the component in the specified ambient environment and applying a short duration pulse current (to eliminate self-heating effects) to the component.
3. The heating current is the DC current which causes the temperature of the part to increase by approximately 45°C. This current is determined by mounting the component on a PCB with .25" wide, 2 oz. equivalent copper traces, and applying the current to the device for 30 minutes with no forced air cooling.
4. In high volt*time applications, additional heating in the component can occur due to core losses in the inductor which may necessitate derating the current in order to limit the temperature rise of the component. In order to determine the approximate total losses (or temperature rise) for a given application, the total copper and core losses should be taken into account. For approximate value of core losses, in a given application, use the core loss graph on page 24.
5. Optional Tape & Reel packaging can be ordered by adding a "T" suffix to the part number (i.e. PA1294.450NL becomes PA1294.450NLT). Pulse complies to industry standard tape and reel specification EIA481.
6. Meets solderability test per IPC/EIA J-STD-002B using flux type ORLO.
7. The "NL" suffix indicates an RoHS-compliant, but are electrically and mechanically equivalent to NL versions. If a part number does not have the "NL" suffix, but an RoHS compliant version is required, please contact Pulse for availability.
8. The temperature of the component (ambient plus temperature rise) must be within the stated operating temperature range.

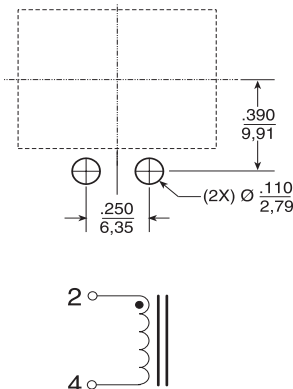
* Contact Pulse for availability

Mechanical

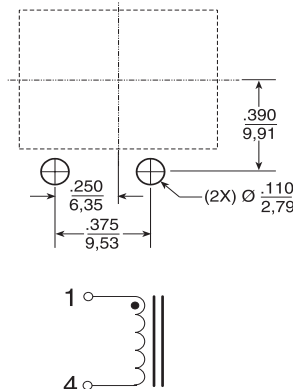


Suggested Pad Layouts and Schematics

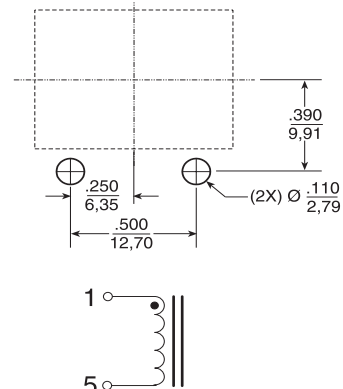
PA1292 & PA1294
 .405 to 1.50 μ H
 21 to 73 Adc



PA1393
 1.00 to 3.40 μ H
 12 to 42 Adc

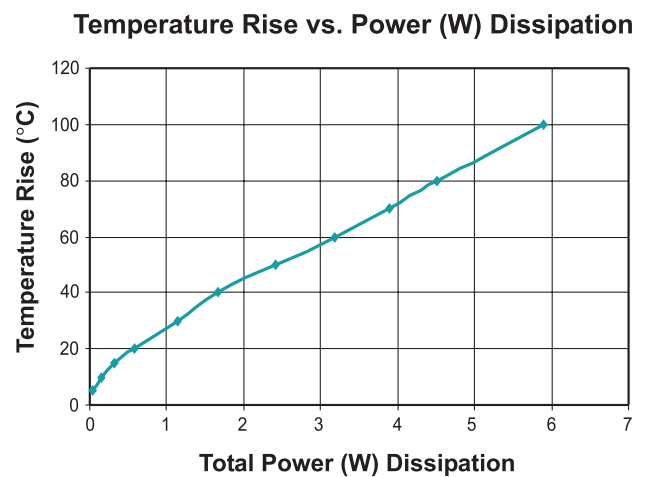
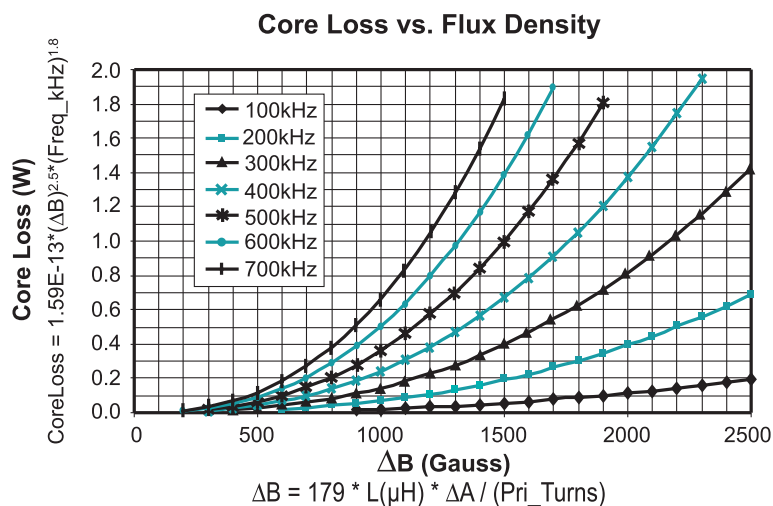
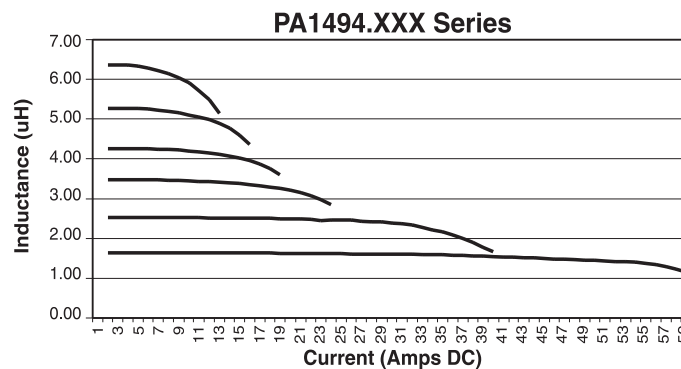
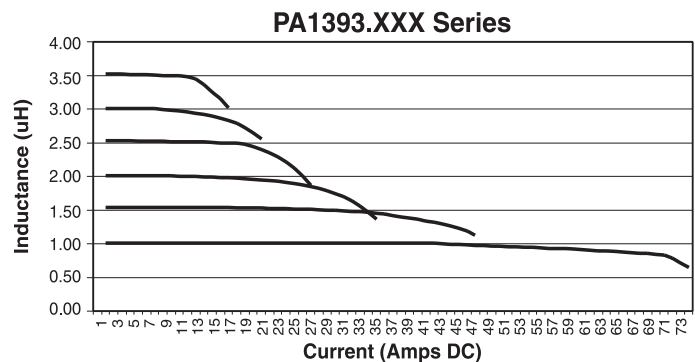
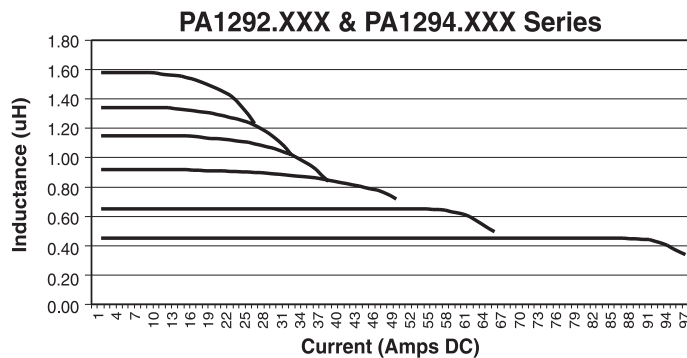


PA1494
 1.60 to 6.20 μ H
 9 to 37 Adc



Inductance vs. Current Characteristics (25°C)

PA1X9XNL



Total Power Dissipation = Copper Loss (W) + Core Loss (W)

Copper Loss (W) = $\text{Current (rms)}^2 * \text{DCR (m}\Omega) / 1000$
Core Loss (W) = per table

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For More Information

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