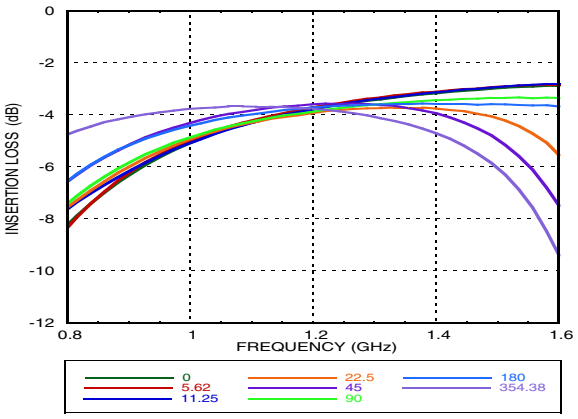
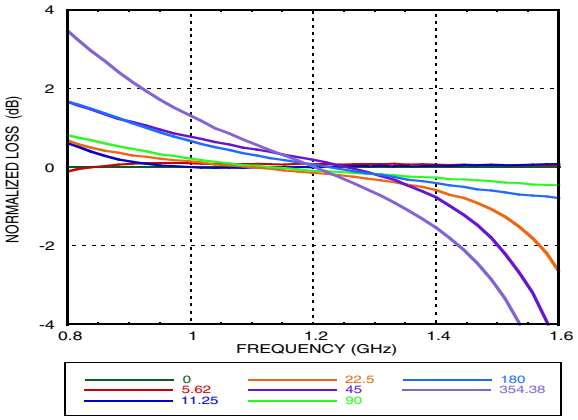
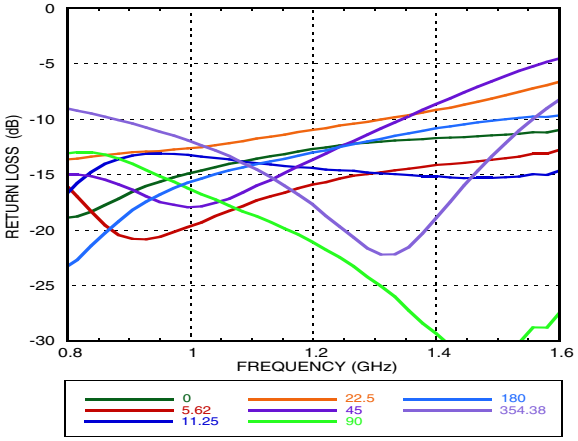
Insertion Loss, Major States Only



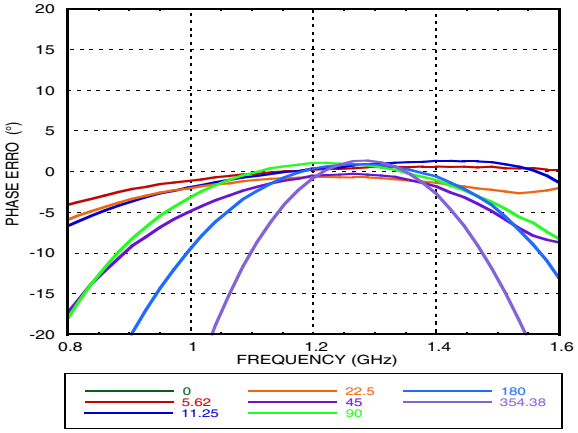
Normalized Loss, Major States Only



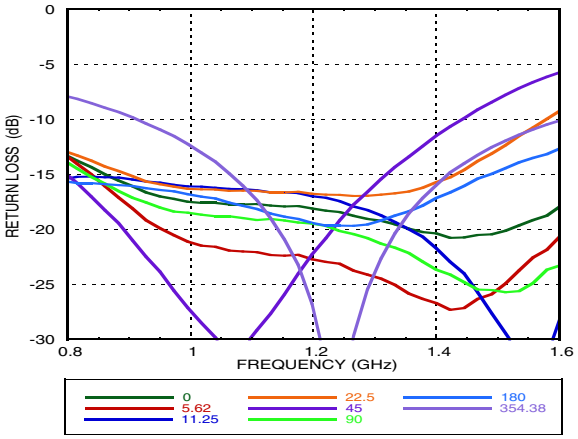
Input Return Loss, Major States Only



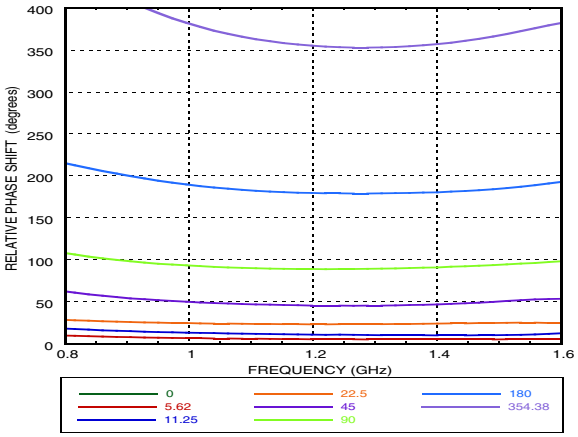
Phase Error, Major States Only



Output Return Loss, Major States Only

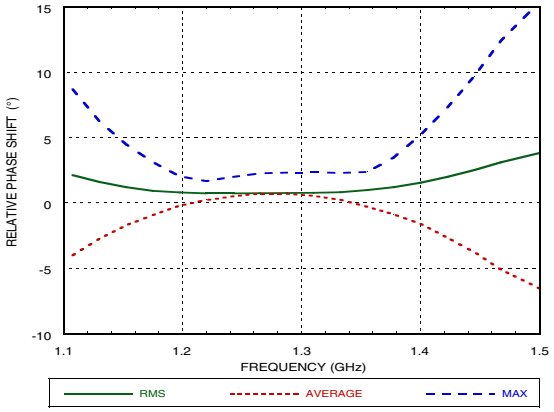


Relative Phase Shift Major States Including All Bits

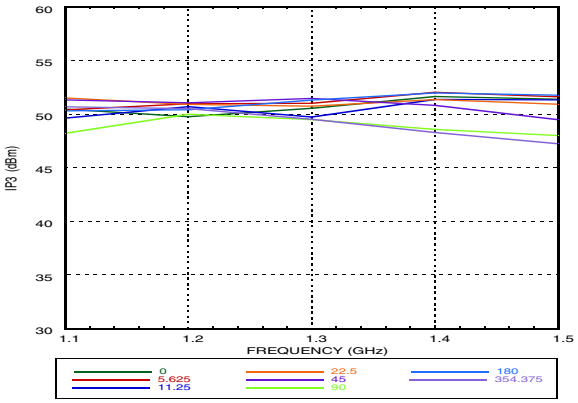


GaAs MMIC 6 BIT DIGITAL PHASE SHIFTERS, 1.2 - 1.4 GHz

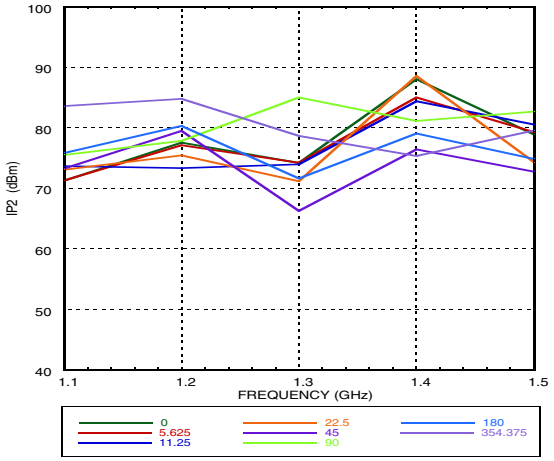
Relative Phase Shift, RMS, Average, Max, All States



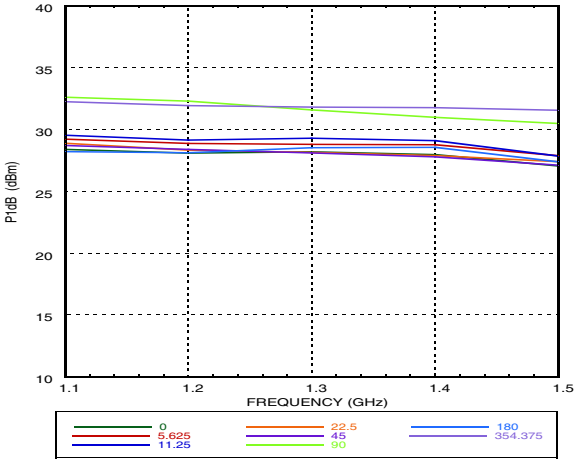
Input IP3, Major States Only



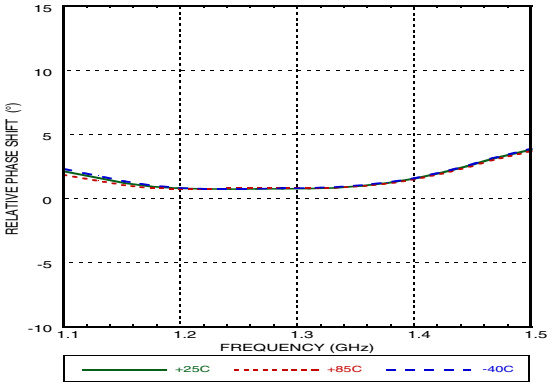
Input IP2, Major States Only



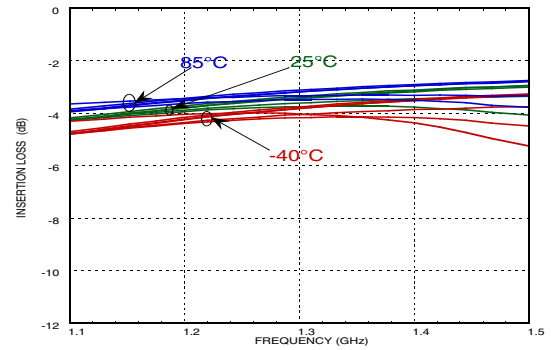
Input P1dB, Major States Only



RMS Phase Error vs. Temperature

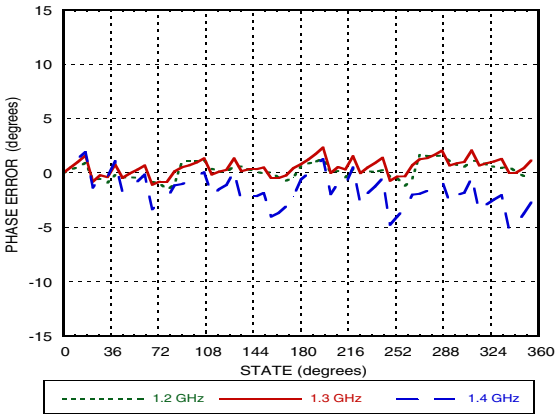


Insertion Loss vs. Temperature, Major States Only



GaAs MMIC 6 BIT DIGITAL PHASE SHIFTERS, 1.2 - 1.4 GHz

Phase Error vs. State



Absolute Maximum Ratings

Input Power (RFIN)	33 dBm (T= +85 °C)
Bias Voltage Range (Vdd)	-0.2 to +12V
Channel Temperature (Tc)	150 °C
Thermal Resistance (channel to ground paddle)	100 °C/W
Storage Temperature	-65 to +150 °C
Operating Temperature	-40 to +85 °C
ESD Sensitivity (HBM)	Class 1A

ELECTROSTATIC SENSITIVE DEVICE
OBSERVE HANDLING PRECAUTIONS

Bias Voltage & Current

Vdd	Idd
5.0	3 mA

Control Voltage

State	Bias Condition
Low (0)	0 to 0.2 Vdc
High (1)	Vdd ±0.2 Vdc @ 35 µA Typ.

Truth Table

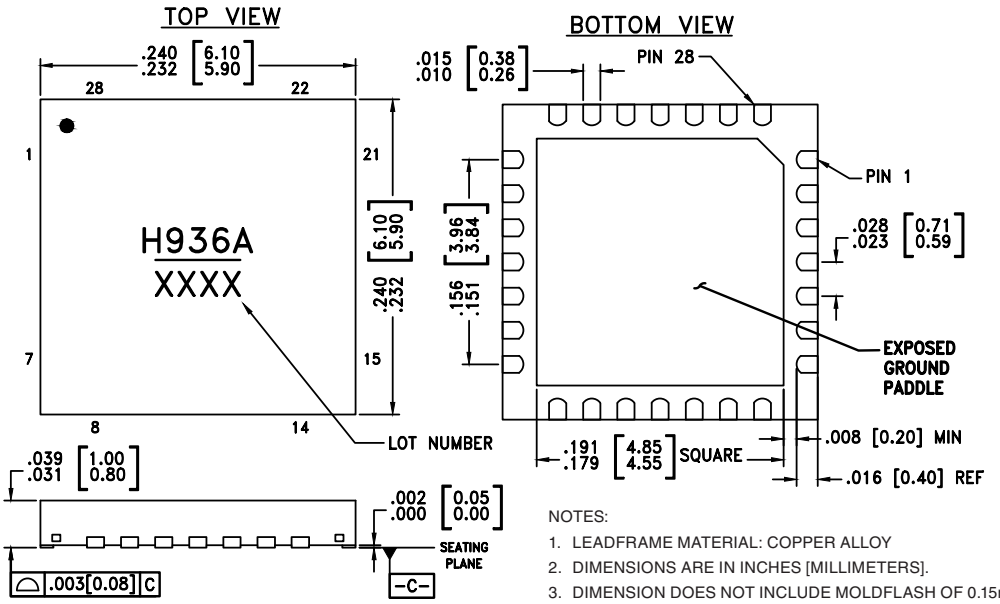
Control Voltage Input						Phase Shift (Degrees) RFIN - RFOUT
Bit 1	Bit 2	Bit 3	Bit 4	Bit 5	Bit 6	
1	1	1	1	1	1	Reference*
0	1	1	1	1	1	5.625
1	0	1	1	1	1	11.25
1	1	0	1	1	1	22.5
1	1	1	0	1	1	45.0
1	1	1	1	0	1	90.0
1	1	1	1	1	0	180.0
0	0	0	0	0	0	354.375

Any combination of the above states will provide a phase shift approximately equal to the sum of the bits selected.

*Reference corresponds to monotonic setting

GaAs MMIC 6 BIT DIGITAL PHASE SHIFTERS, 1.2 - 1.4 GHz

Outline Drawing



NOTES:

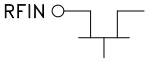
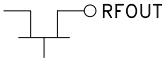
1. LEADFRAME MATERIAL: COPPER ALLOY
2. DIMENSIONS ARE IN INCHES [MILLIMETERS].
3. DIMENSION DOES NOT INCLUDE MOLDFLASH OF 0.15mm PER SIDE.
4. DIMENSION DOES NOT INCLUDE MOLDFLASH OF 0.25mm PER SIDE.
5. ALL GROUND LEADS MUST BE SOLDERED TO PCB RF GROUND.
6. CLASSIFIED AS MOISTURE SENSITIVITY LEVEL (MSL) 1.

Package Information

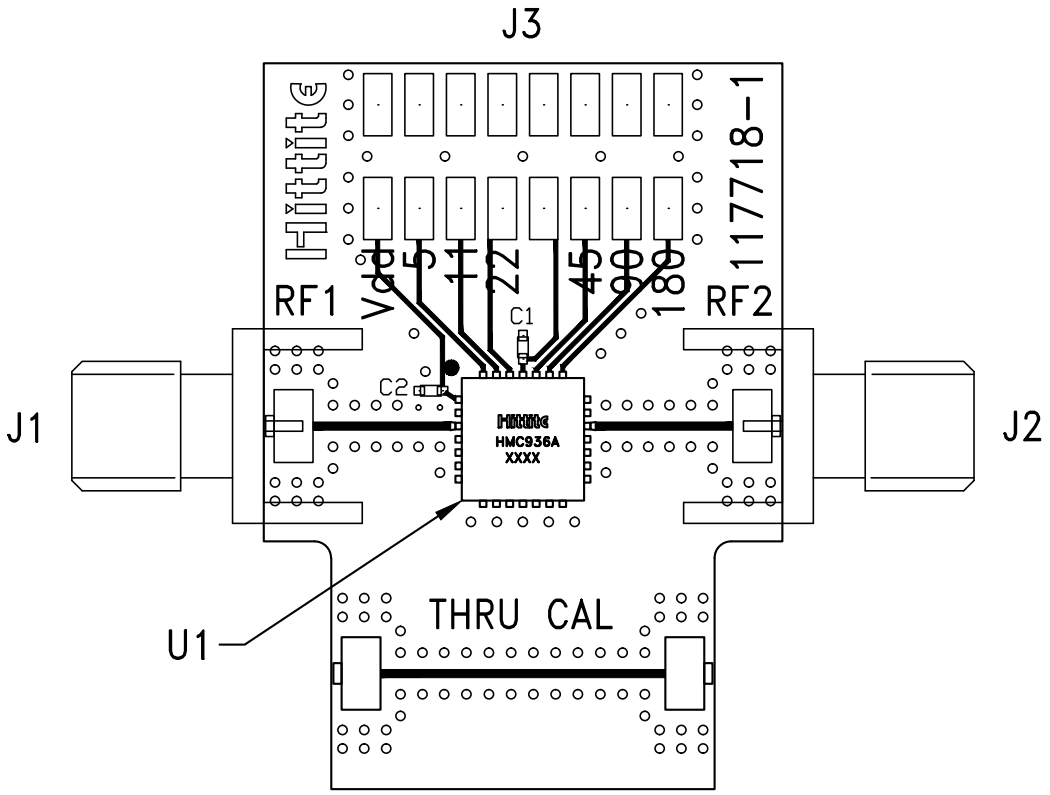
Part Number	Package Body Material	Lead Finish	MSL Rating	Package Marking [1]
HMC936ALP6E	RoHS-compliant Low Stress Injection Molded Plastic	100% matte Sn	MSL3 [2]	H936 XXXX

[2] Max peak reflow temperature of 260 °C
[1] 4-Digit lot number XXXX

Pin Descriptions

Pin Number	Function	Description	Interface Schematic
1	Vdd	Voltage supply.	
2, 20	GND	These pins and exposed ground paddle must be connected to RF/DC ground.	
3	RFIN	This port is DC coupled and matched to 50 Ohms.	
4 - 18, 21, 25	N/C	The pins are not connected internally; however, all data shown herein was measured with these pins connected to RF/DC ground externally.	
19	RFOUT	This port is DC coupled and matched to 50 Ohms.	
22 - 24, 26 - 28	BIT6, BIT5, BIT4, BIT3, BIT2, BIT1	Control Input. See truth table and control voltage tables.	

Evaluation PCB



List of Materials for Evaluation PCB EV1HMC936ALP6 [1][3]

Item	Description
J1 - J2	PCB Mount SMA RF Connector
J3	Header 2mm, 16 Pin
C1, C2	1000 pF Capacitor, 0402 Pkg.
U1	HMC936ALP6E 6-Bit Digital Phase Shifter
PCB [2]	117718 Evaluation PCB

[1] Reference this number when ordering complete evaluation PCB

[2] Circuit Board Material: Rogers 4350

[3] Please refer to part's pin description and functional diagram for pin out assignments on evaluation board.

The circuit board used in the application should use RF circuit design techniques. Signal lines should have 50 ohm impedance while the package ground leads and exposed paddle should be connected directly to the ground plane similar to that shown. A sufficient number of via holes should be used to connect the top and bottom ground planes. The evaluation board should be mounted to an appropriate heat sink. The evaluation circuit board shown is available from Analog Devices upon request.