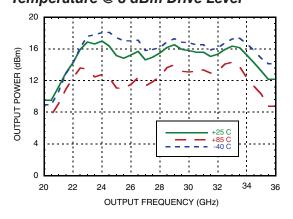




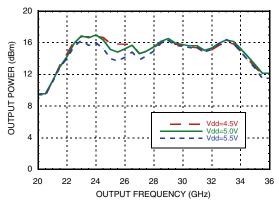
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SMT GaAs MMIC x2 ACTIVE FREQUENCY MULTIPLIER, 23 - 33 GHz

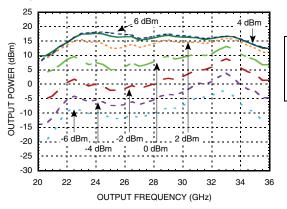
Output Power vs. Temperature @ 3 dBm Drive Level



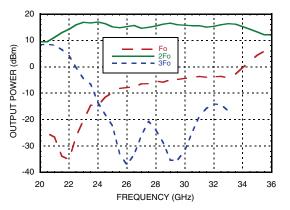
Output Power vs. Supply Voltage @ 3 dBm Drive Level

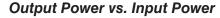


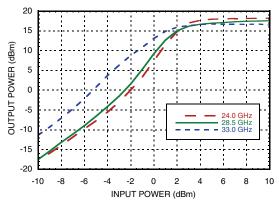
Output Power vs. Drive Level



Isolation @ 3 dBm Drive Level







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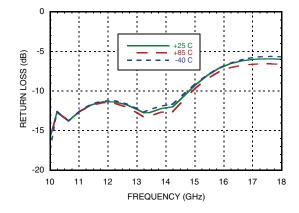




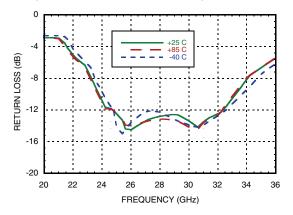
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Input Return Loss vs. Temperature

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Output Return Loss vs. Temperature



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Absolute Maximum Ratings

RF Input (Vdd = +5V)	+13 dBm
Supply Voltage (Vdd)	+6.0 Vdc
Channel Temperature	175 °C
Continuous Pdiss (T= 85 °C) (derate 7.4 mW/°C above 85 °C)	670 mW
Thermal Resistance (channel to ground paddle)	135 °C/W
Storage Temperature	-65 to +150 °C
Operating Temperature	-40 to +85 °C

v03.0514

Typical Supply Current vs. Vdd

BOTTOM VIEW

Vdd (Vdc)	ldd (mA)
4.5	81
5.0	81
5.5	81

Note:

Multiplier will operate over full voltage range shown above.



ELECTROSTATIC SENSITIVE DEVICE OBSERVE HANDLING PRECAUTIONS

Outline Drawing

PIN 12 0.118±.005 PIN [3.00±.13] .013 [0.32] 1 0.36 .014 REF 12 10 PIN 1 $\Box \Box$.022 .017 0.56 0.44 9 10 H578 $0.118 \pm .005$ 1.06 0.94 .042 .037 \square С [3.00±.13] ХХХХ 7 \cap 3 6 **EXPOSED** 4 -.083 [2.10] LOT NUMBER GROUND .059 [1.50] 0.036 [0.92] PADDLE SQUARE MAX NOTES: 1. PACKAGE BODY MATERIAL: ALUMINA SEATING 2. LEAD AND GROUND PADDLE PLATING: 30-80 MICROINCHES GOLD OVER PLANE 50 MICROINCHES MINIMUM NICKEL. 3. DIMENSIONS ARE IN INCHES [MILLIMETERS]. -C-4. LEAD SPACING TOLERANCE IS NON-CUMULATIVE 5. PACKAGE WARP SHALL NOT EXCEED 0.05mm DATUM -C-

6. ALL GROUND LEADS AND GROUND PADDLE MUST BE SOLDERED TO PCB RF GROUND.

Package Information

Part Number	Package Body Material	Lead Finish	MSL Rating	Package Marking ^[2]
HMC578LC3B	Alumina, White	Gold over Nickel	MSL3 ^[1]	H578 XXXX
[1] Max peak reflow temperature of 260 °C				

[2] 4-Digit lot number XXXX

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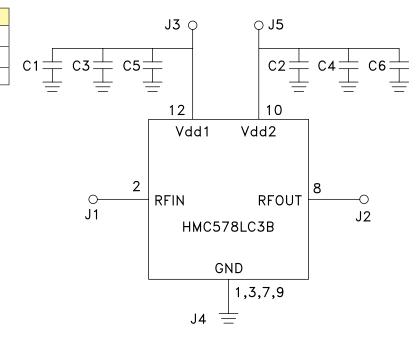
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Pin Description

Pin Number	Function	Description	Interface Schematic
1, 3, 7, 9	GND	Package bottom must also be connected to RF/DC ground.	
2	RFIN	Pin is AC coupled and matched to 50 Ohms.	
4 - 6, 11	N/C	These pins are internally not connected; however, this product was specified with these pins connected to RF/ DC ground.	
8	RFOUT	Pin is AC coupled and matched to 50 Ohms.	
10, 12	Vdd2, Vdd1	Supply voltage 5V \pm 0.5V. External bypass capacitors of 100 pF, 1,000 pF and 2.2 μF are required.	Vdd1, Vdd2

Application Circuit

Value
100 pF
1,000 pF
2.2 µF



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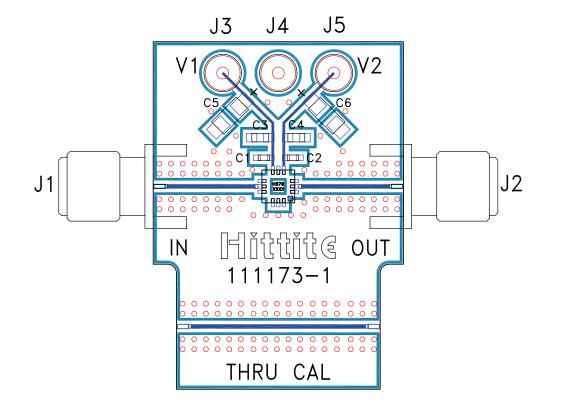
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Evaluation PCB



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List of Materials for Evaluation PCB 112409^[1]

Item	Description
J1, J2	PCB Mount SRI K Connector
J3 - J5	DC Pin
C1, C2	100 pF Capacitor, 0402 Pkg.
C3, C4	1,000 pF Capacitor, 0603 Pkg.
C5, C6	2.2 µF Tantalum Capacitor
U1	HMC578LC3B x2 Active Multiplier
PCB [2]	111173 Eval Board

[1] Reference this number when ordering complete evaluation PCB

[2] Circuit Board Material: Rogers 4350

The circuit board used in the final application should be generated with proper RF circuit design techniques. Signal lines should have 50 ohm impedance while the package ground leads and exposed paddle should be connected directly to the ground plane similar to that shown. A sufficient number of via holes should be used to connect the top and bottom ground planes. The evaluation circuit board shown is available from Hittite upon request.

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