

## **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Drain-Source Voltage	$V_{DSS}$	30	V
Gate-Source Voltage	$V_{GSS}$	±8	V
Drain Current (Note 6)	Ι <sub>D</sub>	3.2	Α
Pulsed Drain Current (Note 6)	$I_{DM}$	12.8	Α

## **Thermal Characteristics**

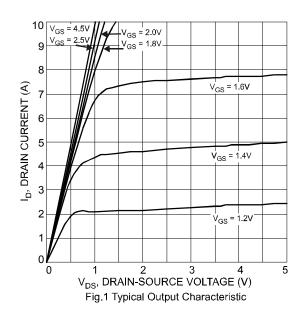
Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 6)	P <sub>D</sub>	900	mW
Thermal Resistance, Junction to Ambient	$R_{ heta JA}$	139	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

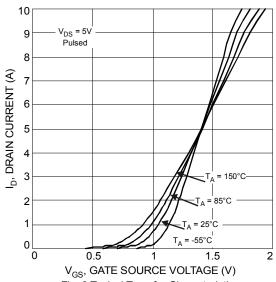
## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	30	_	_	V	$V_{GS} = 0V, I_D = 100\mu A$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>			1	μΑ	$V_{DS} = 30V, V_{GS} = 0V$	
Gate-Source Leakage	IGSS			±5	μΑ	$V_{GS} = \pm 8V$ , $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V <sub>GS(th)</sub>	0.5		1.0	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
	R <sub>DS (ON)</sub>		40 50 76	60	mΩ	$V_{GS} = 4.5V, I_D = 6A$	
Static Drain-Source On-Resistance				80 130		$V_{GS} = 2.5V, I_D = 2A$	
						$V_{GS} = 1.5V, I_D = 1.0A$	
Forward Transfer Admittance	Y <sub>fs</sub>		8	_	S	$V_{DS} = 10V, I_D = 6A$	
Diode Forward Voltage (Note 7)	$V_{SD}$	_	0.7	1.1	V	$V_{GS} = 0V, I_{S} = 2A$	
DYNAMIC CHARACTERISTICS							
Input Capacitance	C <sub>iss</sub>	_	476	_	pF	V 45V V 9V	
Output Capacitance	Coss	_	77	_	pF	$V_{DS} = 15V, V_{GS} = 0V$ - f = 1.0MHz	
Reverse Transfer Capacitance	C <sub>rss</sub>		59	_	pF		

Notes:

- 6. Device mounted on FR-4 PCB, minimum recommended pad layout on 2oz. Copper pads.
- 7. Short duration pulse test used to minimize self-heating effect.







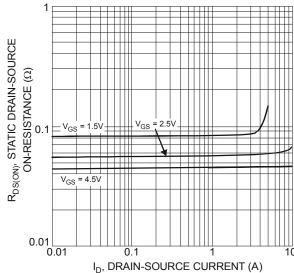


Fig. 3 On-Resistance vs. Drain Current & Gate Voltage

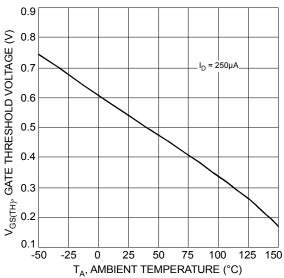


Fig. 5 Gate Threshold Variation vs. Ambient Temperature

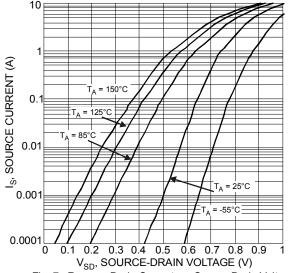


Fig. 7 Reverse Drain Current vs. Source-Drain Voltage

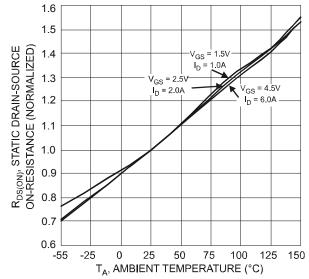
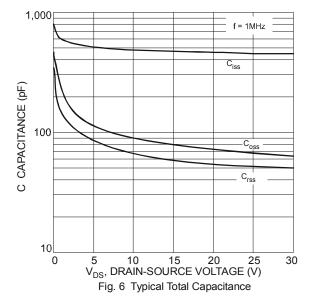


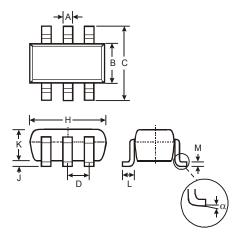
Fig. 4 Normalized Static Drain-Source On-Resistance vs. Ambient Temperature





# **Package Outline Dimensions**

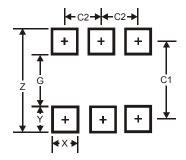
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



SOT26					
Dim	Min	Max	Тур		
Α	0.35	0.50	0.38		
В	1.50	1.70	1.60		
С	2.70	3.00	2.80		
D	_		0.95		
Н	2.90	3.10	3.00		
J	0.013	0.10	0.05		
K	1.00	1.30	1.10		
L	0.35	0.55	0.40		
M	0.10	0.20	0.15		
α	0°	8°	_		
All D	All Dimensions in mm				

# **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	3.20
G	1.60
Х	0.55
Υ	0.80
C1	2.40
C2	0.95



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