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Vishay Semiconductors

ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS		
	V _{FM} ⁽¹⁾	10 A	T _{.1} = 25 °C	0.64	V	
Maximum forward voltage drop per leg		20 A	1j=23 0	0.76		
See fig. 1		10 A	T 105 %C	0.57		
		20 A	T _J = 125 °C	0.68		
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	2	mA	
See fig. 2		T _J = 125 °C	$v_{\rm R} = naleu v_{\rm R}$	15		
Maximum junction capacitance per leg	CT	V_R = 5 V_{DC} (test signal range 100 kHz to 1 MHz) 25 °C		900	pF	
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		8.0	nH	
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs	

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,\,duty\,cycle$ < 2 $\,\%$

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL TEST CONDITIONS		VALUES	UNITS	
Maximum junction and storage temperature range)	T _J , T _{Stg}		- 55 to 175	°C	
Maximum thermal resistance, junction to case per leg		R _{thJC}	DC operation See fig. 4	3.25		
Maximum thermal resistance, junction to case per package		hthJC	DC operation	1.63		
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50		
Approvimeto weight				2	g	
Approximate weight				0.07	oz.	
Mounting towns	minimum			6 (5)	kgf ⋅ cm	
Mounting torque	maximum			12 (10)	(lbf · in)	
Marking device				20CTQ035		
			Case style TO-220AB	20CT	Q040	
				20CT	Q045	

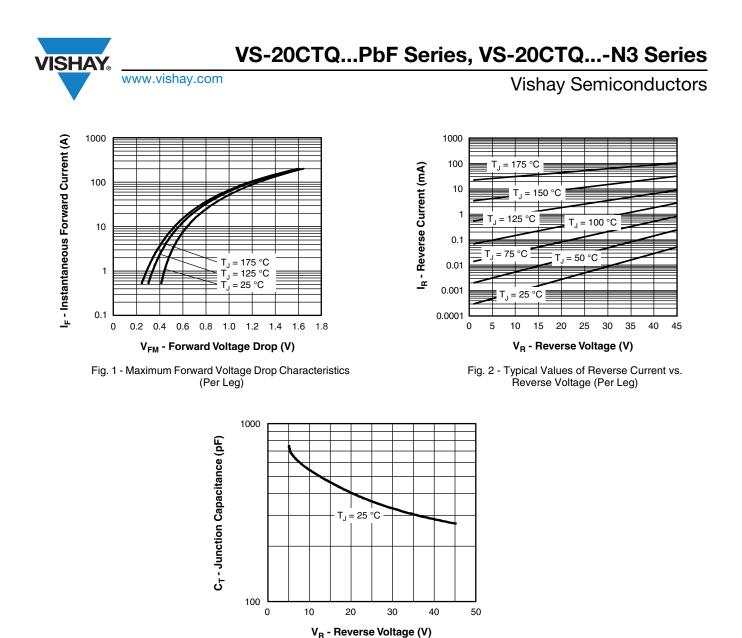
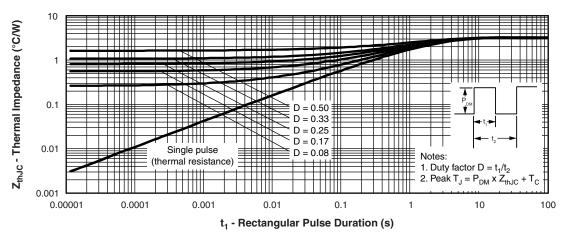
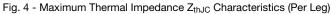


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)



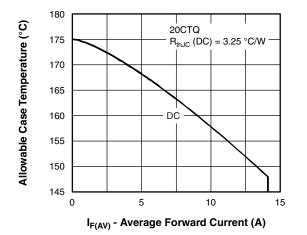


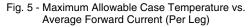
Revision: 25-Aug-11 3 Document Number: 94162 For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>

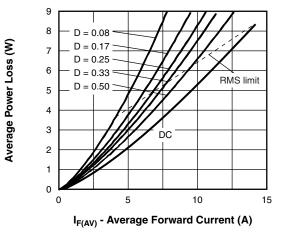


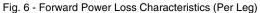
VS-20CTQ...PbF Series, VS-20CTQ...-N3 Series

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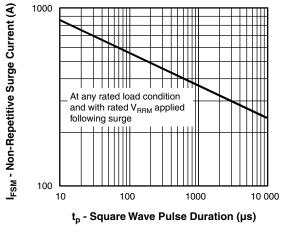


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

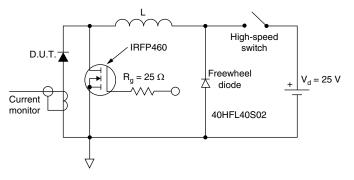


Fig. 8 - Unclamped Inductive Test Circuit



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ORDERING INFORMATION TABLE

Device code	VS-	20	С	т	Q	045	PbF
	1	2	3	4	5	6	7
1 2 3	- - -	Curren Circuit	t rating configu	onductor (20 = 20 ration cathode	•	ct	
4	-	Package T = TO-220					
5 6 7	-	Schottky "Q" series Voltage rating Environmental digit • PbF = Lead (Pb)-free and R			035 = 040 = 045 =	40 V 45 V	

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-20CTQ035PbF	50	1000	Antistatic plastic tube			
VS-20CTQ035-N3	50	1000	Antistatic plastic tube			
VS-20CTQ040PbF	50	1000	Antistatic plastic tube			
VS-20CTQ040-N3	50	1000	Antistatic plastic tube			
VS-20CTQ045PbF	50	1000	Antistatic plastic tube			
VS-20CTQ045-N3	50	1000	Antistatic plastic tube			

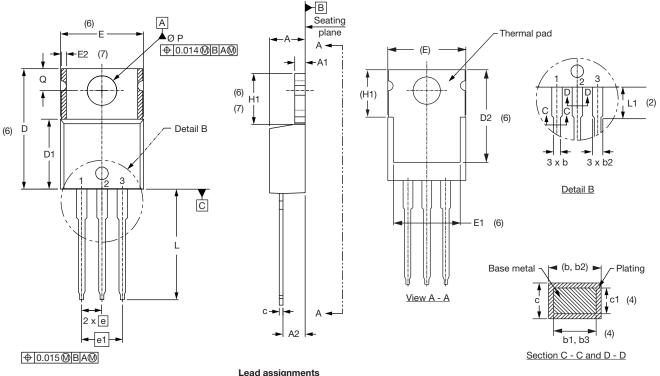
LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?95222				
Part marking information	TO-220AB PbF	www.vishay.com/doc?95225		
	TO-220AB -N3	www.vishay.com/doc?95028		

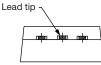


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TO-220AB

DIMENSIONS in millimeters and inches





ead.	assignments

Diodes

3. - Anode

1. - Anode/open 2. - Cathode

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6

Notes

- ⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994
- ⁽²⁾ Lead dimension and finish uncontrolled in L1
- ⁽³⁾ Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed $0.127 \text{ mm} (0.005^{\circ})$ per side. These dimensions are measured at the outermost extremes of the plastic body
- $^{\left(4\right) }$ Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1

SYMBOL		MILLIMETERS		INC	NOTES	
		MIN.	MAX.	MIN.	MAX.	NOTES
Е		10.11	10.51	0.398	0.414	3, 6
E1		6.86	8.89	0.270	0.350	6
E2		-	0.76	-	0.030	7
е		2.41	2.67	0.095	0.105	
e1		4.88	5.28	0.192	0.208	
H1		6.09	6.48	0.240	0.255	6, 7
L		13.52	14.02	0.532	0.552	
L1		3.32	3.82	0.131	0.150	2
ØΡ)	3.54	3.73	0.139	0.147	
Q		2.60	3.00	0.102	0.118	
θ		90° to 93°		90° t	o 93°	
θ		90° to 93°		90° t	90° to 93°	

Conforms to JEDEC outline TO-220AB

- $^{(7)}$ Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- Outline conforms to JEDEC TO-220, except A2 (maximum) and (8) D2 (minimum) where dimensions are derived from the actual package outline

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