

**Vishay Semiconductors** 

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT				
INPUT								
LED continuous forward current		١ <sub>F</sub>	50	mA				
LED reverse voltage		V <sub>R</sub>	5	V				
Ουτρυτ								
Repetitive peak off-state voltage	Sine wave, 50 Hz to 60 Hz, gate open	V <sub>DRM</sub>	600	V				
On-state RMS current		I <sub>T(RMS)</sub>	1	А				
Peak non-repetitive surge current (50 Hz, peak)		I <sub>TSM</sub>	10	А				
COUPLER								
Total power dissipation <sup>(2)</sup>		P <sub>diss</sub>	1.2	W				
Ambient temperature range		T <sub>amb</sub>	-40 to +85	°C				
Storage temperature range		T <sub>stg</sub>	-40 to +125	°C				
Soldering temperature <sup>(1)</sup>	$t \le 10$ s max.	T <sub>sld</sub>	260	°C				
Isolation test voltage	For 1 s	V <sub>ISO</sub>	5300	V <sub>RMS</sub>				

Notes

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability

<sup>(1)</sup> Refer to wave profile for soldering conditions for through hole devices

<sup>(2)</sup> Total power dissipation value is based on 2S2P PCB

#### **ABSOLUTE MAXIMUM RATING CURVES**

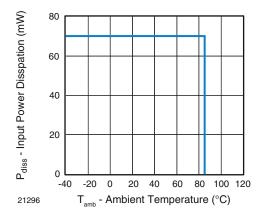


Fig. 1 - Power Dissipation vs. Temperature

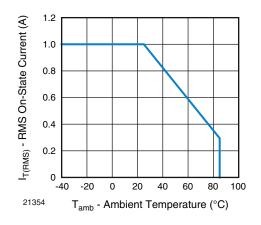


Fig. 2 - Allowable Load Current vs. Ambient Temperature

#### Note

 The allowable load current was calculated out under a given operating conditions and only for reference: LED power: Q<sub>E</sub> = 0.015 W, θ<sub>BA</sub> (4-layer) = 30 °C/W

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ELECTRICAL CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
INPUT							
LED trigger current	V <sub>T</sub> = 6 V	I <sub>FT</sub>	2.5	-	10	mA	
Input reverse current	V <sub>R</sub> = 5 V	I <sub>R</sub>	-	-	10	μA	
LED forward voltage	I <sub>F</sub> = 10 mA	V <sub>F</sub>	0.9	-	1.4	V	
OUTPUT							
Peak on-state voltage	I <sub>TM</sub> = 1 A	V <sub>TM</sub>	-	-	1.7	V	
Peak off-state current	V <sub>DRM</sub> = 600 V, T <sub>A</sub> = 110 °C	I <sub>DRM</sub>	-	-	100	μA	
Holding current	$R_L = 100 \Omega$	Ι <sub>Η</sub>	-	-	25	mA	
Critical rate of rise of off-state voltage	V <sub>IN</sub> = 400 V <sub>RMS</sub> (Fig. 3)	dV/dt <sub>cr</sub>	-	210	-	V/µs	
Critical rate of rise of commutating voltage	$V_{IN} = 240 V_{RMS}$ , $I_T = 1 A_{RMS}$ (Fig. 3)	dV/dt <sub>crq</sub>	-	0.7	-	V/µs	

#### Note

• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements

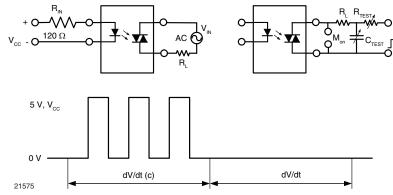


Fig. 3 - dV/dt Test Circuit

SAFETY AND INSULATION RATINGS							
PARAMETER		TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Climatic classification		IEC 68 part 1		-	40/85/21	-	
Pollution degree		DIN VDE 0109		-	2	-	
Tracking resistance (comparative tracking index)		Insulation group IIIa	CTI	175	-	-	
Highest allowable overvoltage		Transient overvoltage	V <sub>IOTM</sub>	8000	-	-	V <sub>peak</sub>
Maximum working insulation voltage		Recurring peak voltage	V <sub>IORM</sub>	890	-	-	V <sub>peak</sub>
Insulation resistance at 25 °C		V <sub>IO</sub> = 500 V	R <sub>IS</sub>	-	-	≥ 10 <sup>12</sup>	Ω
Insulation resistance at T <sub>S</sub>		V <sub>IO</sub> = 500 V	R <sub>IS</sub>	-	-	≥ 10 <sup>9</sup>	Ω
Insulation resistance at 100 °C		V <sub>IO</sub> = 500 V	R <sub>IS</sub>	-	-	≥ 10 <sup>11</sup>	Ω
Partial discharge test voltage		Method b, V <sub>pd</sub> = V <sub>IORM</sub> x 1.6	V <sub>pd</sub>	-	-	1424	V <sub>peak</sub>
Safety limiting values - maximum values allowed in the event of a failure	Case temperature		T <sub>SI</sub>	-	-	165	°C
	Input current		I <sub>SI</sub>	-	-	150	mA
	Output power		P <sub>SO</sub>	-	-	2000	mW
Minimum external air gap (clearance distance)		Measured from input terminals to output terminals, shortest distance through air		≥7	-	-	mm
Minimum external tracking (creepage distance)		Measured from input terminals to output terminals, shortest distance path along body		≥7	-	-	mm

Note

• This phototriac coupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with safety ratings shall be ensured by means of protective circuits

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### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

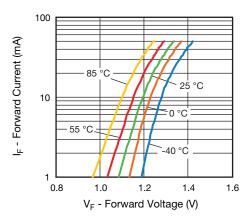


Fig. 4 - Forward Current vs. Forward Voltage

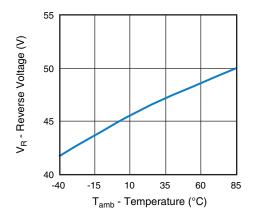


Fig. 5 - Reverse Voltage vs. Temperature

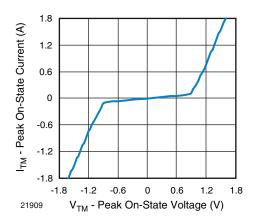


Fig. 6 - On-State Current vs. On-State Voltage

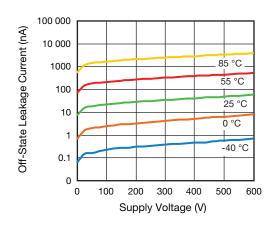


Fig. 7 - Off-State Leakage Current vs. Voltage

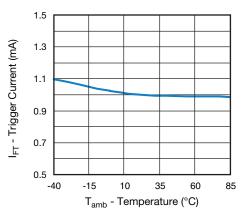


Fig. 8 - Normalized Trigger Input Current vs. Temperature

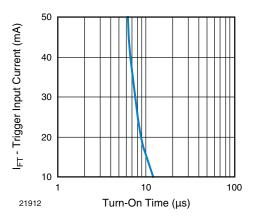


Fig. 9 - Trigger Input Current vs. Turn-on Time

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# Vishay Semiconductors

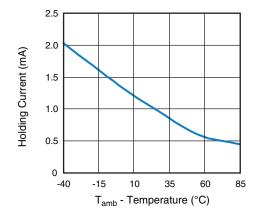


Fig. 10 - Normalized Holding Current vs. Temperature

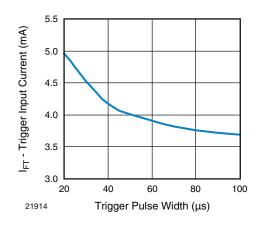


Fig. 11 - Trigger Current vs. Trigger Pulse Width

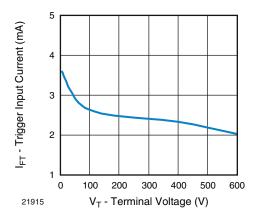
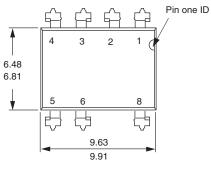


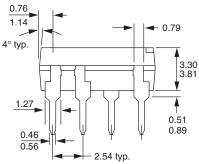
Fig. 12 - Trigger Current vs. VLOAD

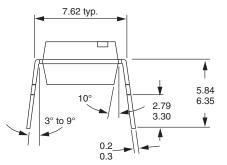
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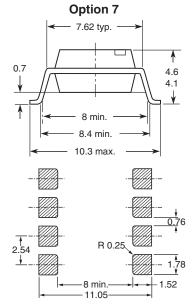
### **PACKAGE DIMENSIONS** in millimeters







ISO method A



PACKAGE MARKING (Example of VO2223A-X001)



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