Absolute Maximum Ratings (Ta = 25°C)

Characteristics			Symbol	Rating	Unit
LED	Forward current		I_F	50	mA
	Forward current derating (Ta ≥53°C)		ΔI_F/°C	−0.7	mA/°C
	Peak forward current (100 μs pulse, 100 pps)		I_{FP}	1	A
	Reverse voltage		V_R	5	V
	Diode power dissipation		P_D	100	mW
	Diode power dissipation derating (Ta ≥53°C)		ΔP_D /°C	-1.4	mW/°C
	Junction temperature		T_j	125	°C
Detector	Off-state output terminal voltage		V_{DRM}	600	V
	On-state RMS current	Ta = 25°C	$I_{T(RMS)}$	70	mA
		Ta = 70°C		40	
	On-state current derating (Ta ≥ 25°C)		ΔI_T/°C	−0.67	mA/°C
	Peak on-state current (100 μs pulse, 120 pps)		I_{TP}	2	A
	Peak non-repetitive surge current (P_W = 10 ms)		I_{TSM}	1.2	A
	Output power dissipation		P_O	200	mW
	Output power dissipation derating (Ta ≥ 25°C)		ΔP_O / °C	−2.0	mW / °C
	Junction temperature		T_j	115	°C
Storage temperature range			T_{stg}	−55 to 125	°C
Operating temperature range			T_{opr}	−40 to 100	°C
Lead soldering temperature (10 s)			T_{sol}	260	°C
Isolation voltage (AC, 60 s, R.H. ≤ 60 %) (Note)			BV_S	2500	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/“Derating Concept and Methods”) and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Note: Device considered a two terminal device: Pins 1 and 3 shorted together and pins 4 and 6 shorted together.

Recommended Operating Conditions

Characteristics	Symbol	Min	Typ.	Max	Unit
Supply voltage	V_{AC}	—	—	240	Vac
Forward current	I_F	15	20	25	mA
Peak on-state current	I_{TP}	—	—	1	A
Operating temperature	T_{opr}	−25	—	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Individual Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
LED	Forward voltage	V_F	I_F = 10 mA	1.0	1.15	1.3	V
	Reverse current	I_R	V_R = 5 V	—	—	10	μA
	Capacitance	C_T	VF = 0 V, f = 1 MHz	—	30	—	pF
Detector	Peak off-state current	I_{DRM}	V_{DRM} = 600 V	—	10	1000	nA
	Peak on-state voltage	V_{TM}	I_{TM} = 70 mA	—	1.7	2.8	V
	Holding current	I_H	—	—	0.6	—	mA
	Critical rate of rise of off-state voltage	dv/dt	V_{in} = 240 Vrms, Ta = 85 °C (Figure 1)	200	500	—	V/μs
	Critical rate of rise of commutating voltage	dv/dt(c)	V_{in} = 60 Vrms, I_T = 15 mA (Figure 1)	—	0.2	—	V/μs

Coupled Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Trigger LED current	I_{FT}	V_T = 3 V	—	5	10	mA
Inhibit voltage	V_{IH}	I_F = Rated I_{FT}	—	—	50	V
Leakage in inhibited state	I_{IH}	I_F = Rated I_{FT}, V_T = Rated V_{DRM}	—	200	600	μA
Capacitance (input to output)	C_S	V_S = 0 V, f = 1 MHz	—	0.8	—	pF
Isolation resistance	R_S	V_S = 500 V, R.H. ≤ 60 %	1×10^{12}	10^{14}	—	Ω
Isolation voltage	BV_S	AC, 60 s	2500	—	—	Vrms

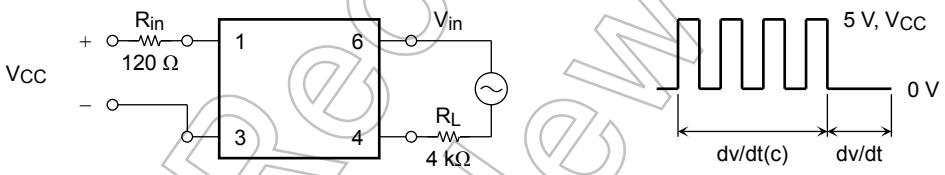
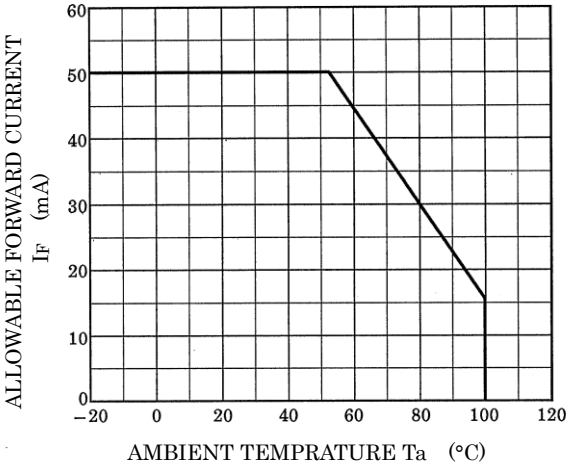
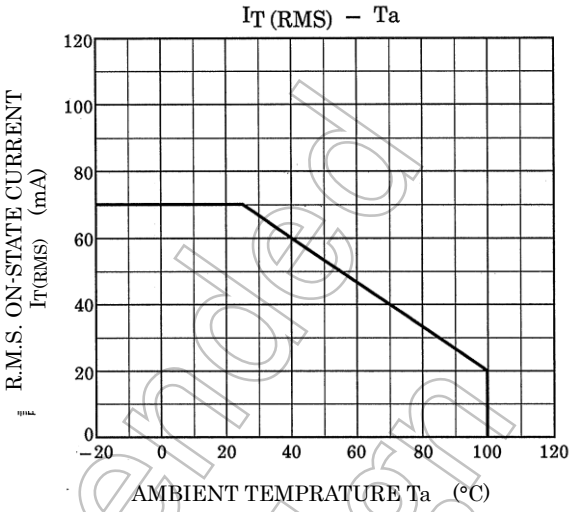


Figure 1 dv/dt Test Circuit

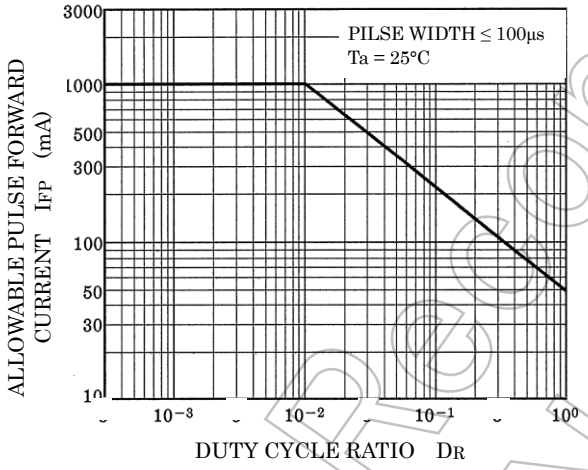
I_F – Ta



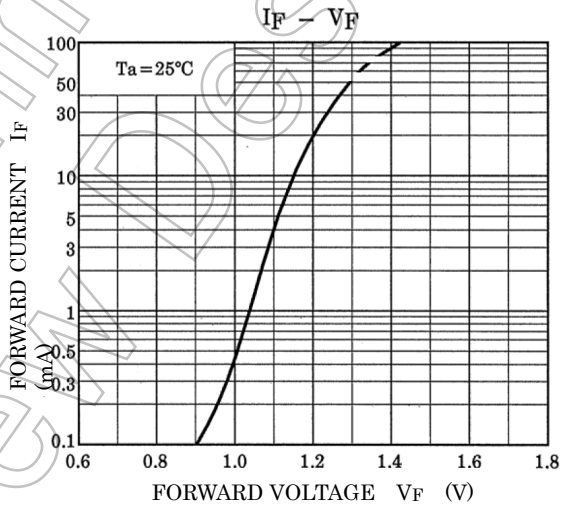
$I_{T\,(RMS)}$ – Ta



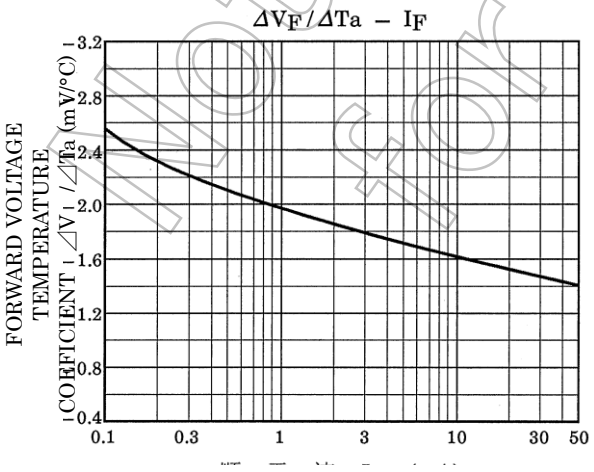
I_{FP} – D_R



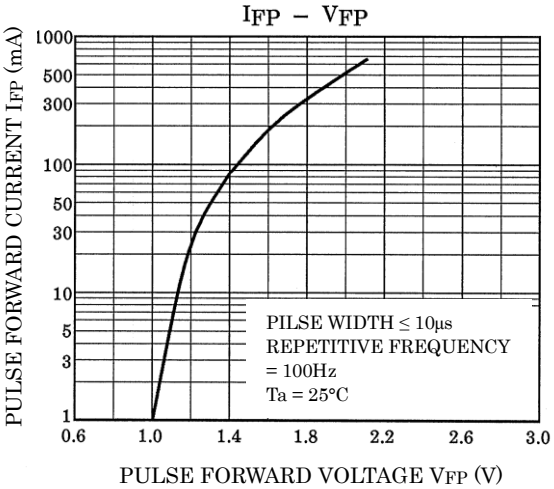
I_F – V_F



$\Delta V_F / \Delta Ta$ – I_F

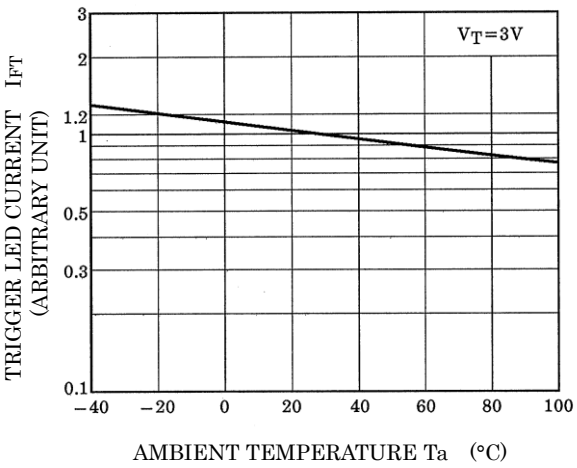


I_{FP} – V_{FP}

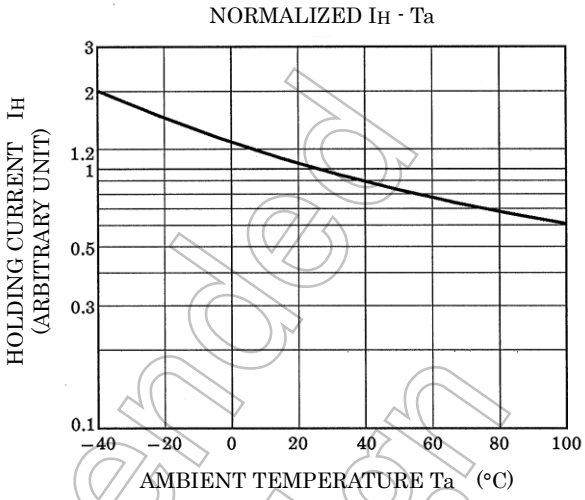


NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

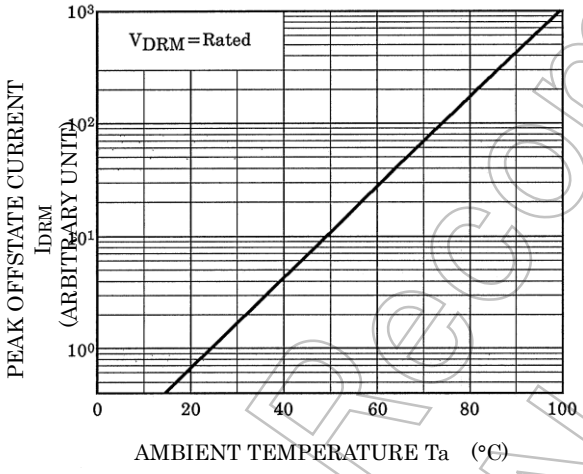
NORMALIZED I_{FT} - Ta



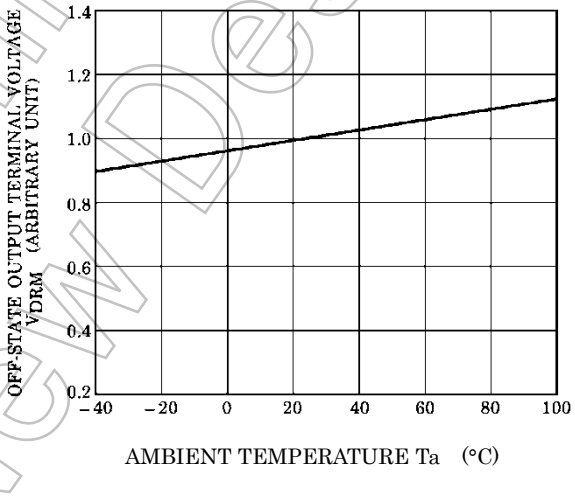
NORMALIZED I_H - Ta



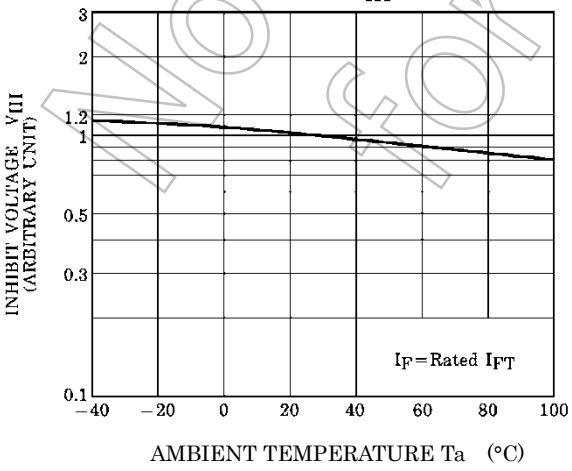
NORMALIZED I_{DRM} - Ta



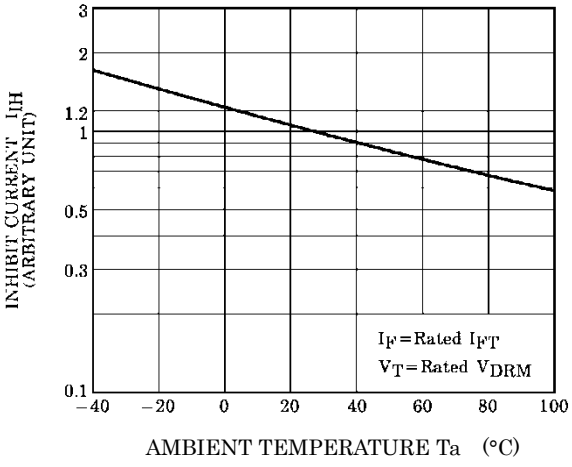
NORMALIZED V_{DRM} – Ta



NORMALIZED V_{III} – Ta



NORMALIZED I_{III} – Ta



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