

1 Electrical ratings

Table 1. Absolute maximum ratings

Cumbal	Payamatay	Value		I I m i 4
Symbol	Parameter	D²PAK, TO-220	TO-220FP	Unit
V_{GS}	Gate-source voltage	±30		V
	Drain current (continuous) at T _C = 25 °C	12	12 ⁽¹⁾	^
I _D	Drain current (continuous) at T _C = 100 °C	7.5	7.5 ⁽¹⁾	Α
I _{DM} ⁽²⁾	Drain current pulsed	48	48(1)	Α
P _{TOT}	Total power dissipation at T _C = 25 °C	160	35	W
V _{ISO}	Insulation withstand voltage (RMS) from all three leads to external heat sink (t = 1 s, T_C = 25 °C)		2.5	kV
dv/dt ⁽³⁾	Peak diode recovery voltage slope	15		V/ns
T _J	Operating junction temperature range		°C	
T _{stg}	Storage temperature range	-65 to 150		°C

- 1. Limited by maximum junction temperature.
- 2. Pulse width limited by safe operating area.
- 3. $I_{SD} \le$ 12 A, $di/dt \le$ 400 A/ μ s, $V_{DD} =$ 80% $V_{(BR)DSS}$.

Table 2. Thermal data

Symbol	Parameter	Value D ² PAK TO-220 TO-2			Unit
Syllibol	Faranietei			TO-220FP	Offic
R _{thj-case}	Thermal resistance junction-case	2.78 3.57		3.57	°C/W
R _{thj-a}	Thermal resistance junction-ambient	62.5			°C/W
R _{thj-pcb} ⁽¹⁾	Thermal resistance junction-pcb	35			°C/W

^{1.} When mounted on an 1-inch² FR-4, 2 Oz copper board.

Table 3. Avalanche characteristics

Symbol	Parameter	Value	Unit
I _{AS}	Avalanche current, repetitive or not repetitive (pulse width limited by T_J max)	6	А
E _{AS}	Single-pulse avalanche energy (starting $T_J = 25$ °C, $I_D = I_{AS}$, $V_{DD} = 50$ V)	400	mJ

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2 Electrical characteristics

(T_C = 25 °C unless otherwise specified)

Table 4. Static

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V _{(BR)DSS}	Drain-source breakdown voltage	V _{GS} = 0 V, I _D = 250 μA	500			V
1	Zono moto vielto no duoje ovimont	V _{GS} = 0 V, V _{DS} = 500 V			1	
I _{DSS}	Zero gate voltage drain current	V_{GS} = 0 V, V_{DS} = 500 V, T_{C} = 125 °C ⁽¹⁾			10	μA
I _{GSS}	Gate-body leakage current	V _{DS} = 0 V, V _{GS} = ±30 V			±100	nA
V _{GS(th)}	Gate threshold voltage	$V_{DS} = V_{GS}$, $I_D = 50 \mu A$	3	4	5	V
R _{DS(on)}	Static drain-source on-resistance	V _{GS} = 10 V, I _D = 6 A		300	350	mΩ

^{1.} Defined by design, not subject to production test.

Table 5. Dynamic

Symbol	Parameter	neter Test conditions		Тур.	Max.	Unit
C _{iss}	Input capacitance		-	1000	-	pF
C _{oss}	Output capacitance	V_{DS} = 25 V, f = 1 MHz, V_{GS} = 0 V	-	250	-	pF
C _{rss}	Reverse transfer capacitance		-	20	-	pF
Coss eq. (1)	Equivalent output capacitance	V _{DS} = 0 to 400 V, V _{GS} = 0 V	-	90	-	pF
t _{d(on)}	Turn-on Delay Time	V _{DD} = 250 V, I _D = 6 A,	-	20	-	ns
t _r	Rise Time	R_G = 4.7 Ω , V_{GS} = 10 V (see Figure 13. Test circuit for resistive load switching times and Figure 18. Switching time waveform)	-	10	-	ns
Qg	Total gate charge	V _{DD} = 400 V, I _D = 12 A, V _{GS} = 0 to 10 V	-	28	-	nC
Q _{gs}	Gate-source charge	(see Figure 14. Test circuit for gate	-	8	-	nC
Q _{gd}	Gate-drain charge	charge behavior)	-	16	-	nC
R _g	Gate input resistance	f = 1 MHz, gate DC Bias = 0, test signal level = 20 mV, open drain	-	1.6	-	Ω

^{1.} $C_{\text{oss eq.}}$ is defined as a constant equivalent capacitance giving the same charging time as C_{oss} when V_{DS} increases from 0 to 80% V_{DSS} .

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Table 6. Source-drain diode

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I _{SD}	Source-drain current		-		12	Α
I _{SDM} ⁽¹⁾	Source-drain current (pulsed)		-		48	Α
V _{SD} (2)	Forward on voltage	V _{GS} = 0 V, I _{SD} = 12 A	-		1.5	V
t _{rr}	Reverse recovery time	I _{SD} = 12 A, di/dt = 100 A/μs,	-	270		ns
Q _{rr}	Reverse recovery charge	V _{DD} = 100 V	-	2.23		μC
I _{RRM}	Reverse recovery current	(see Figure 15. Test circuit for inductive load switching and diode recovery times)	-	16.5		Α
t _{rr}	Reverse recovery time	I _{SD} = 12 A, di/dt = 100 A/μs,	-	340		ns
Q _{rr}	Reverse recovery charge	V _{DD} = 100 V, T _J = 150 °C	-	3		μC
I _{RRM}	Reverse recovery current	(see Figure 15. Test circuit for inductive load switching and diode recovery times)	-	18		Α

^{1.} Pulse width is limited by safe operating area.

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^{2.} Pulse test: pulse duration = 300 μ s, duty cycle 1.5%.



2.1 Electrical characteristics (curves)

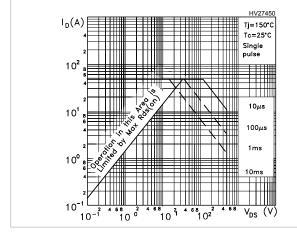
10 °

Figure 3. Safe operating area for TO-220FP

10 1 68 102

10ms

V_{DS} (V)



K GC20521 0.2 0.05 0.005 0.002 0.01Single pulse 0.05 0.02 0.01 0.05 0.02 0.01 0.05 0.02

Figure 4. Thermal impedance for TO-220FP

Figure 5. Output characteristics

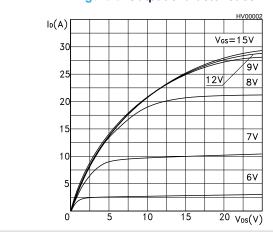
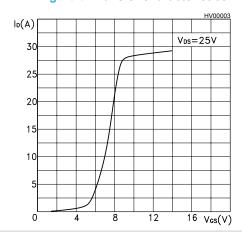


Figure 6. Transfer characteristics



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Figure 7. Static drain-source on-resistance

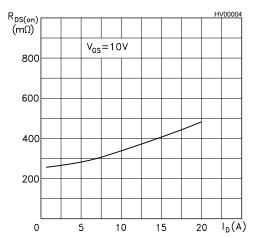


Figure 8. Gate charge vs gate-source voltage

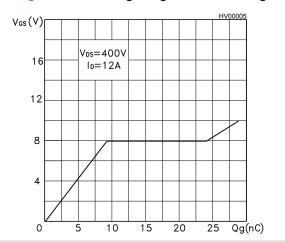


Figure 9. Capacitance variations

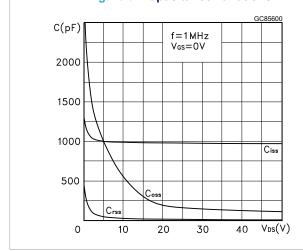


Figure 10. Normalized gate threshold voltage vs temperature

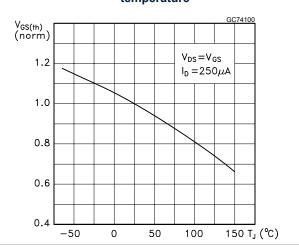


Figure 11. Normalized on resistance vs temperature

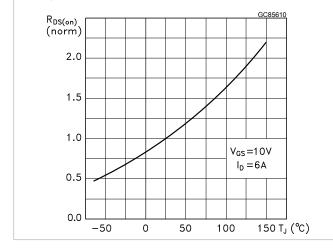
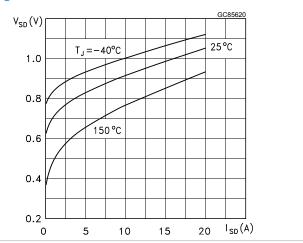


Figure 12. Source-drain diode forward characteristics



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3 Test circuits

Figure 13. Test circuit for resistive load switching times

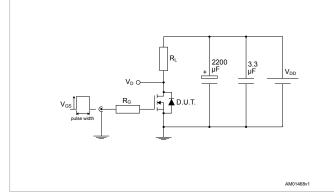


Figure 14. Test circuit for gate charge behavior

Figure 15. Test circuit for inductive load switching and diode recovery times

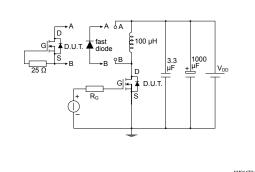


Figure 16. Unclamped inductive load test circuit

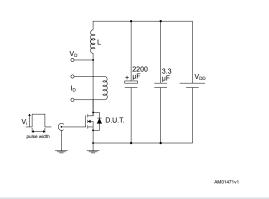


Figure 17. Unclamped inductive waveform

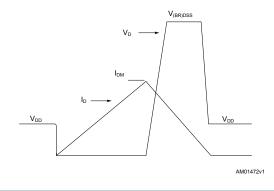
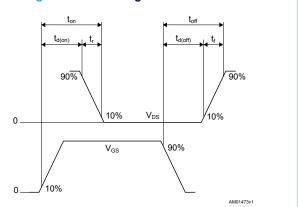


Figure 18. Switching time waveform



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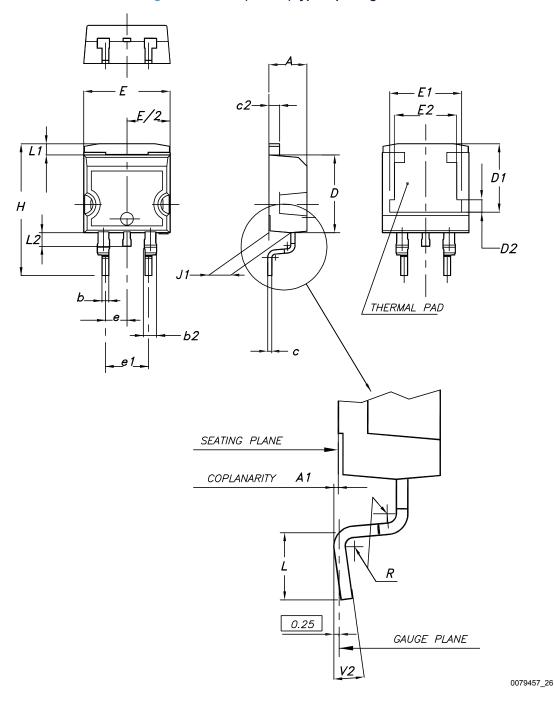


4 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK packages, depending on their level of environmental compliance. ECOPACK specifications, grade definitions and product status are available at: www.st.com. ECOPACK is an ST trademark.

4.1 D²PAK (TO-263) type A package information

Figure 19. D²PAK (TO-263) type A package outline



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Table 7. D²PAK (TO-263) type A package mechanical data

Dim.		mm	
Dim.	Min.	Тур.	Max.
А	4.40		4.60
A1	0.03		0.23
b	0.70		0.93
b2	1.14		1.70
С	0.45		0.60
c2	1.23		1.36
D	8.95		9.35
D1	7.50	7.75	8.00
D2	1.10	1.30	1.50
E	10.00		10.40
E1	8.30	8.50	8.70
E2	6.85	7.05	7.25
е		2.54	
e1	4.88		5.28
Н	15.00		15.85
J1	2.49		2.69
L	2.29		2.79
L1	1.27		1.40
L2	1.30		1.75
R		0.40	
V2	0°		8°

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9.75 16.90 1.60 2.54 5.08

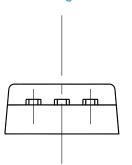
Figure 20. D²PAK (TO-263) recommended footprint (dimensions are in mm)

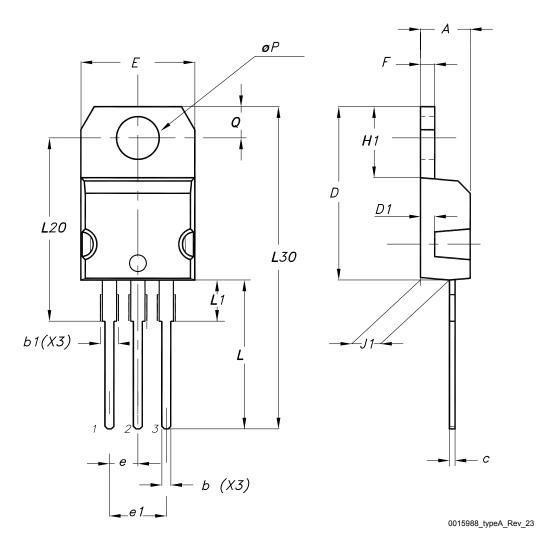
Footprint_26



4.2 TO-220 type A package information

Figure 21. TO-220 type A package outline





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Table 8. TO-220 type A package mechanical data

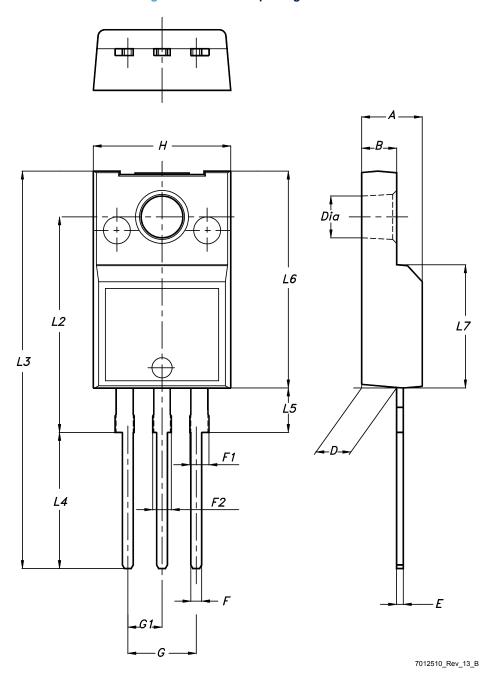
Dim		mm	
Dim.	Min.	Тур.	Max.
A	4.40		4.60
b	0.61		0.88
b1	1.14		1.55
С	0.48		0.70
D	15.25		15.75
D1		1.27	
E	10.00		10.40
е	2.40		2.70
e1	4.95		5.15
F	1.23		1.32
H1	6.20		6.60
J1	2.40		2.72
L	13.00		14.00
L1	3.50		3.93
L20		16.40	
L30		28.90	
ØΡ	3.75		3.85
Q	2.65		2.95
Slug flatness		0.03	0.10

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4.3 TO-220FP package information

Figure 22. TO-220FP package outline



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Table 9. TO-220FP package mechanical data

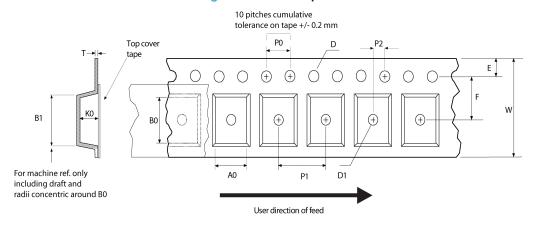
Dim.		mm				
Dilli.	Min.	Тур.	Max.			
Α	4.40		4.60			
В	2.50		2.70			
D	2.50		2.75			
Е	0.45		0.70			
F	0.75		1.00			
F1	1.15		1.70			
F2	1.15		1.70			
G	4.95		5.20			
G1	2.40		2.70			
Н	10.00		10.40			
L2		16.00				
L3	28.60		30.60			
L4	9.80		10.60			
L5	2.90		3.60			
L6	15.90		16.40			
L7	9.00		9.30			
Dia	3.00		3.20			

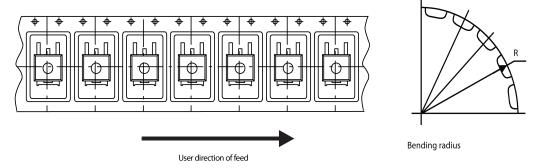
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4.4 D²PAK packing information

Figure 23. D²PAK tape outline



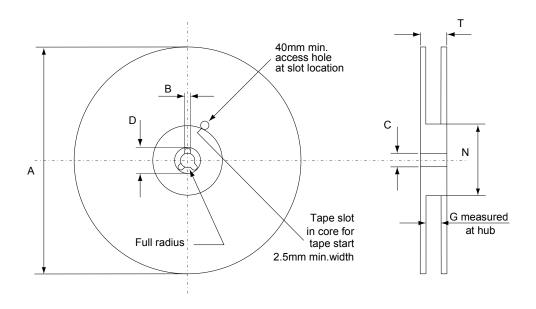


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Figure 24. D²PAK reel outline



AM06038v1

Table 10. D²PAK tape and reel mechanical data

Tape			Reel				
Dim.	n	ım	Dim.	Dim mm		mm	m
Dim.	Min.	Max.	D iiii.	Min.	Max.		
A0	10.5	10.7	Α		330		
В0	15.7	15.9	В	1.5			
D	1.5	1.6	С	12.8	13.2		
D1	1.59	1.61	D	20.2			
E	1.65	1.85	G	24.4	26.4		
F	11.4	11.6	N	100			
K0	4.8	5.0	Т		30.4		
P0	3.9	4.1					
P1	11.9	12.1	Base	quantity	1000		
P2	1.9	2.1	Bulk	quantity	1000		
R	50						
Т	0.25	0.35					
W	23.7	24.3					

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5 Ordering information

Table 11. Order codes

Order codes	Marking	Package	Packing
STB12NM50T4	B12NM50	D²PAK	Tape and reel
STP12NM50	P12NM50	TO-220	Tube
STP12NM50FP	P12NM50FP	TO-220FP	Tube

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Revision history

Table 12. Document revision history

Date	Revision	Changes
14-Mar-2004	8	Preliminary version
15-Feb-2006	9	New voltage value on first page at tjmax.
05-Apr-2006	10	Inserted ecopack indication
27-Jul-2006	11	New template, no content change
		The part number STB12NM50-1 have been moved to a separate datasheet and the document has been updated accordingly.
		Updated cover page.
22-Oct-2020	12	Updated Section 1 Electrical ratings and Section 2 Electrical characteristics.
		Added Section Section 5 Ordering information.
		Minor text changes.

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