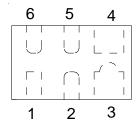
# PROTECTION PRODUCTS - RailClamp®

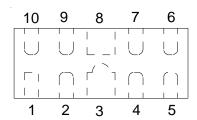
Pin Identification and Configuration

# RClamp0522P



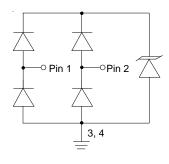
Pin	Identification
1 - 2	Input Lines
5 - 6	Output Lines (No Internal Connection)
3 - 4	Ground

# RClamp0524PA



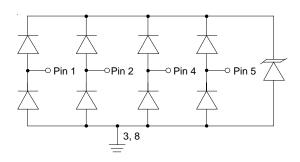
Pin	Identification
1, 2, 4, 5	Input Lines
6, 7, 9, 10	Output Lines (No Internal Connection)
3, 8	Ground

#### **SLP1610P4** Pin Configuration (Top View)



**Circuit Diagram** 

#### **SLP2510P8 Pin Configuration (Top View)**



**Circuit Diagram** 

# Ordering Information

Part Number	Number of Lines	Qty per Reel	Reel Size
RClamp0522P.TCT	2	3000	7 Inch
RClamp0524PATCT	4	3000	7 Inch

RailClamp and RClamp are marks of Semtech Corporation.



# Absolute Maximum Rating

Rating	Symbol	Value	Units
Peak Pulse Power (tp = 8/20μs)	$P_{pk}$	150	Watts
Peak Pulse Current (tp = 8/20μs)	I <sub>PP</sub>	5	А
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	V <sub>ESD</sub>	+/- 17 +/- 12	kV
Operating Temperature	T <sub>J</sub>	-55 to +125	°C
Storage Temperature	T <sub>STG</sub>	-55 to +150	°C

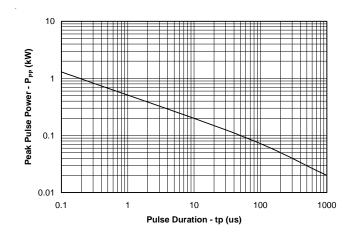
# Electrical Characteristics (T=25°C)

Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
Reverse Stand-Off Voltage	V <sub>RWM</sub>	Any I/O pin to ground			5	V
Reverse Breakdown Voltage	V <sub>BR</sub>	I <sub>t</sub> = 1mA Any I/O pin to ground	6			V
Reverse Leakage Current	I <sub>R</sub>	V <sub>RWM</sub> = 5V, T=25°C Any I/O pin to ground			1	μΑ
Clamping Voltage	V <sub>c</sub>	I <sub>PP</sub> = 1A, tp = 8/20µs Any I/O pin to ground			15	V
Junction Capacitance	C <sub>j</sub>	V <sub>R</sub> = 0V, f = 1MHz Between I/O pins		0.30	0.4	pF
Junction Capacitance	C <sub>j</sub>	V <sub>R</sub> = OV, f = 1MHz Any I/O pin to ground			0.8	pF

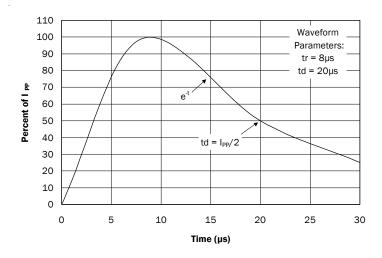


# Typical Characteristics

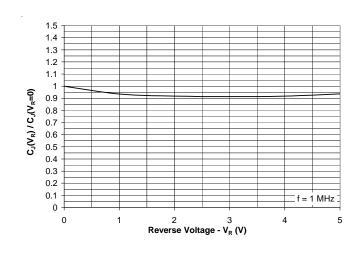
#### Non-Repetitive Peak Pulse Power vs. Pulse Time



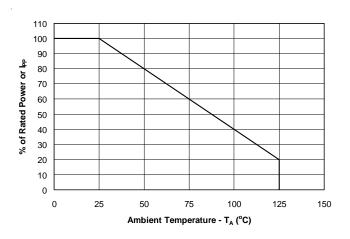
#### **Pulse Waveform**



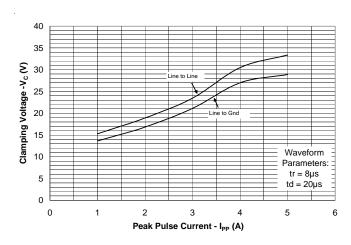
#### Normalized Capacitance vs. Reverse Voltage



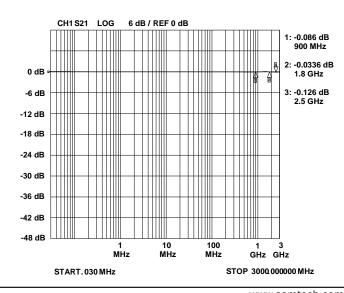
#### **Power Derating Curve**



# Clamping Voltage vs. Peak Pulse Current (Between any I/O and Ground)



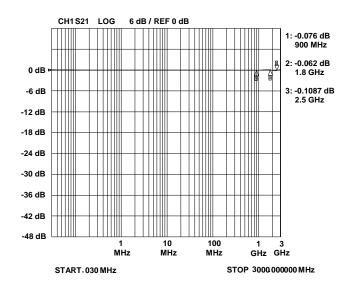
#### Insertion Loss S21 - I/O to GND





# Typical Characteristics (Con't)

Insertion Loss S21 - I/O to I/O





## **Applications Information**

#### **Design Recommendations for HDMI Protection**

Adding external ESD protection to HDMI ports can be challenging. First, ESD protection devices have an inherent junction capacitance. Furthermore, adding even a small amount of capacitance will cause the impedance of the differential pair to drop. Second, large packages and land pattern requirements cause discontinuities that adversely affect signal integrity. The RClamp0524PA and RClamp0522P are specifically designed for protection of high-speed interfaces such as HDMI. They present <0.4pF capacitance between the pairs while being rated to handle >±8kV ESD contact discharges (>±15kV air discharge) as outlined in IEC 61000-4-2. Each device is in a leadless SLP package that is less than 1.1mm wide. They are designed such that the traces flow straight through the device. The narrow package and flow-through design reduces discontinuities and minimizes impact on signal integrity. This becomes even more critical as signal speeds increase.

#### **Pin Configuration**

Figure 1 is an example of how to route the high speed differential traces through the RClamp0524PA. The solid line represents the PCB trace. The PCB traces are used to connect the pin pairs for each line (pin 1 to pin 10, pin 2 to pin 9, pin 4 to pin 7, pin 5 to pin 6). For example, line 1 enters at pin 1 and exits at Pin 10 and the PCB trace connects pin 1 and 10 together. This is true for lines connected at pins 2, 4, and 5 also. Ground is connected at pins 3 and 8. One large ground pad should be used in lieu of two separate pads. The same layout rules apply for the RClamp0522P.

#### **TDR Measurements for HDMI**

The combination of low capacitance, small package, and flow-through design means it is possible to use these devices to meet the HDMI impedance requirements of 100 Ohms ±15% without any PCB board modification. Figure 3 shows a typical impedance test result for a TDR risetime of 200ps using a Semtech evaluation board with 100 Ohm traces throughout. Measurements were taken using a TDR method as outlined in the HDMI Compliance Test Specification (CTS). In each

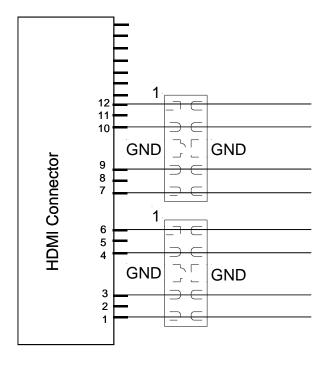


Figure 1. Flow through Layout Using RClamp0524PA



#### **Applications Information**

case, the device meets the HDMI CTS requirement of 100 0hm ±15% with plenty of margin.

Figure 3 shows a typical HDMI 1.3 eye pattern at 1080p resolution. As shown there are no violations of the eye pattern with RClamp0524PA in the circuit. The RClamp0524PA may also be used to protect the remaining lines (I2C, CEC, and hot plug). Figure 4 shows the complete protection solution.

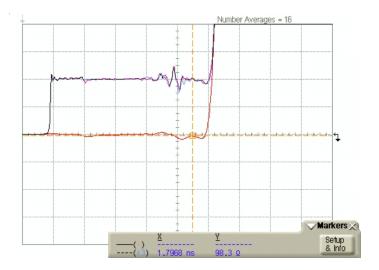


Good circuit board layout is critical not only for signal integrity, but also for effective suppression of ESD induced transients. For optimum ESD protection, the following guidelines are recommended:

- Place the device as close to the connector as possible. This practice restricts ESD coupling into adjacent traces and reduces parasitic inductance.
- The ESD transient return path to ground should be kept as short as possible. Whenever possible, use multiple micro vias connected directly from the device ground pad to the ground plane.
- Avoid running critical signals near board edges.

# Protecting MDDI Ports with RClamp0524PA and RClamp0522P

The small package size and low capacitance of the RClamp0522P and RClamp0524PA make them ideal for high-speed lines in portable applications. One such application is the protection of MDDI ports in cellular phones. MDDI is a serial data interface operating at 480Mb/s per line pair. The lines are scalable for increased speed and display resolution. A MDDI port protection example is shown in Figure 5. The RClamp0524PA is used to protect two differential line pairs while an RClamp0522P is used to protect the MDDI strobe lines. Note that devices are used on both the main board and LCD side of the MDDI port. Devices on the main board are needed to protect the MDDI controller in the Baseband processor and devices on the LCD board to protect MDDI controller in LCD module. A single line TVS such as the uClamp0501P is used to protect the MDDI power line. The protection devices should be placed close to the connector of each board. Traces are routed directly through each device, minimizing parasitic inductance. Connections to the ground plane should be made with multiple micro vias when possible.



X-axis	1.79	(nsec)
Y-axis	98.3	(Ohm)

Figure 2 - TDR Measurement with 200ps risetime using Semtech Evaluation Board

Note: Measurements were taken on SLP HDMI EVAL Rev C Board that has  $100\Omega$  differential traces impedance throughout (No trace Compensation).

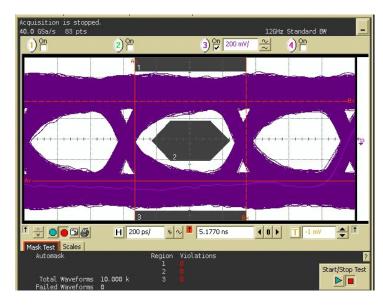
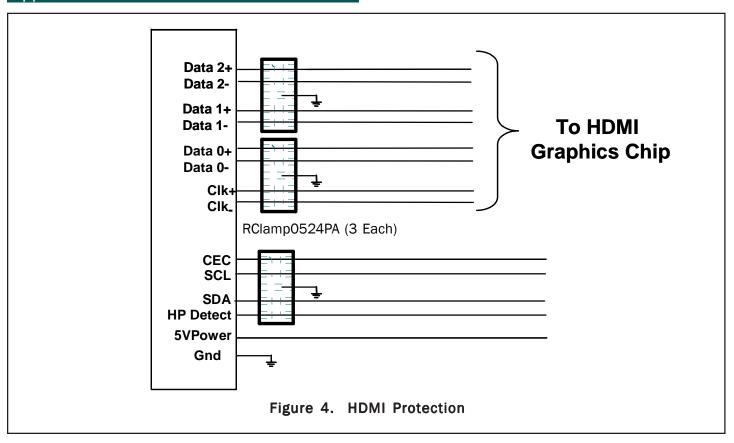
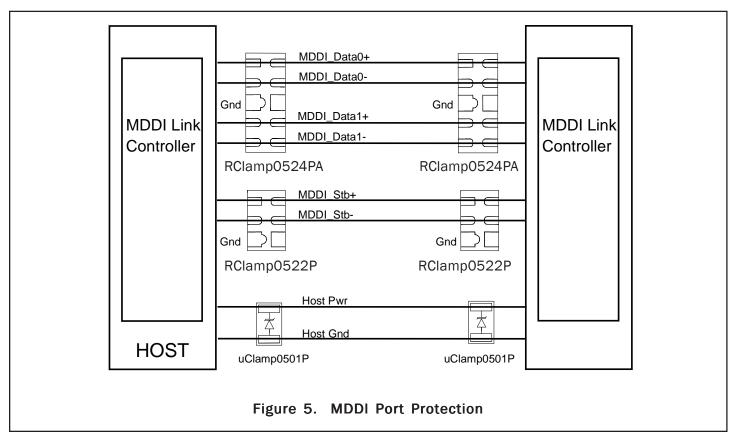


Figure 3 - Typical HDMI 1.3 Eye Pattern (1080p) with RClamp0524PA



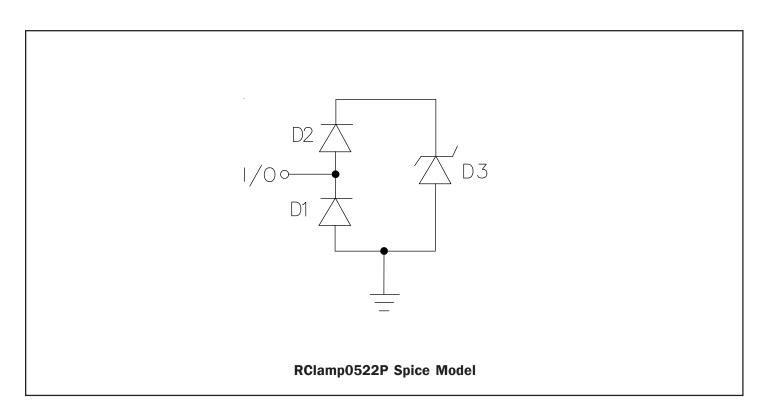
# **Applications Information**







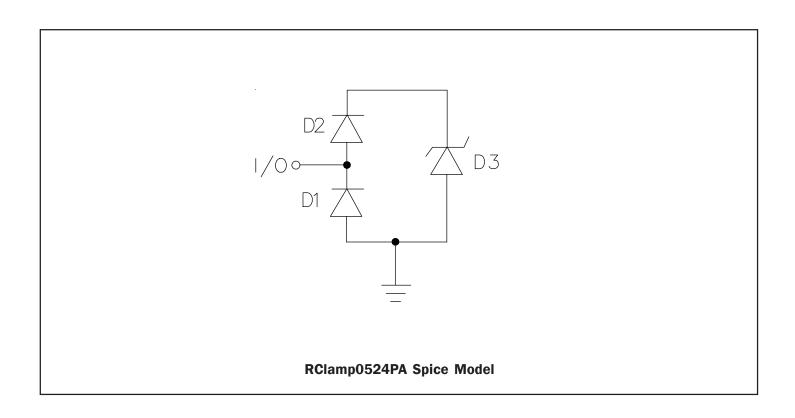
# Applications Information Spice Model



	RClamp0522P Spice Parameters								
Parameter	Unit	D1	D2	D3					
IS	Amp	1E-20	1E-20	2E-12					
BV	Volt	100	100	8.2					
VJ	VJ Volt RS Ohm		VJ Volt	0.83	0.83	0.6			
RS			0.6	0.2					
IBV	Amp	1E-3	1E-3	1E-3					
CJO	CJO Farad		0.25E-12	56E-12					
TT sec		sec 2.541E-9 2.5	2.541E-9	2.541E-9					
М	М		- 0.01		0.01	0.23			
N	N		1.1	1.1					
EG	eV	1.11	1.11	1.11					



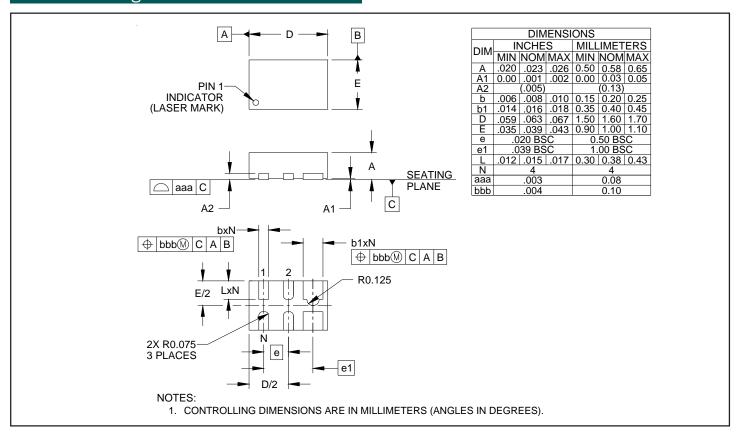
# Applications Information Spice Model



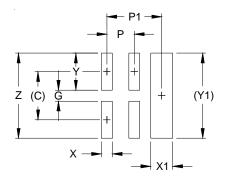
	RClamp0524PA Spice Parameters								
Parameter	Unit	D1	D2	D3					
IS	Amp	1E-20	1E-20	2E-12					
BV	Volt	100	100	8.2					
٧J	Volt	0.83	0.83	0.6					
RS	RS Ohm  IBV Amp  CJO Farad		0.6	0.2					
IBV			Amp 1E-3	1E-3	1E-3	1E-3			
CJO			0.25E-12	56E-12					
TT sec M		sec 2.541E-9	2.541E-9	2.541E-9					
		0.01	0.01	0.23					
N	N		1.1	1.1					
EG	eV	1.11	1.11	1.11					



## Outline Drawing - SLP1610P4



#### Land Pattern - SLP1610P4



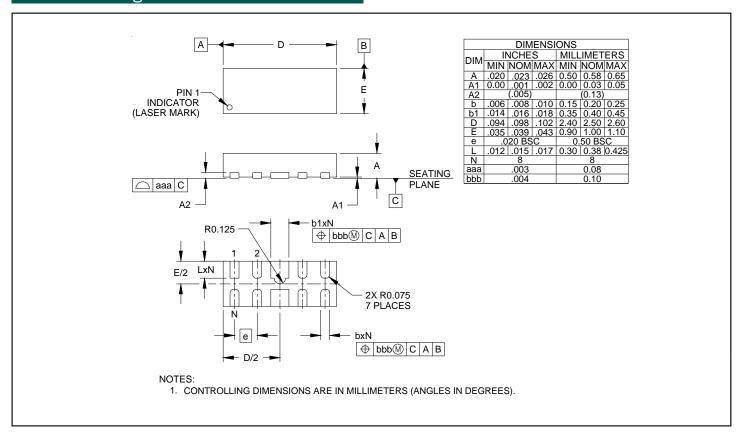
	DIMENS	SIONS
DIM	INCHES	MILLIMETERS
С	(.034)	(0.87)
G	.007	0.19
Р	.020	0.50
P1	.039	1.00
Χ	.008	0.20
X1	.016	0.40
Υ	.027	0.68
Y1	(.061)	(1.55)
Z	.061	1.55

#### NOTES:

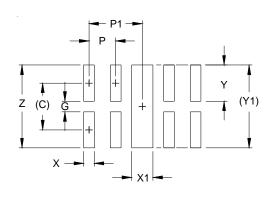
- 1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).
- THIS LAND PATTERN IS FOR REFERENCE PURPOSES ONLY.
   CONSULT YOUR MANUFACTURING GROUP TO ENSURE YOUR
   COMPANY'S MANUFACTURING GUIDELINES ARE MET.



# Outline Drawing - SLP2510P8



## Land Pattern - SLP2510P8



	DIMENSIONS							
DIM	INCHES	MILLIMETERS						
С	(.034)	(0.875)						
G	.008	0.20						
Р	.020	0.50						
P1	.039 1.00							
X	.008	0.20						
X1	.016	0.40						
Υ	.027	0.675						
Y1	(.061)	(1.55)						
Z	.061	1.55						

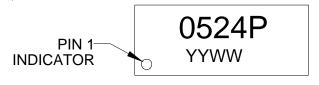
#### NOTES:

- 1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).
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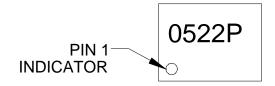


## Marking Codes

#### RClamp0524PA

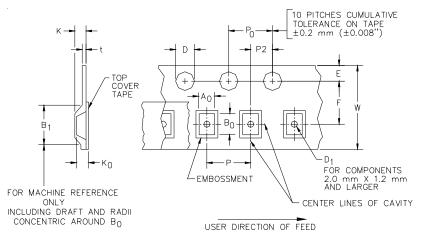


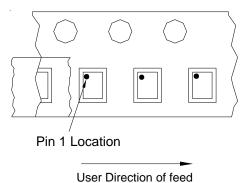
#### RClamp0522P



YYWW = Date Code

# Tape and Reel Specification





**Device Orientation in Tape** 

Part Number A0		В0	ко		
RClamp0522P 1.30 +/-0.05 mm		1.75 +/-0.05 mm	0.70 +/-0.05 mm		
RClamp0524PA 1.23 +/-0.05 mm		2.70 +/-0.05 mm	0.70 +/-0.05 mm		

Tape Width	B, (Max)	D	D1	E	F	K (MAX)	Р	PO	P2	T(MAX)	W
8 mm	4.2 mm	1.5 + 0.1 mm - 0.0 mm )	0.5 mm ±0.05	1.750±.10 mm	3.5±0.05 mm	2.4 mm	4.0±0.1 mm	4.0±0.1 mm	2.0±0.05 mm	0.4 mm	8.0 mm + 0.3 mm - 0.1 mm

# Contact Information for Semtech International AG

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Shanghai Office	Tel: 86-21-6391-0830 Fax: 86-21-6391-0831	Semtech France SARL	Tel: 33-(0)169-28-22-00 Fax: 33-(0)169-28-12-98
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