

PZT2222AT1

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector-Emitter Breakdown Voltage ($I_C = 10\text{ mAdc}$, $I_B = 0$)	$V_{(BR)CEO}$	40	–	Vdc
Collector-Base Breakdown Voltage ($I_C = 10\text{ }\mu\text{Adc}$, $I_E = 0$)	$V_{(BR)CBO}$	75	–	Vdc
Emitter-Base Breakdown Voltage ($I_E = 10\text{ }\mu\text{Adc}$, $I_C = 0$)	$V_{(BR)EBO}$	6.0	–	Vdc
Base-Emitter Cutoff Current ($V_{CE} = 60\text{ Vdc}$, $V_{BE} = -3.0\text{ Vdc}$)	I_{BEX}	–	20	nAdc
Collector-Emitter Cutoff Current ($V_{CE} = 60\text{ Vdc}$, $V_{BE} = -3.0\text{ Vdc}$)	I_{CEX}	–	10	nAdc
Emitter-Base Cutoff Current ($V_{EB} = 3.0\text{ Vdc}$, $I_C = 0$)	I_{EBO}	–	100	nAdc
Collector-Base Cutoff Current ($V_{CB} = 60\text{ Vdc}$, $I_E = 0$) ($V_{CB} = 60\text{ Vdc}$, $I_E = 0$, $T_A = 125^\circ\text{C}$)	I_{CBO}	– –	10 10	nAdc μAdc

ON CHARACTERISTICS

DC Current Gain ($I_C = 0.1\text{ mAdc}$, $V_{CE} = 10\text{ Vdc}$) ($I_C = 1.0\text{ mAdc}$, $V_{CE} = 10\text{ Vdc}$) ($I_C = 10\text{ mAdc}$, $V_{CE} = 10\text{ Vdc}$) ($I_C = 10\text{ mAdc}$, $V_{CE} = 10\text{ Vdc}$, $T_A = -55^\circ\text{C}$) ($I_C = 150\text{ mAdc}$, $V_{CE} = 10\text{ Vdc}$) ($I_C = 150\text{ mAdc}$, $V_{CE} = 1.0\text{ Vdc}$) ($I_C = 500\text{ mAdc}$, $V_{CE} = 10\text{ Vdc}$)	h_{FE}	35 50 70 35 100 50 40	– – – – 300 – –	–
Collector-Emitter Saturation Voltages ($I_C = 150\text{ mAdc}$, $I_B = 15\text{ mAdc}$) ($I_C = 500\text{ mAdc}$, $I_B = 50\text{ mAdc}$)	$V_{CE(sat)}$	– –	0.3 1.0	Vdc
Base-Emitter Saturation Voltages ($I_C = 150\text{ mAdc}$, $I_B = 15\text{ mAdc}$) ($I_C = 500\text{ mAdc}$, $I_B = 50\text{ mAdc}$)	$V_{BE(sat)}$	0.6 –	1.2 2.0	Vdc
Input Impedance ($V_{CE} = 10\text{ Vdc}$, $I_C = 1.0\text{ mAdc}$, $f = 1.0\text{ kHz}$) ($V_{CE} = 10\text{ Vdc}$, $I_C = 10\text{ mAdc}$, $f = 1.0\text{ kHz}$)	h_{ie}	2.0 0.25	8.0 1.25	k Ω
Voltage Feedback Ratio ($V_{CE} = 10\text{ Vdc}$, $I_C = 1.0\text{ mAdc}$, $f = 1.0\text{ kHz}$) ($V_{CE} = 10\text{ Vdc}$, $I_C = 10\text{ mAdc}$, $f = 1.0\text{ kHz}$)	h_{re}	– –	8.0×10^{-4} 4.0×10^{-4}	–
Small-Signal Current Gain ($V_{CE} = 10\text{ Vdc}$, $I_C = 1.0\text{ mAdc}$, $f = 1.0\text{ kHz}$) ($V_{CE} = 10\text{ Vdc}$, $I_C = 10\text{ mAdc}$, $f = 1.0\text{ kHz}$)	$ h_{fe} $	50 75	300 375	–
Output Admittance ($V_{CE} = 10\text{ Vdc}$, $I_C = 1.0\text{ mAdc}$, $f = 1.0\text{ kHz}$) ($V_{CE} = 10\text{ Vdc}$, $I_C = 10\text{ mAdc}$, $f = 1.0\text{ kHz}$)	h_{oe}	5.0 25	35 200	μmhos
Noise Figure ($V_{CE} = 10\text{ Vdc}$, $I_C = 100\text{ }\mu\text{Adc}$, $f = 1.0\text{ kHz}$)	F	–	4.0	dB

DYNAMIC CHARACTERISTICS

Current-Gain – Bandwidth Product ($I_C = 20\text{ mAdc}$, $V_{CE} = 20\text{ Vdc}$, $f = 100\text{ MHz}$)	f_T	300	–	MHz
Output Capacitance ($V_{CB} = 10\text{ Vdc}$, $I_E = 0$, $f = 1.0\text{ MHz}$)	C_c	–	8.0	pF
Input Capacitance ($V_{EB} = 0.5\text{ Vdc}$, $I_C = 0$, $f = 1.0\text{ MHz}$)	C_e	–	25	pF

SWITCHING TIMES ($T_A = 25^\circ\text{C}$)

Delay Time	$(V_{CC} = 30\text{ Vdc}$, $I_C = 150\text{ mAdc}$, $I_{B(on)} = 15\text{ mAdc}$, $V_{EB(off)} = 0.5\text{ Vdc}$) Figure 1	t_d	–	10	ns
Rise Time		t_r	–	25	
Storage Time	$(V_{CC} = 30\text{ Vdc}$, $I_C = 150\text{ mAdc}$, $I_{B(on)} = I_{B(off)} = 15\text{ mAdc}$) Figure 2	t_s	–	225	ns
Fall Time		t_f	–	60	

PZT2222AT1

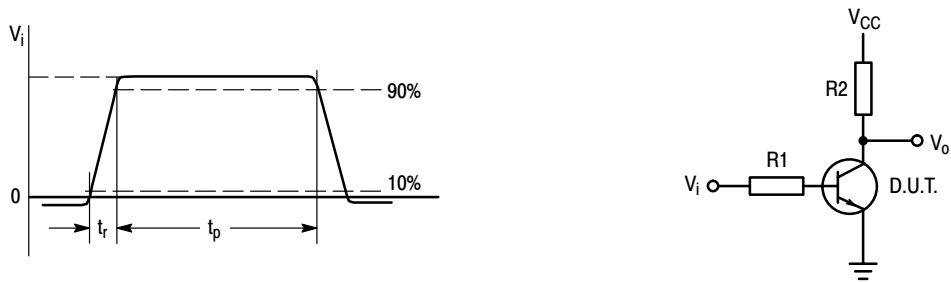


Figure 1. Input Waveform and Test Circuit for Determining Delay Time and Rise Time

$V_i = -0.5 \text{ V to } +9.9 \text{ V}$, $V_{CC} = +30 \text{ V}$, $R_1 = 619 \Omega$, $R_2 = 200 \Omega$.

PULSE GENERATOR:

PULSE DURATION t_p 3 200 ns
RISE TIME t_r 3 2 ns
DUTY FACTOR δ = 0.02

OSCILLOSCOPE:

INPUT IMPEDANCE $Z_i > 100 \text{ k}\Omega$
INPUT CAPACITANCE $C_i < 12 \text{ pF}$
RISE TIME $t_r < 5 \text{ ns}$

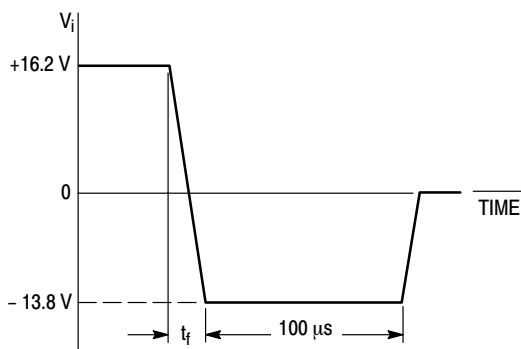


Figure 2. Input Waveform and Test Circuit for Determining Storage Time and Fall Time

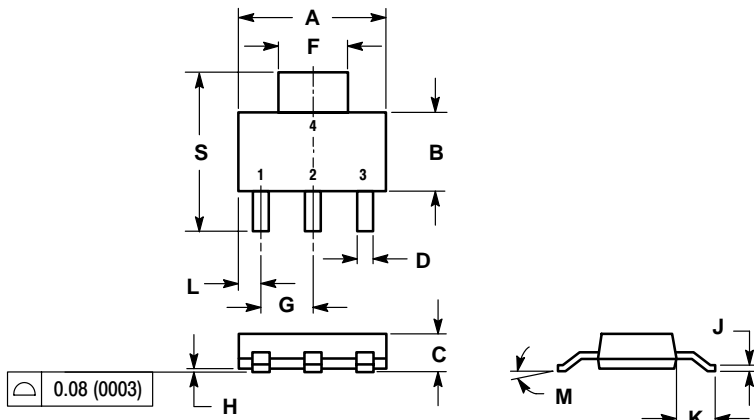
PZT2222AT1

PACKAGE DIMENSIONS

SOT-223 (TO-261)

CASE 318E-04

ISSUE K



NOTES:

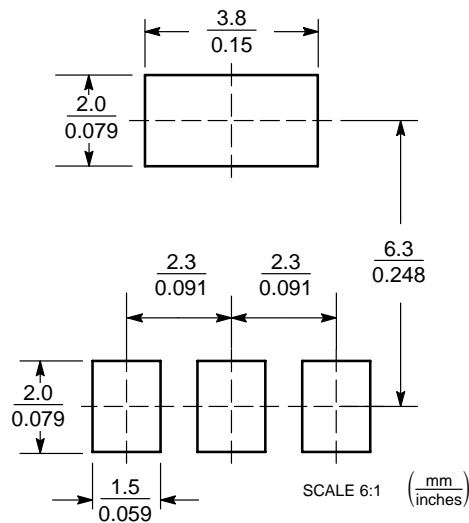
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.249	0.263	6.30	6.70
B	0.130	0.145	3.30	3.70
C	0.060	0.068	1.50	1.75
D	0.024	0.035	0.60	0.89
E	0.115	0.126	2.90	3.20
F	0.087	0.094	2.20	2.40
G	0.0008	0.0040	0.020	0.100
H	0.009	0.014	0.24	0.35
J	0.060	0.078	1.50	2.00
K	0.033	0.041	0.85	1.05
L	0°	10°	0°	10°
M	0.264	0.287	6.70	7.30


STYLE 1:

- PIN 1: BASE
2. COLLECTOR
3. EMITTER
4. COLLECTOR

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and  are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor
P.O. Box 61312, Phoenix, Arizona 85082-1312 USA
Phone: 480-829-7710 or 800-344-3860 Toll Free USA/Canada
Fax: 480-829-7709 or 800-344-3867 Toll Free USA/Canada
Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free
USA/Canada

Japan: ON Semiconductor, Japan Customer Focus Center
2-9-1 Kamimeguro, Meguro-ku, Tokyo, Japan 153-0051
Phone: 81-3-5773-3850

ON Semiconductor Website: <http://onsemi.com>

Order Literature: <http://www.onsemi.com/litorder>

For additional information, please contact your local Sales Representative.

PZT2222AT1/D