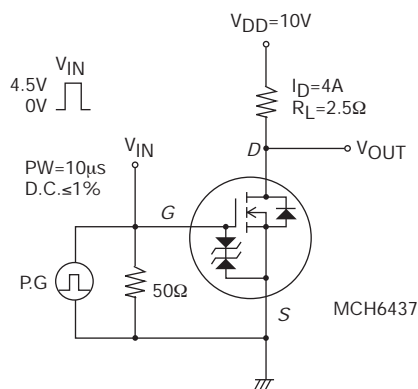


MCH6437

Electrical Characteristics at Ta=25°C

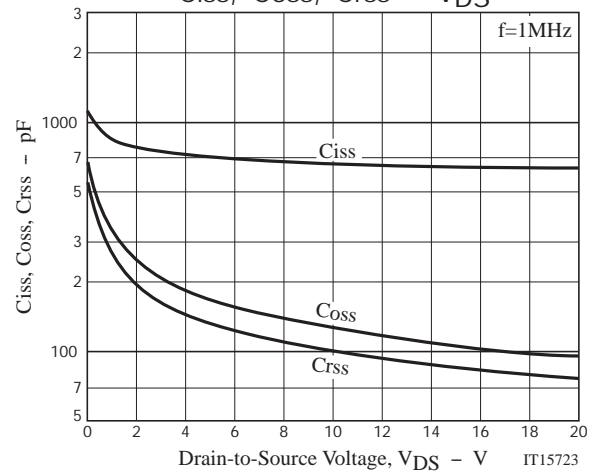
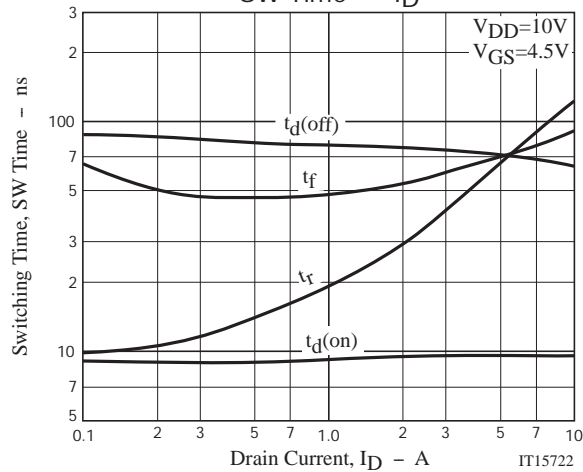
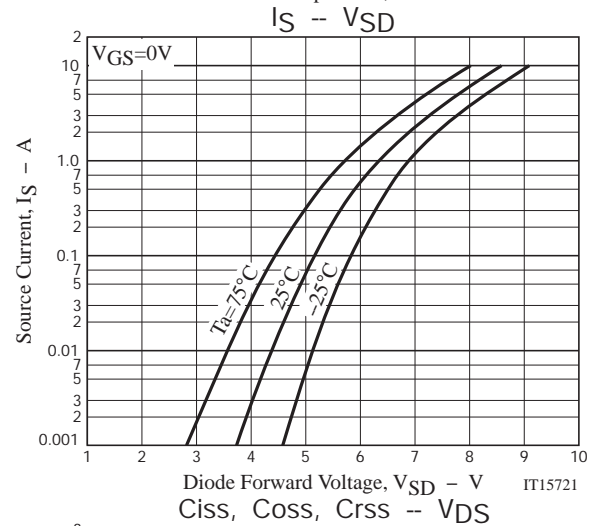
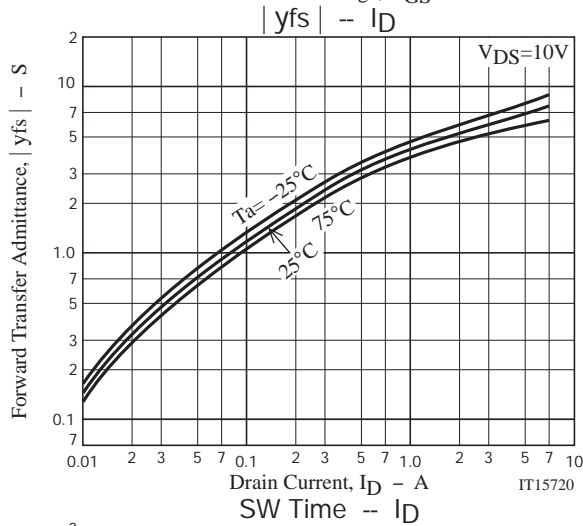
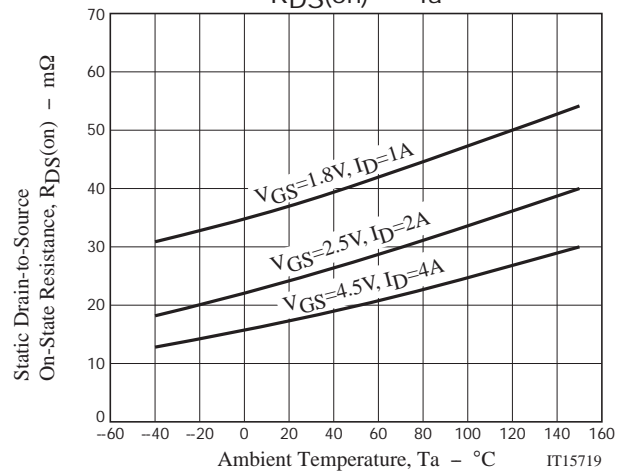
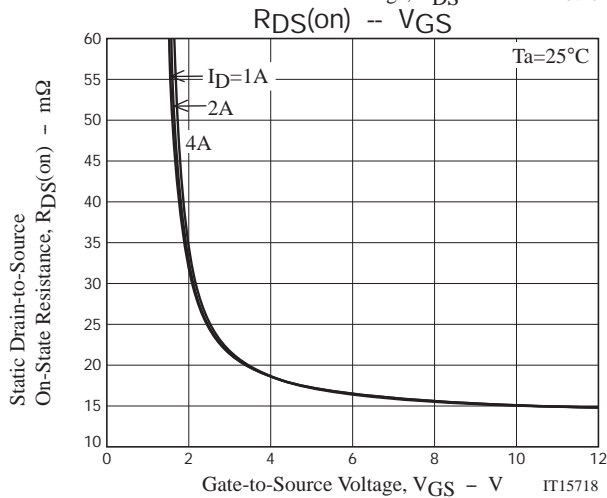
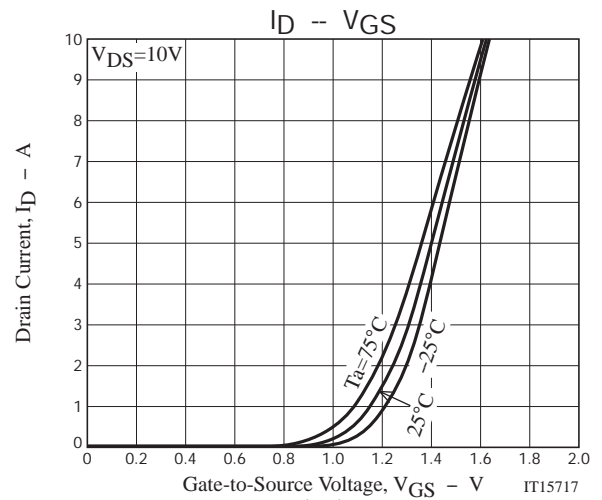
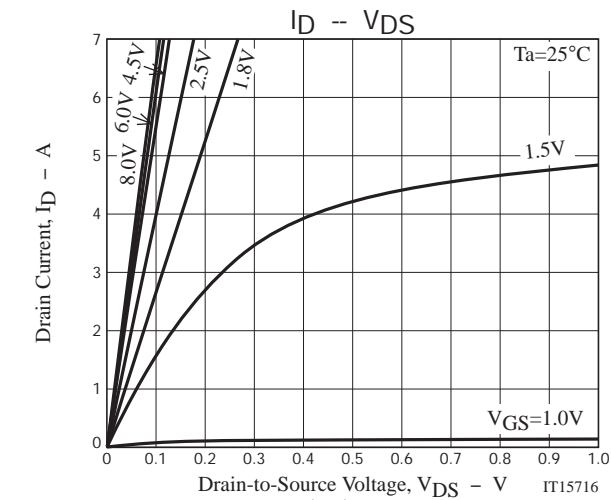
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D=1mA, V_{GS}=0V$	20			V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS}=20V, V_{GS}=0V$			1	μA
Gate-to-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 8V, V_{DS}=0V$			± 10	μA
Cutoff Voltage	$V_{GS(off)}$	$V_{DS}=10V, I_D=1mA$	0.4		1.3	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS}=10V, I_D=4A$		6.2		S
Static Drain-to-Source On-State Resistance	$R_{DS(on)1}$	$I_D=4A, V_{GS}=4.5V$		18	24	$m\Omega$
	$R_{DS(on)2}$	$I_D=2A, V_{GS}=2.5V$		25	35	$m\Omega$
	$R_{DS(on)3}$	$I_D=1A, V_{GS}=1.8V$		38	65	$m\Omega$
Input Capacitance	C_{iss}	$V_{DS}=10V, f=1MHz$		660		pF
Output Capacitance	C_{oss}			125		pF
Reverse Transfer Capacitance	C_{rss}			100		pF
Turn-ON Delay Time	$t_{d(on)}$	See specified Test Circuit.		9.7		ns
Rise Time	t_r			53		ns
Turn-OFF Delay Time	$t_{d(off)}$			72		ns
Fall Time	t_f			65		ns
Total Gate Charge	Q_g	$V_{DS}=10V, V_{GS}=4.5V, I_D=7A$		8.4		nC
Gate-to-Source Charge	Q_{gs}			1.0		nC
Gate-to-Drain "Miller" Charge	Q_{gd}			2.4		nC
Diode Forward Voltage	V_{SD}	$I_S=7A, V_{GS}=0V$		0.81	1.2	V

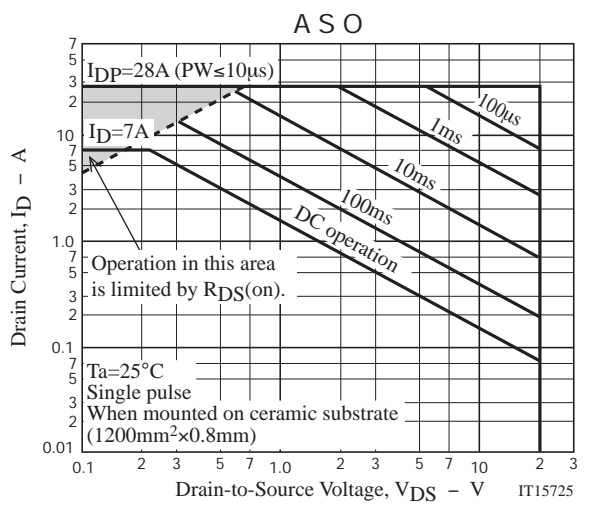
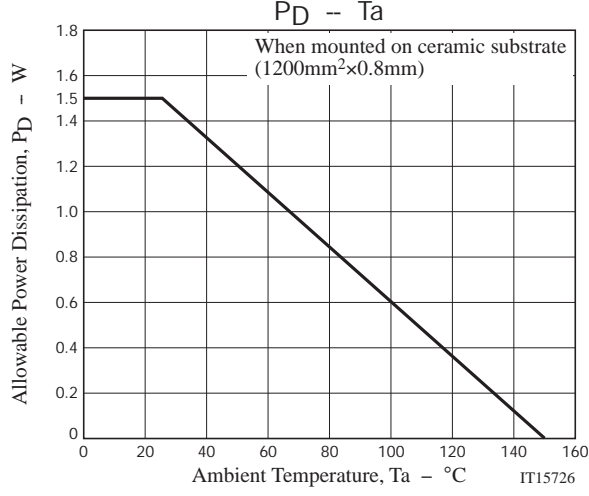
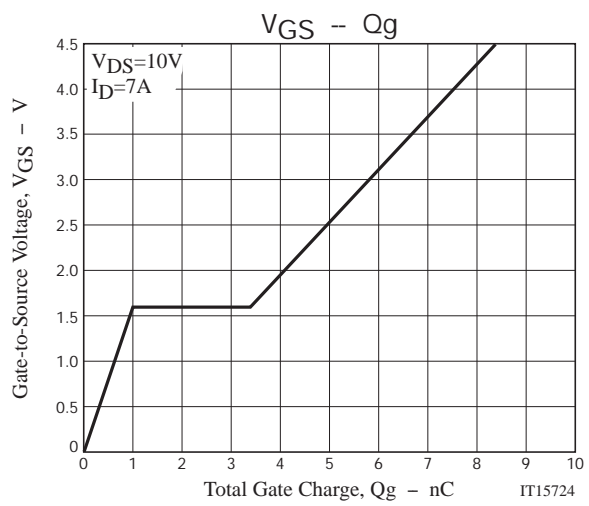
Switching Time Test Circuit



Ordering Information

Device	Package	Shipping	memo
MCH6437-TL-E	MCPH6	3,000pcs./reel	Pb Free





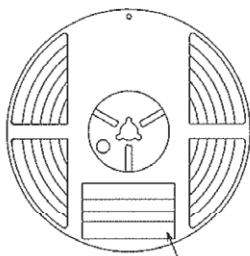
Taping Specification

MCH6437-TL-E

1. Packing Format

Package Name	Carrier Tape Type	Maximum Number of devices contained (pcs)			Packing format	
		Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)
MCPH6	MCP4	3,000	15,000	90,000	5 reels contained Dimensions:mm (external) 183×72×185	6 inner boxes contained Dimensions:mm (external) 440×195×210

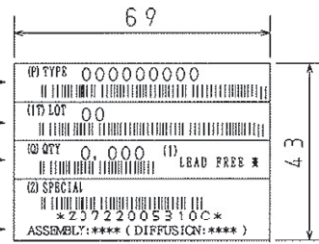
Packing method



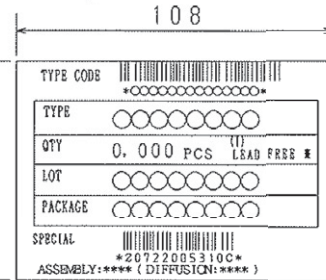
Reel label

Type No.
LOT No.
Quantity
Origin

Reel label, Inner box label
(unit:mm)



Outer box label
It is a label at the time of factory shipments.
The form of a label may change in physical
distribution process.



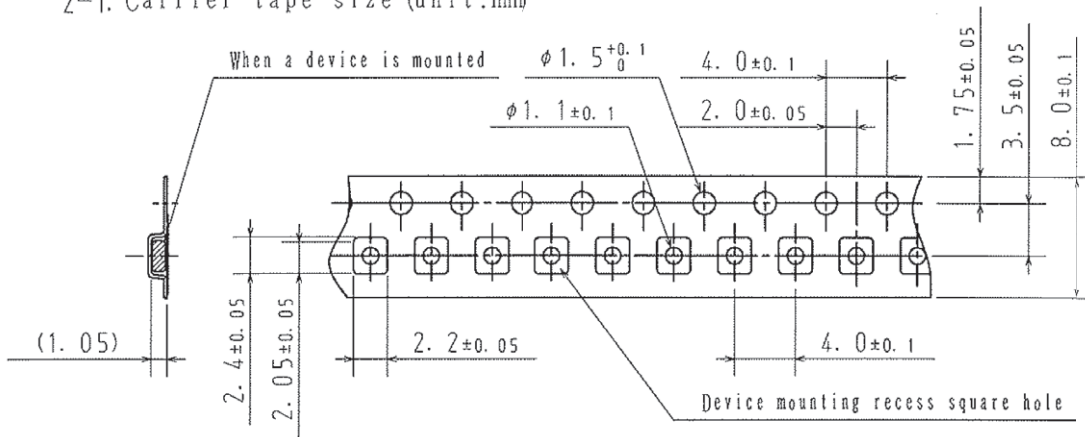
NOTE (1)

The LEAD FREE * description shows that the surface treatment of the terminal is lead free.

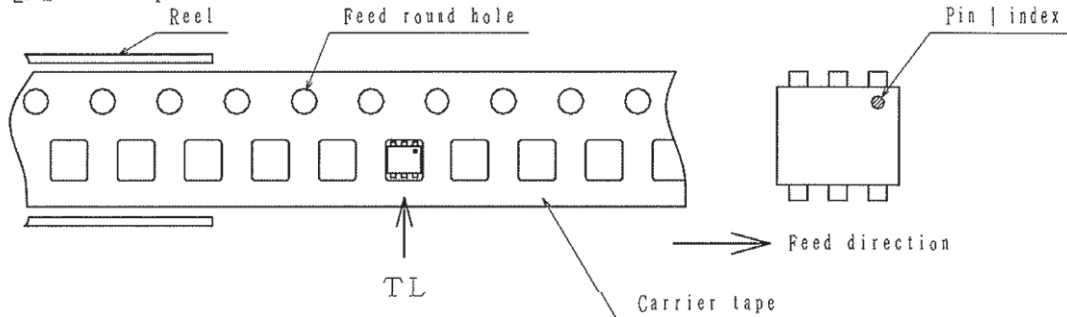
Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

2. Taping configuration

2-1. Carrier tape size (unit:mm)

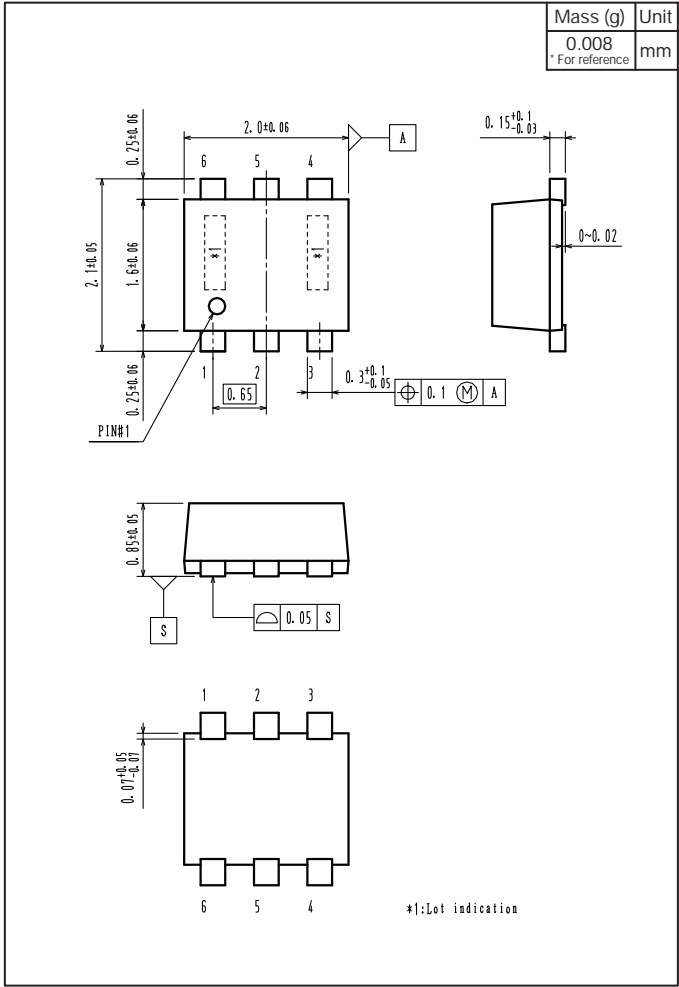


2-2. Device placement direction

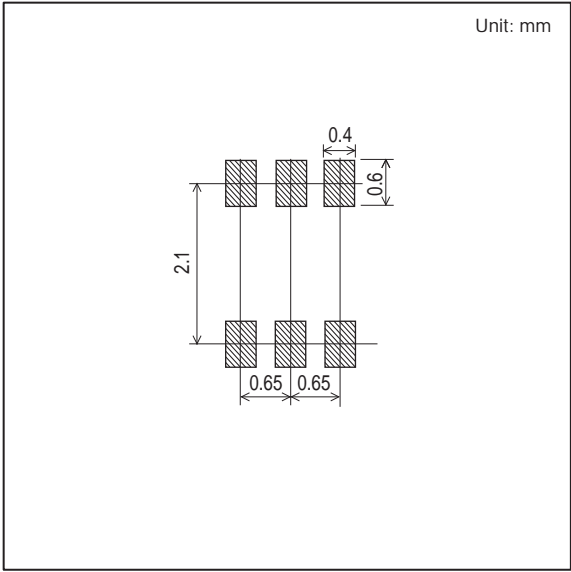


Those with pin 1 index on the feed hole side.....TL

Outline Drawing
MCH6437-TL-E



Land Pattern Example



Note on usage : Since the MCH6437 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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