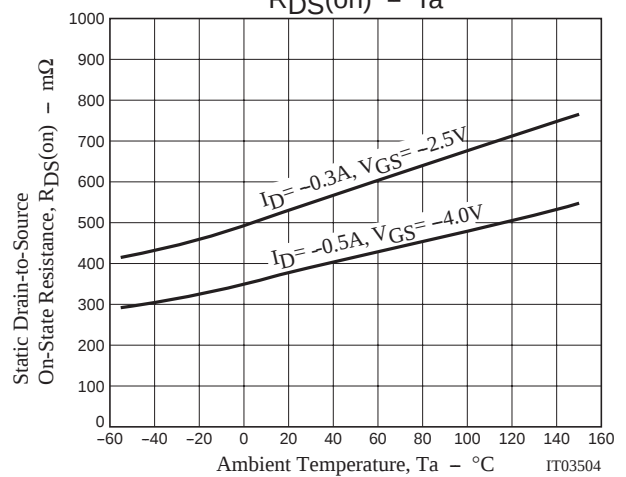
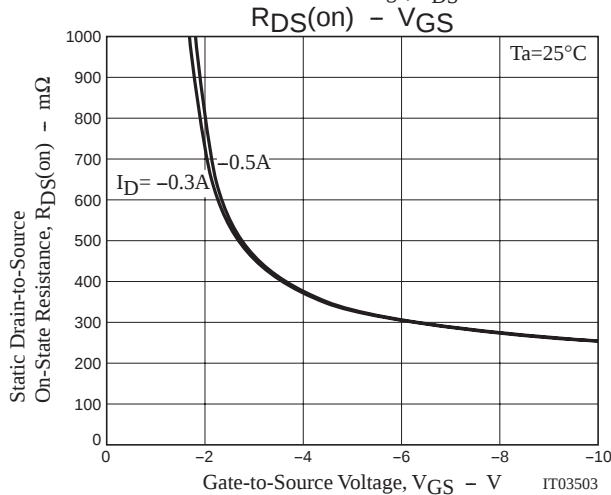
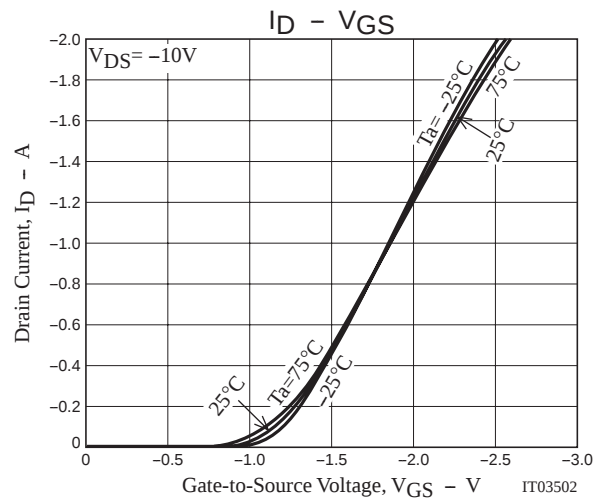
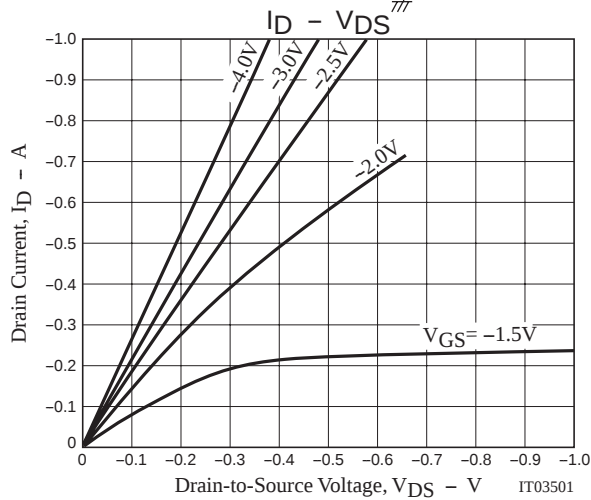
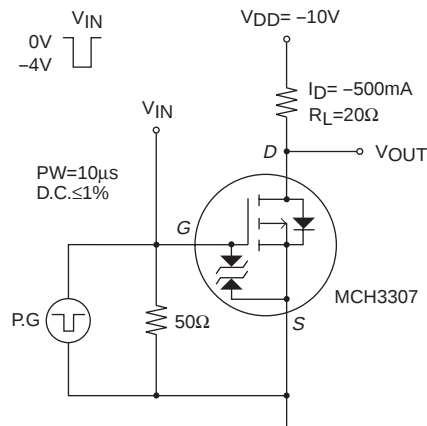
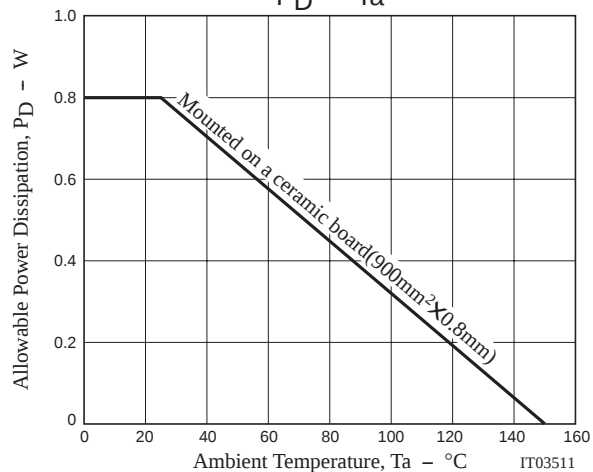
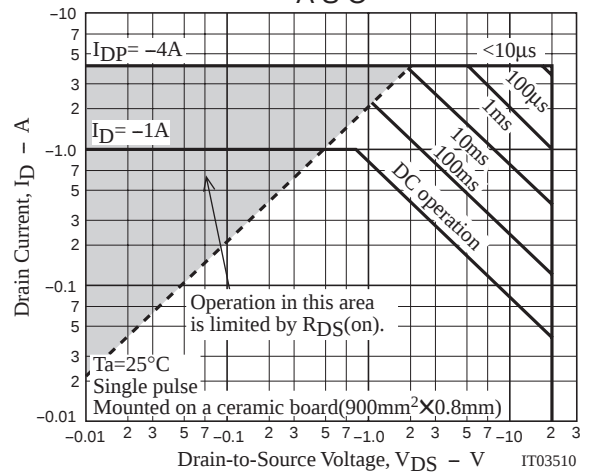
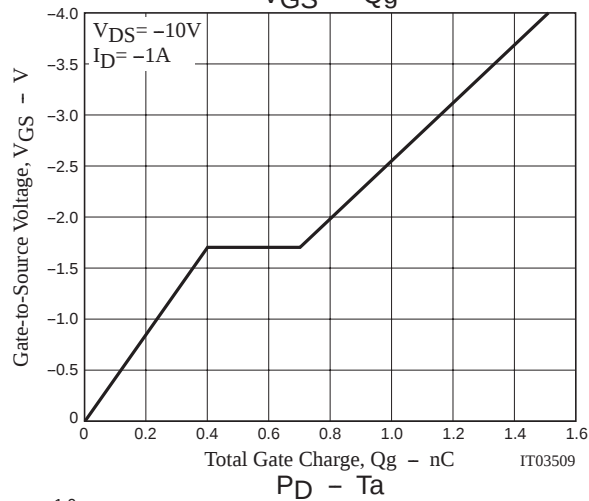
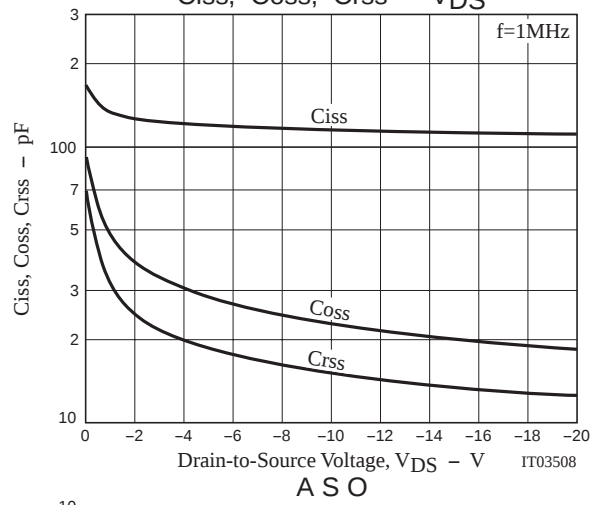
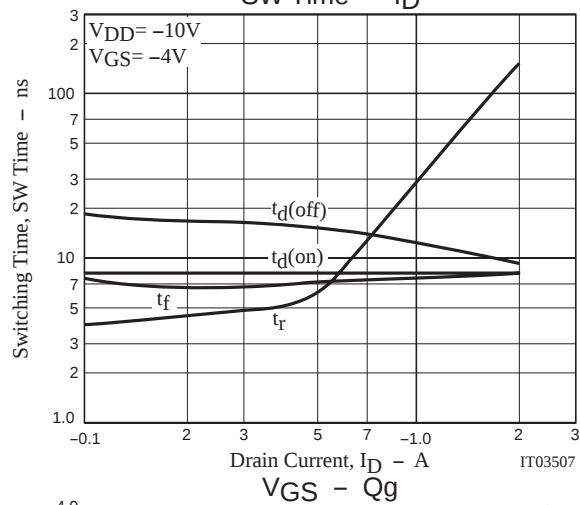
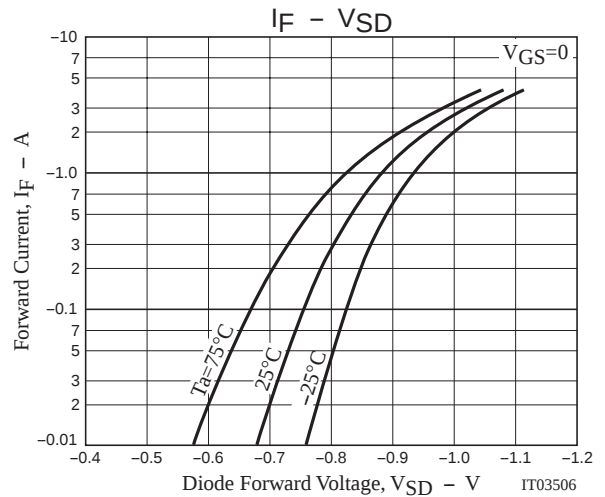
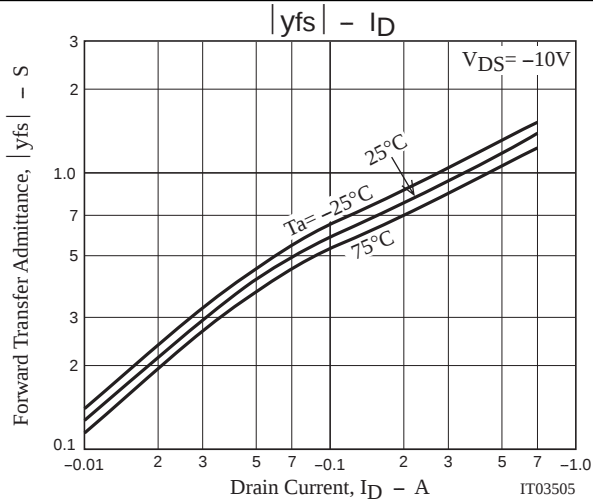


Continued from preceding page.

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Input Capacitance	C_{iss}	$V_{DS} = -10V, f = 1MHz$		115		pF
Output Capacitance	C_{oss}	$V_{DS} = -10V, f = 1MHz$		23		pF
Reverse Transfer Capacitance	C_{rss}	$V_{DS} = -10V, f = 1MHz$		15		pF
Turn-ON Delay Time	$t_d(on)$	See specified Test Circuit		8		ns
Rise Time	t_r	See specified Test Circuit		6		ns
Turn-OFF Delay Time	$t_d(off)$	See specified Test Circuit		15		ns
Fall Time	t_f	See specified Test Circuit		7		ns
Total Gate Charge	Q_g	$V_{DS} = -10V, V_{GS} = -4V, I_D = -1A$		1.5		nC
Gate-to-Source Charge	Q_{gs}	$V_{DS} = -10V, V_{GS} = -4V, I_D = -1A$		0.4		nC
Gate-to-Drain "Miller" Charge	Q_{gd}	$V_{DS} = -10V, V_{GS} = -4V, I_D = -1A$		0.3		nC
Diode Forward Voltage	V_{SD}	$I_S = -1A, V_{GS} = 0$		-0.89	-1.2	V

Switching Time Test Circuit





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