ABSOLUTE MAXIMUM RATINGS

V _{CC} to GND V+ to GND (Note 1)	0.3V to +7V
V- to GND (Note 1)	
V+ + IV-I (Note 1)	+13V
Input Voltages	
T_IN, EN, SHDN, FORCEON,	
FORCEOFF to GND	0.3V to +6V
R_IN to GND	±25V
Output Voltages	
T_OUT to GND	±13.2V
R_OUT, INVALID, READY to GND	0.3V to (V _{CC} + 0.3V)

///XI///

Note 1: V+ and V- can have maximum magnitudes of 7V, but their absolute difference cannot exceed 13V.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

(V_{CC} = +2.25V to +3.0V, C1-C4 = 0.1µF, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at V_{CC} = +2.5V, T_A = +25°C.)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
DC CHARACTERISTICS (V _{CC}	= +2.5V, T _A =	= +25°C)				
AutoShutdown Plus Supply Current		FORCEON = GND, FORCEOFF = V _{CC} , all R_IN idle, all T_IN idle (MAX3318E/MAX3319E)		1	10	μA
Shutdown Supply Current		SHDN = GND (MAX3317E);FORCEOFF =GND (MAX3318E/MAX3319E)		1	10	μA
Supply Current		$\overline{SHDN} = V_{CC}$, no load (MAX3317E); FORCEON = FORCEOFF = V_{CC} , no load (MAX3318E/MAX3319E)		0.3	1	mA
LOGIC INPUTS						
Input Logic Threshold Low		T_IN, EN, SHDN, FORCEON, FORCEOFF			0.3 • V _{CC}	V
Input Logic Threshold High		T_IN, EN, SHDN, FORCEON, FORCEOFF	0.7 • V _{CC}			V
Transmitter Input Hysteresis				0.3		V
Input Leakage Current		T_IN, EN, SHDN, FORCEON, FORCEOFF		±0.01	±1	μΑ
RECEIVER OUTPUTS						
Output Leakage Current		EN = V _{CC} (MAX3317E), receivers disabled		±0.05	±10	μA
Output Voltage Low		I _{OUT} = 0.5mA			0.1 • V _{CC}	V
Output Voltage High		I _{OUT} = -0.5mA	0.9 • V _{CC}			V
RECEIVER INPUTS						
Input Voltage Range			-25		+25	V
Input Threshold Low		$T_A = +25^{\circ}C$			0.3 • V _{CC}	V
Input Threshold High		$T_A = +25^{\circ}C$	0.7 • V _{CC}			V
Input Hysteresis				0.3		V
Input Resistance		$T_A = +25^{\circ}C$	3	5	7	kΩ
TRANSMITTER OUTPUTS		•				
Output Voltage Swing		All transmitter outputs loaded with ${\rm 3k}\Omega$ to ground	±3.7	±4		V

2

ELECTRICAL CHARACTERISTICS (continued)

(V_{CC} = +2.25V to +3.0V, C1–C4 = 0.1 μ F, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at V_{CC} = +2.5V, T_A = +25°C.)

PARAMETER	SYMBOL	COND	MIN	TYP	MAX	UNITS	
Output Resistance		V _{CC} = 0, transmitter ou	utput = ±2V	300	10M		Ω
Output Short-Circuit Current					±25	±60	mA
Output Leakage Current		V _{CC} = 0 or 2.25V to 3.0 transmitters disabled	$0V, V_{OUT} = \pm 12V,$			±25	μA
ESD PROTECTION		I		1			1
		Human Body Model			±15		
R_IN, T_OUT		IEC 1000-4-2 Air-Gap	Discharge method		±15		kV
		IEC 1000-4-2 Contact	Discharge method		±8		
AutoShutdown Plus (FORCEON	$V = GND, \overline{F}C$	$DRCEOFF = V_{CC}$ (MAX)	3318E/MAX3319E)				
Receiver Input Threshold to		Figure 4a	Positive threshold			2.7	v
INVALID Output High		rigure 4a	Negative threshold	-2.7			
Receiver Input Threshold to INVALID Output Low		Figure 4a		-0.3		0.3	V
INVALID, READY Output Voltage Low		I _{OUT} = 0.5mA			C).1 • Vcc	V
ĪNVALID, READY Output Voltage High		I _{OUT} = -0.5mA	I _{OUT} = -0.5mA				V
Receiver Positive or Negative Threshold to INVALID High	tinvh	V _{CC} = 2.5V, Figure 4b			1		μs
Receiver Positive or Negative Threshold to INVALID Low	tinvl	V _{CC} = 2.5V, Figure 4b			30		μs
Receiver or Transmitter Edge to Transmitters Enabled	twu	$V_{CC} = 2.5V$, Figure 4b (Note 2)			100		μs
Receiver or Transmitter Edge to Transmitters Shutdown	t _{AUTO-} SHDN	V _{CC} = 2.5V, Figure 4b	(Note 2)	15	30	60	S

TIMING CHARACTERISTICS

(V_{CC} = +2.25V to +3.0V, C1-C4 = 0.1 μ F, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at V_{CC} = +2.5V, T_A = +25°C.)

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNITS
Maximum Data Rate		$R_L = 3k\Omega$, $C_L = 1000pF$, one transmitter switching	460			kbps
	t PHL	0.17		0.175		
Receiver Propagation Delay	t _{PLH}	R_IN to R_OUT, $C_L = 150pF$		0.175		μs
Receiver Output Enable Time		Normal operation (MAX3317E)		250		ns
Receiver Output Disable Time		Normal operation (MAX3317E)		250		ns
Transmitter Skew	tphl - tplh	(Note 3)		100		ns
Receiver Skew	t _{PHL} - t _{PLH}			50		ns

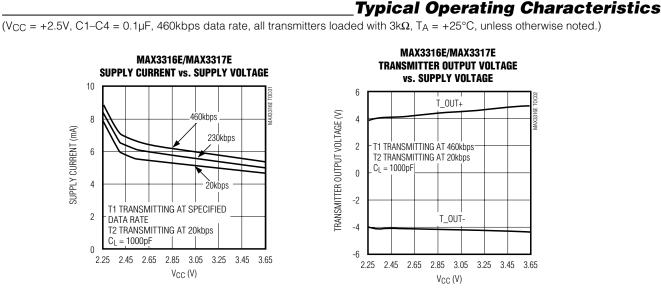


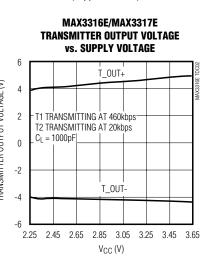
TIMING CHARACTERISTICS (continued)

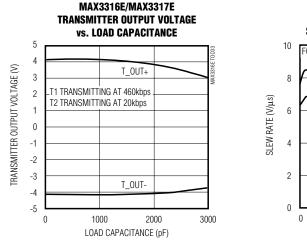
 $(V_{CC} = +2.25V$ to +3.0V, C1-C4 = 0.1µF, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at $V_{CC} = +2.5V$, $T_A = +25^{\circ}C.$

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNITS
Transition-Region Slew Rate		$\label{eq:VCC} \begin{array}{l} V_{CC} = 2.5 \text{V}, \ T_A = +25^{\circ}\text{C}, \ R_L = 3 \text{k}\Omega \ \text{to} \ 7 \text{k}\Omega, \\ \text{measured from} \ +3 \text{V} \ \text{to} \ -3 \text{V} \ \text{or} \ -3 \text{V} \ \text{to} \ +3 \text{V}, \\ \text{C}_L = 150 \text{pF} \ \text{to} \ 2500 \text{pF} \end{array}$	4		30	V/µs

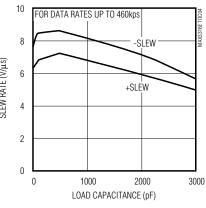
Note 2: A transmitter/receiver edge is defined as a transition through the transmitter/receiver input logic thresholds. Note 3: Transmitter skew is measured at the transmitter zero crosspoints.









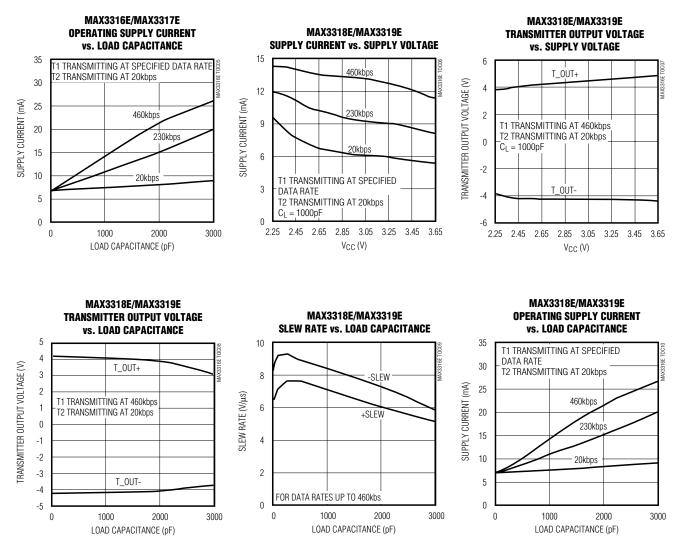


M/IXI/N

4

Typical Operating Characteristics (continued)

 $(V_{CC} = +2.5V, C1-C4 = 0.1\mu F, 460 \text{kbps} \text{ data rate, all transmitters loaded with } 3k\Omega, T_A = +25^{\circ}C, \text{ unless otherwise noted.})$



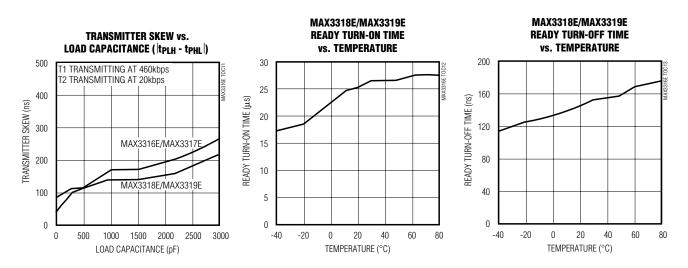
MAX3316E-MAX3319E

M/X/W

MAX3316E-MAX3319E

_Typical Operating Characteristics (continued)

 $(V_{CC} = +2.5V, C1-C4 = 0.1\mu F, 460 \text{kbps} \text{ data rate, all transmitters loaded with } 3k\Omega, T_A = +25^{\circ}C, \text{ unless otherwise noted.})$



Pin Description

		PIN						
MAX	3316E	MAX3317E	MAX3318E	MAX3319E	NAME	FUNCTION		
SSOP	TSSOP		WIAX55TOL	WAX5519E				
1	2	2	2	2	C1+	Positive Terminal of Voltage-Doubler Charge- Pump Capacitor		
2	3	3	3	3	V+	$+2 \cdot V_{CC}$ Generated by the Charge Pump		
3	4	4	4	4	C1-	Negative Terminal of Voltage-Doubler Charge- Pump Capacitor		
4	5	5	5	5	C2+	Positive Terminal of Inverting Charge-Pump Capacitor		
5	6	6	6	6	C2-	Negative Terminal of Inverting Charge-Pump Capacitor		
6	7	7	7	7	V-	-2 • V _{CC} Generated by the Charge Pump		
7, 14	8, 17	8, 17	8, 17	13	T_OUT	RS-232 Transmitter Outputs		
8, 13	9, 16	9, 16	9, 16	8	R_IN	RS-232 Receiver Inputs		
9, 12	12, 15	10, 15	10, 15	9	R_OUT	CMOS Receiver Outputs		
10, 11	13, 14	12, 13	12, 13	11	T_IN	CMOS Transmitter Inputs		
15	18	18	18	14	GND	Ground		
16	19	19	19	15	Vcc	+2.25V to +3.0V Single-Supply Voltage		
—		1	—	_	ĒN	Receiver Enable, Active Low		
_	1, 10, 11, 20	11, 14	_	_	N.C.	No Connection		

Pin Description (continued)

		PIN					
MAX	3316E	MAX3317E	MAX3318E	MAX3319E	NAME	FUNCTION	
SSOP	TSSOP	MAASSITE	WANJSTOE	WANJJIJE			
		20	—		SHDN	Shutdown Control, Active Low	
_		_	1	1	READY	Ready to Transmit Output, Active High. READY is enabled high when V- goes below -3.5V and the device is ready to transmit.	
_			11	10	INVALID	Valid Signal Detector Output, Active Low. A logic high indicates that a valid RS-232 level is present on a receiver input.	
_		_	14	12	FORCEON	Force-On Input, Active High. Drive high to override AutoShutdown Plus, keeping transmitters and receivers on (FORCEOFF must be high) (Table 1).	
_			20	16	FORCEOFF	Force-Off Input, Active Low. Drive low to shut down transmitters, receivers, and charge pump. This overrides AutoShutdown Plus and FORCEON (Table 1).	

Detailed Description

Dual Charge-Pump Voltage Converter

The MAX3316E–MAX3319Es' internal power supply consists of a regulated dual charge pump that provides output voltages of +4.4V (doubling charge pump) and -4.3V (inverting charge pump), over the +2.25V to +3.0V range. The charge pump operates in discontinuous mode: if the output voltages are less than 4.4V, the charge pump is enabled; if the output voltages exceed 4.4V, the charge pump is disabled. Each charge pump requires a flying capacitor (C1, C2) and a reservoir capacitor (C3, C4) to generate the V+ and V- supplies.

The READY output (MAX3318E/MAX3319E) is low when the charge pumps are disabled in shutdown mode. The READY signal asserts high when V- goes below -2.75V.

RS-232 Compatible Transmitters

The MAX3316E–MAX3319Es^T transmitters are inverting level translators that convert CMOS-logic levels to RS-232 compatible voltage levels. They guarantee a 460kbps data rate with worst-case loads of $3k\Omega$ in parallel with 1000pF, providing compatibility with PC-to-PC communication software (such as LapLinkTM).

The MAX3317E's transmitters are turned off (high impedance) when \overline{SHDN} is asserted low, putting the device in shutdown mode. The MAX3318E/MAX3319Es'

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transmitters are turned off (high impedance) when FORCEOFF is asserted low, or when the AutoShutdown Plus circuitry senses that all receiver and transmitter inputs are inactive for more than 30 seconds.

The transmitter outputs can be driven to $\pm 12V$ when power is off. The transmitter inputs do not have internal pull-up resistors. Connect unused inputs to GND or V_{CC}.

RS-232 Receivers

The MAX3316E–MAX3319Es' receivers convert $\pm 3.7V$ to $\pm 13.2V$ RS-232 signal levels into CMOS-logic output levels. The receivers are rated to receive signals up to $\pm 25V$. The MAX3316E/MAX3318E/MAX3319Es' receivers feature inverting outputs that always remain active (Table 1). The MAX3317E's receivers have inverting, three-state outputs. In shutdown, the receivers can be active or inactive (Table 2).

The MAX3318E/MAX3319E feature an INVALID output that is asserted low when no valid RS-232 voltage levels have been detected on all receiver inputs. Because INVALID indicates the receiver's input condition, it is independent of the states of FORCEON and FORCEOFF.

MAX3317E Shutdown Mode

Supply current falls to less than 1μ A in shutdown mode (SHDN = low). When shut down, the device's charge pumps are turned off, V+ is pulled down to V_{CC}, V- is pulled to ground, and the transmitter outputs are disabled (high impedance). The time required to exit shut-

OPERATION STATUS	FORCEON	FORCEOFF	VALID RECEIVER LEVEL	RECEIVER OR TRANSMITTER EDGE WITHIN 30s	T_OUT	R_OUT
Shutdown (Forced Off)	Х	0	х	Х	High-Z	Active
Normal Operation (Forced On)	1	1	Х	Х	Active	Active
Normal Operation (AutoShutdown Plus)	0	1	Х	Yes	Active	Active
Shutdown (AutoShutdown Plus)	0	1	Х	No	High-Z	Active
Normal Operation	INVALID *	1	Yes	Х	Active	Active
Normal Operation	INVALID *	1	Х	Yes	Active	Active
Shutdown	INVALID *	1	No	No	High-Z	Active
Normal Operation (AutoShutdown)	INVALID**	INVALID**	Yes	Х	Active	Active
Shutdown (AutoShutdown)	INVALID**	INVALID**	No	Х	High-Z	Active

Table 1. Output Control Truth Table (MAX3318E/MAX3319E)

X = Don't care

* INVALID connected to FORCEON

** INVALID connected to FORCEON and FORCEOFF

Table 2. Shutdown and Enable ControlTruth Table (MAX3317E)

SHDN	EN	T_OUT	R_OUT
0	0	High-Z	Active
0	1	High-Z	High-Z
1	0	Active	Active
1	1	Active	High-Z

down is typically 30 μ s, as shown in Figure 2. Connect SHDN to V_{CC} if the shutdown mode is not used; SHDN has no effect on R_OUT.

MAX3318E/MAX3319E AutoShutdown Plus Mode

Maxim's AutoShutdown Plus feature on the MAX3318E/ MAX3319E allows the supply current to fall to 1µA. These devices will enter the AutoShutdown Plus mode if FORCEOFF is high, FORCEON is low, and they do not sense a valid signal transition on any receiver or transmitter input for 30 seconds. This may occur if the RS-232 cable is disconnected or if the peripheral transmitters are turned off, and the UART driving the transmitter inputs is inactive. The system turns on again when a valid transition is applied to any RS-232 receiver or transmitter input. As a result, the system saves power without changes to the existing BIOS or operating system.

Figure 4a depicts valid and invalid RS-232 receiver voltage levels. INVALID indicates the receiver input's condition and is independent of FORCEON and FORCEOFF states. Figure 3 and Table 1 summarize the operating modes of the MAX3318E/MAX3319E. FORCEON and FORCEOFF override AutoShutdown Plus circuitry. When neither control is asserted, the IC selects between these states automatically based on the last receiver or transmitter edge received.

When shut down, the device's charge pumps turn off, V+ is pulled to V_{CC}, V- is pulled to ground, the transmitter outputs are high impedance, and READY is driven low. The time required to exit shutdown is typically 30μ s (Figure 2).

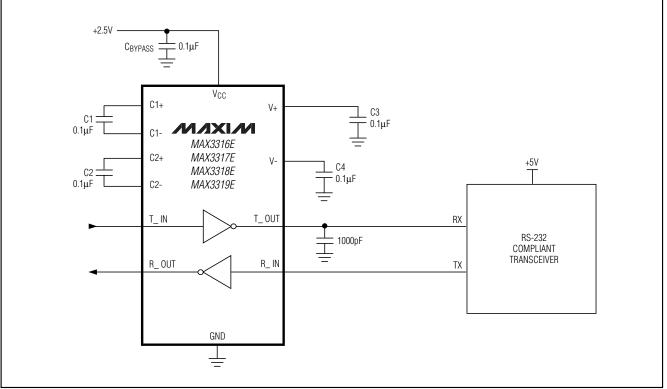


Figure 1a. RS-232 Compatibility Circuit

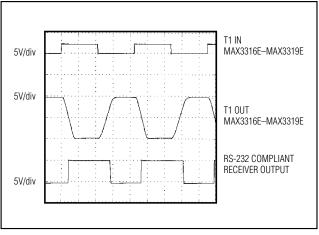


Figure 1b. MAX3316E–MAX3319E Transmitter Output Compatibility with an RS-232 Receiver

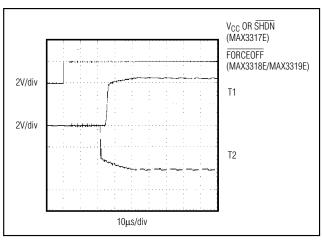


Figure 2. Transmitter Outputs when Exiting Shutdown or Powering Up



By connecting FORCEON to INVALID, the MAX3318E/ MAX3319E shut down when no valid receiver level is detected and wake up when a valid receiver level is detected.

A system with AutoShutdown Plus may need time to wake up. Figure 5 shows a circuit that forces the transmitters on for 100ms, allowing enough time for the other system to realize that the MAX3318E/MAX3319E is awake. If the other system outputs valid RS-232 signal transitions within that time, the RS-232 ports on both systems remain enabled.

Connecting to the PC (MAX3318E/MAX3319E)

If direct software control is desired, use INVALID to indicate DTR or ring indicator (RI) signal. This can be used to connect a hand-held device to a PC. One example is using the Hot Sync[™] function on a personal digital assistant (PDA). The transmitter and receiver signals (T_OUT and R_IN) are used for communication, while INVALID causes a change of state on RI. The change of state on RI will trigger an interrupt on the PC and allow communication to begin between the device and the PC. This eliminates the need for the PC to poll constantly the receiver or transmitter lines to determine if the device is connected.

±15kV ESD Protection

All the pins on the MAX3316E–MAX3319E are protected against ESDs encountered during handling and assembly. The driver outputs and receiver inputs have extra protection against static electricity. Maxim's engineers have developed state-of-the-art structures to protect these pins against ESD of ±15kV without damage. The ESD structures withstand high ESD in all states: normal operation, shutdown, and powered down. After an ESD event, Maxim's E versions keep working without latchup, whereas competing products can latch and must be powered down to remove latchup.

ESD protection can be tested in various ways; the transmitter outputs and receiver inputs of the product family are characterized for protection to the following limits:

- ±15kV using the Human Body Model
- ±8kV using the IEC 1000-4-2 Contact Discharge method
- ±15kV using IEC 1000-4-2 Air-Gap method

ESD Test Conditions

ESD performance depends on a variety of conditions. Contact Maxim for a reliability report that documents test setup, test methodology, and test results.

Hot Sync is a trademark of 3Com.

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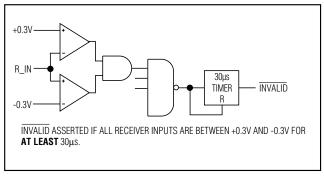


Figure 3a. INVALID Functional Diagram, INVALID Low

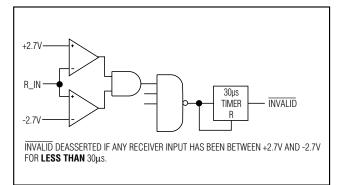


Figure 3b. INVALID Functional Diagram, INVALID High

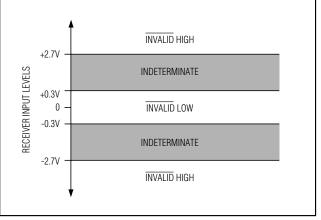


Figure 4a. Receiver Positive/Negative Thresholds for INVALID

Human Body Model

Figure 6a shows the Human Body Model, and Figure 6b shows the current waveform it generates when discharged into low impedance. This model consists of a 100pF capacitor charged to the ESD voltage of interest, which is then discharged into the test device through a $1.5k\Omega$ resistor.



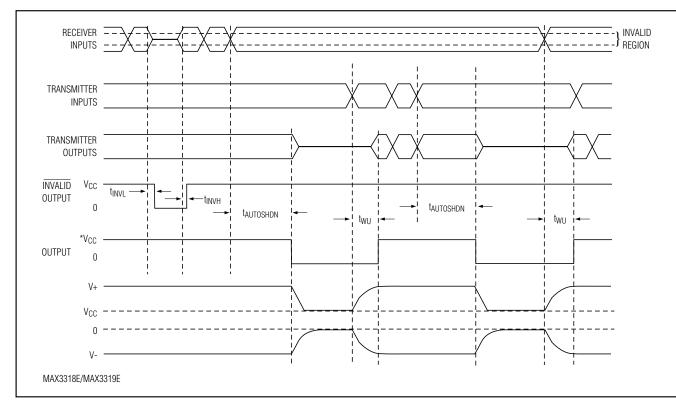


Figure 4b. AutoShutdown Plus, INVALID, and READY Timing Diagram

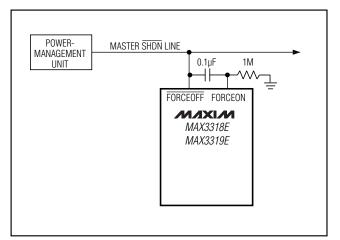


Figure 5. AutoShutdown Plus Initial Turn-On to Wake Up a Mouse or Another System

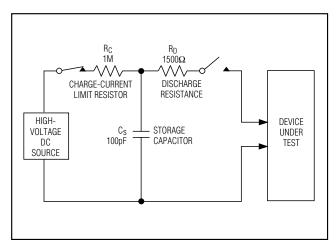


Figure 6a. Human Body ESD Test Model



IEC 1000-4-2

The IEC 1000-4-2 standard covers ESD testing and performance of finished equipment; it does not specifically refer to ICs. The MAX3316E–MAX3319E help you design equipment that meets Level 4 (the highest level) of IEC 1000-4-2 without the need for additional ESD-protection components.

The major difference between tests done using the Human Body Model and IEC 1000-4-2 is higher peak current in IEC 1000-4-2 because series resistance is lower in the IEC 1000-4-2 model. Hence, the ESD withstand voltage measured to IEC 1000-4-2 is generally lower than that measured using the Human Body Model. Figure 7a shows the IEC 1000-4-2 model, and Figure 7b shows the current waveform for the 8kV, IEC 1000-4-2, Level 4, ESD Contact Discharge test.

The Air-Gap Discharge test involves approaching the device with a charged probe. The Contact Discharge method connects the probe to the device before the probe is energized.

Machine Model

The Machine Model for ESD tests all pins using a 200pF storage capacitor and zero discharge resistance. Its objective is to emulate the stress caused by contact that occurs with handling and assembly during manufacturing. All pins require this protection during manufacturing, not just RS-232 inputs and outputs. Therefore, after PC board assembly, the Machine Model is less relevant to I/O ports.

Applications Information

RS-232 Compatible Operation

The MAX3316E–MAX3319E do not meet EIA-232 requirements for transmitter output voltage levels. EIA-232 compliance specifies transmitter output voltage swings of \pm 5V when loaded with 3k Ω and 2500pF.

The receiver inputs are fully EIA-232 compliant.

The MAX3316E–MAX3319E will function properly with most modern RS-232 interfaces. This allows RS-232 compatible communication in low-voltage systems without the added expense of a voltage tripler or switched-mode power supply.

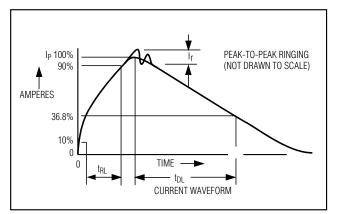


Figure 6b. Human Body Current Waveform

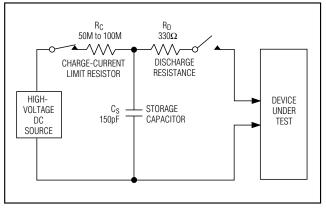


Figure 7a. IEC 1000-4-2 ESD Test Model

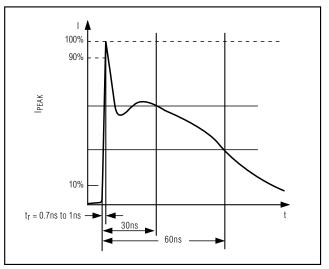


Figure 7b. IEC 1000-4-2 ESD Generator Current Waveform



Capacitor Selection

The capacitor type used for C1–C4 is not critical for proper operation; polarized or nonpolarized capacitors can be used. The charge pump requires 0.1μ F capacitors. Increasing the capacitor values (e.g., by a factor of 2) reduces ripple on the transmitter outputs and slightly reduces power consumption. C2, C3, and C4 can be increased without changing C1's value. However, do not increase C1 without also increasing the values of C2, C3, C4, and C_{BYPASS} to maintain proper ratios (C1 to other capacitors).

When using the minimum-required capacitor values, make sure the capacitor value does not degrade excessively with temperature. If in doubt, use capacitors with a higher nominal value. The capacitor's equivalent series resistance (ESR), which usually rises at low temperatures, influences the amount of ripple on V+ and V-.

Power-Supply Decoupling

In most circumstances, a 0.1μ F bypass capacitor is adequate. In applications that are sensitive to powersupply noise, decouple V_{CC} to ground with a capacitor of the same value as charge pump capacitor C1. Connect bypass capacitors as close to the IC as possible.

Transmitter Outputs when Exiting Shutdown

Figure 2 shows two transmitter outputs when exiting shutdown mode. As they become active, the two transmitter outputs are shown going to opposite RS-232 compatible levels (one transmitter input is high, the other is low). Each transmitter is loaded with $3k\Omega$ in parallel with 2500pF. The transmitter outputs display no ringing or undesirable transients as they come out of shutdown. Note that the transmitters are enabled only when the magnitude of V- exceeds approximately -3V.

High Data Rates

The MAX3316E–MAX3319E maintain RS-232 compatible \pm 3.7V minimum transmitter output voltage even at high data rates. Figure 8 shows a transmitter loopback test circuit. Figure 9 shows a loopback test result at 230kbps. For Figure 9, all transmitters were driven simultaneously at 230kbps into EIA/TIA-562 loads in parallel with 1000pF.

Chip Information

TRANSISTOR COUNT: 1130

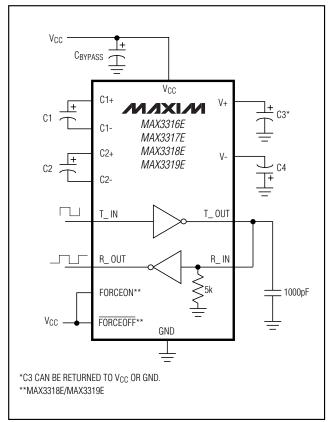


Figure 8. Loopback Test Circuit

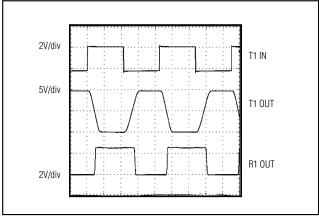
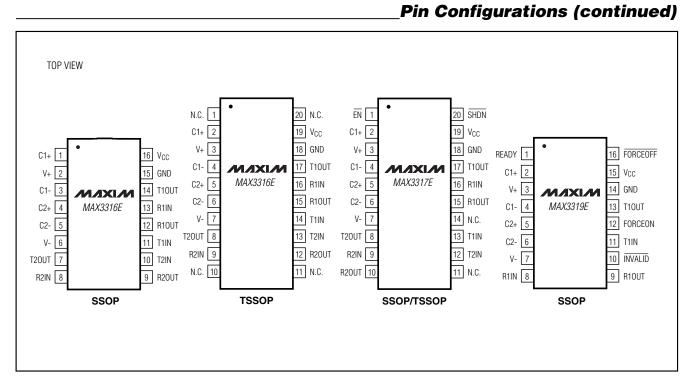


Figure 9. Loopback Test Result at 230kbps



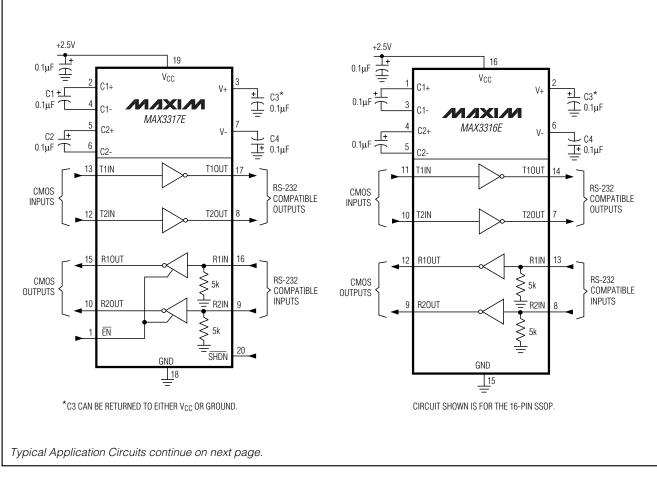
MAX3316E-MAX3319E

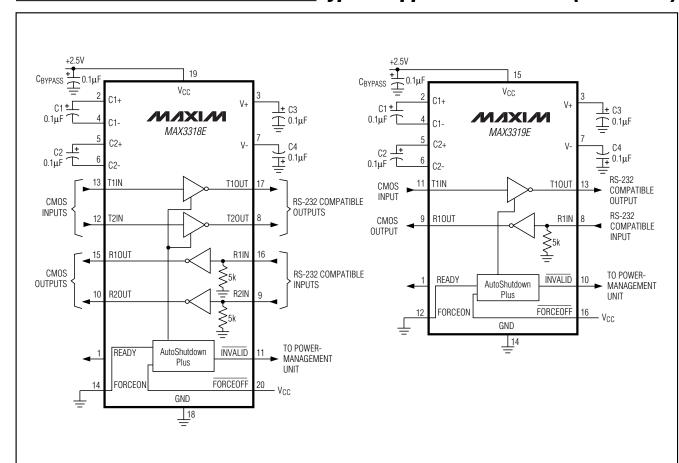
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Selector Guide

PART	NO. OF DRIVERS/ RECEIVERS	GUARANTEED DATA RATE (kbps)	READY OUTPUT	SHUTDOWN	AutoShutdown Plus
MAX3316E	2/2	460	—	—	_
MAX3317E	2/2	460	—	1	—
MAX3318E	2/2	460	✓	—	1
MAX3319E	1/1	460	✓	_	1







Typical Application Circuits (continued)

Maxim cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in a Maxim product. No circuit patent licenses are implied. Maxim reserves the right to change the circuitry and specifications without notice at any time.

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