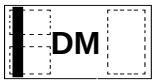
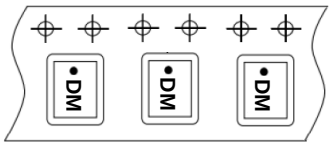
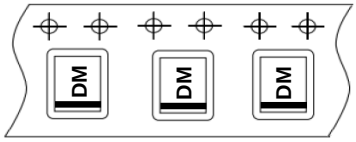
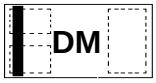
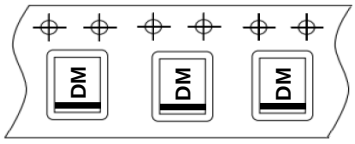
Marking Information

DMN2005LPK-7	 Top View Dot Denotes Drain Side From date code 1527 (YYWW), this changes to:  Top View Bar Denotes Gate and Source Side  
DMN2005LPK-7B	 Top View Bar Denotes Gate and Source Side DM = Part Marking Code 

Maximum Ratings (@T_A = +25°C unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V_{DSS}	20	V
Gate-Source Voltage	V_{GSS}	±10	V
Drain Current per element (Note 5)	I_D	440	mA

Thermal Characteristics (@T_A = +25°C unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P_D	450	mW
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	218	°C/W
Operating and Storage Temperature Range	T_j, T_{STG}	-65 to +150	°C

Electrical Characteristics (@T_A = +25°C unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)						
Drain-Source Breakdown Voltage	BV_{DSS}	20	—	—	V	V_{GS} = 0V, I_D = 100µA
Zero Gate Voltage Drain Current	I_{DSS}	—	—	10	µA	V_{DS} = 17V, V_{GS} = 0V
Gate-Source Leakage	I_{GSS}	—	—	±5	µA	V_{GS} = ±8V, V_{DS} = 0V
ON CHARACTERISTICS (Note 6)						
Gate Threshold Voltage	$V_{GS(th)}$	0.53	—	1.2	V	V_{DS} = V_{GS}, I_D = 100µA
Static Drain-Source On-Resistance	$R_{DS\ (ON)}$	— — — — —	0.35 0.4 0.45 0.55 0.65	1.5 1.7 1.7 3.5 3.5	Ω	V_{GS} = 4V, I_D = 10mA V_{GS} = 2.7V, I_D = 200mA V_{GS} = 2.5V, I_D = 10mA V_{GS} = 1.8V, I_D = 200mA V_{GS} = 1.5V, I_D = 1mA
Forward Transfer Admittance	$\|Y_{fs}\|$	40	—	—	mS	V_{DS} = 3V, I_D = 10mA

Notes: 5. Device mounted on FR-4 PCB.
6. Short duration pulse test used to minimize self-heating effect.

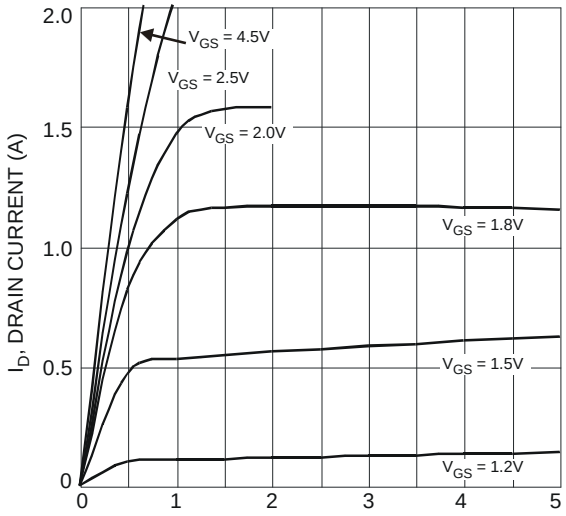


Fig. 1 Typical Output Characteristics

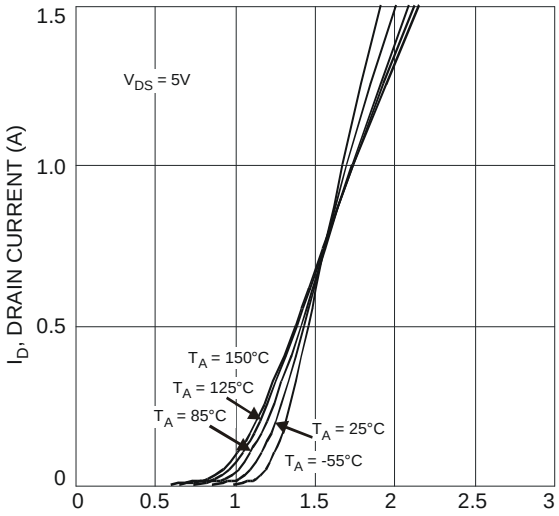


Fig. 2 Typical Transfer Characteristics

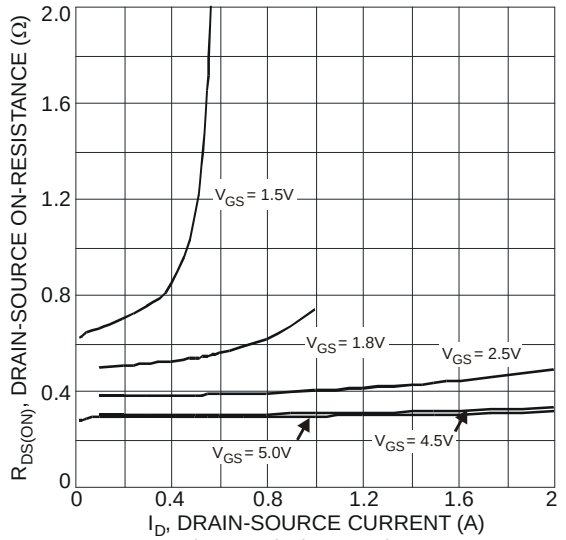


Fig. 3 Typical On-Resistance
vs. Drain Current and Gate Voltage

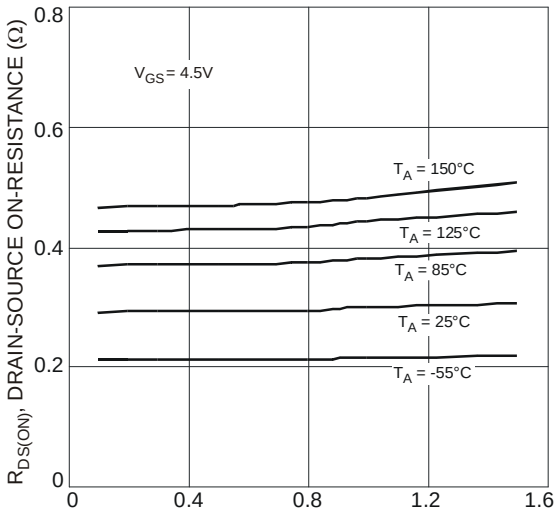


Fig. 4 Typical Drain-Source On-Resistance
vs. Drain Current and Temperature

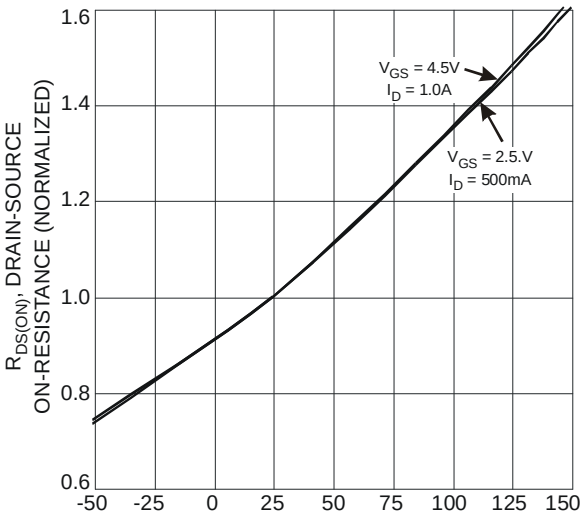


Fig. 5 On-Resistance Variation with Temperature

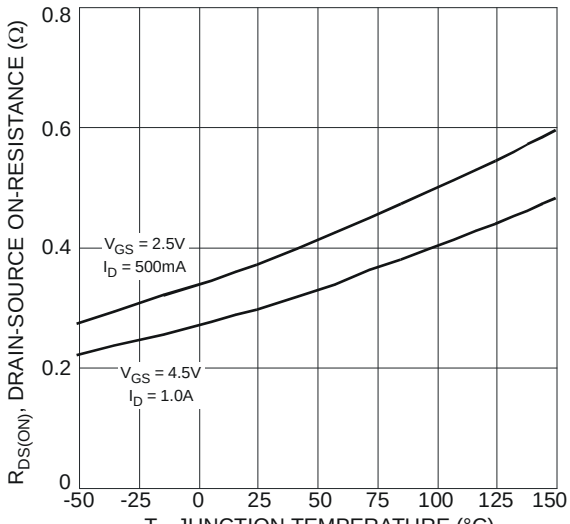


Fig. 6 On-Resistance Variation with Temperature

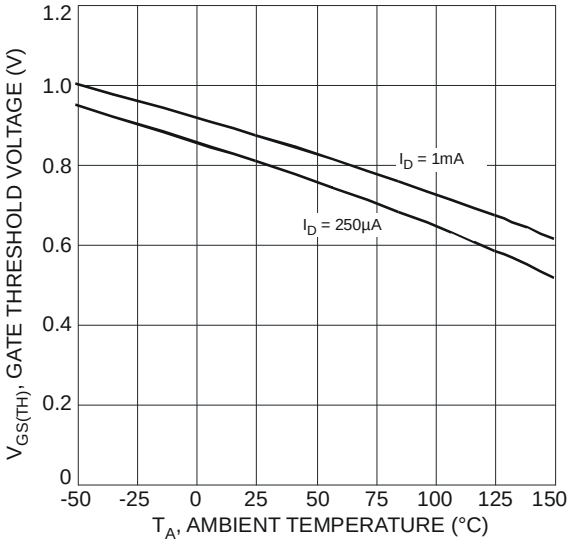


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

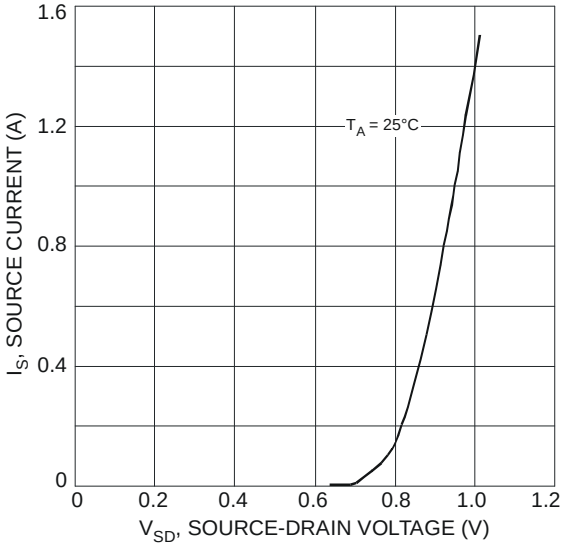


Fig. 8 Diode Forward Voltage vs. Current

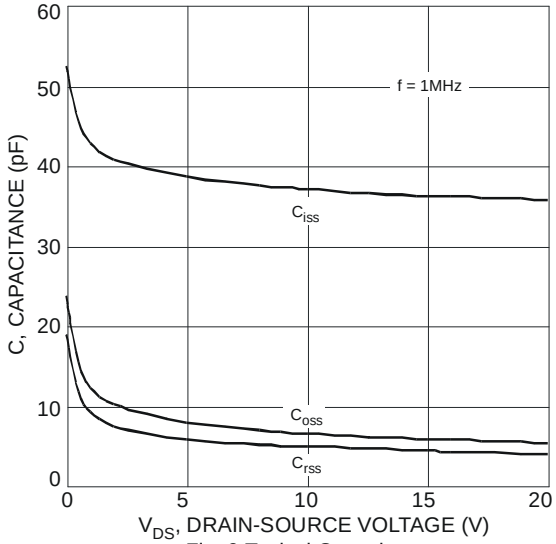


Fig. 9 Typical Capacitance

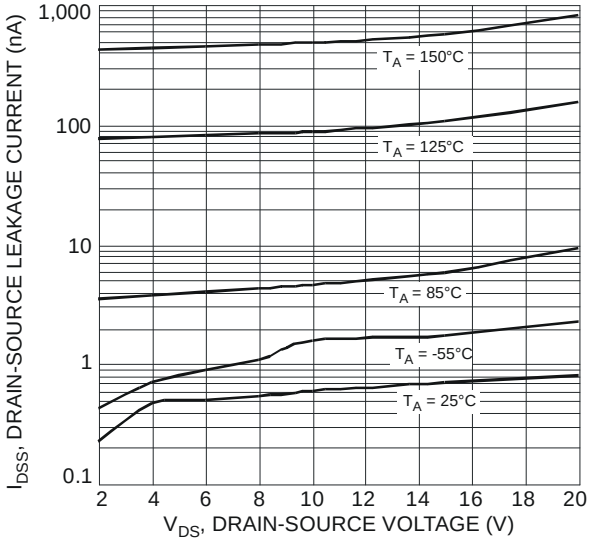


Fig. 10 Typical Drain-Source Leakage Current vs. Drain-Source Voltage

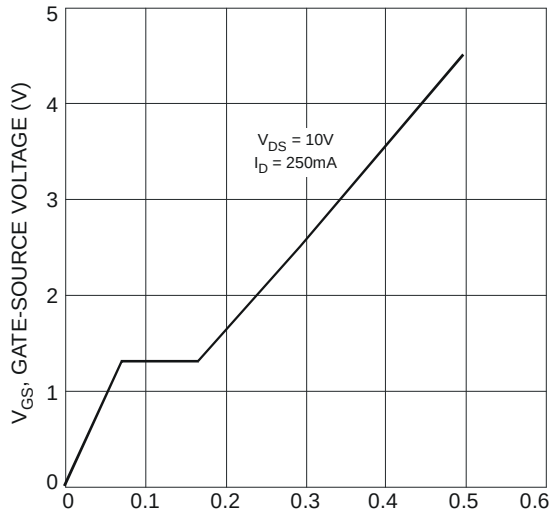


Fig. 11 Gate-Charge Characteristics

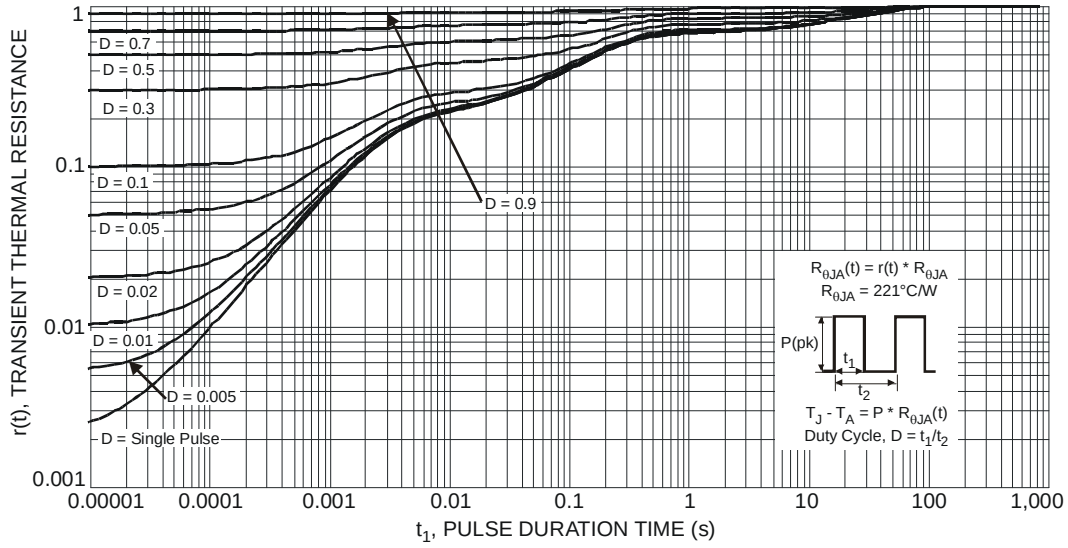
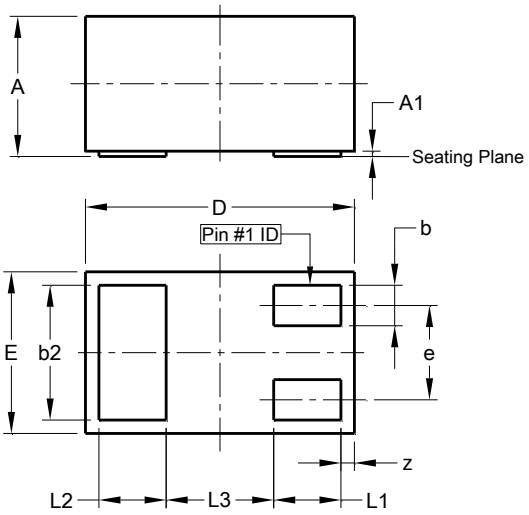


Fig. 12 Transient Thermal Response

Package Outline Dimensions

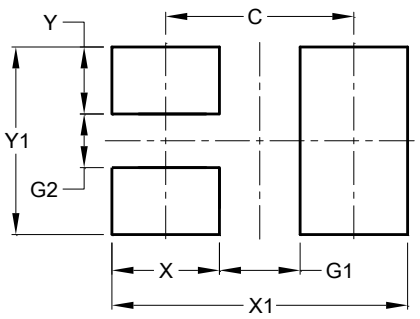
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



X1-DFN1006-3			
Dim	**Min**	**Max**	**Typ**
A	0.47	0.53	0.50
A1	0.00	0.05	0.03
b	0.10	0.20	0.15
b2	0.45	0.55	0.50
D	0.95	1.075	1.00
E	0.55	0.675	0.60
e	-	-	0.35
L1	0.20	0.30	0.25
L2	0.20	0.30	0.25
L3	-	-	0.40
z	0.02	0.08	0.05
All Dimensions in mm			

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
C	0.70
G1	0.30
G2	0.20
X	0.40
X1	1.10
Y	0.25
Y1	0.70

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