

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	-30	V
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current (Note 6) V _{GS} = -10V	Steady State	T _A = +25°C T _A = +70°C	I _D	-4.0 -3.2	A
	t < 10s	T _A = +25°C T _A = +70°C	I _D	-4.6 -3.6	A
Continuous Drain Current (Note 6) V _{GS} = -4.5V	Steady State	T _A = +25°C T _A = +70°C	I _D	-3.3 -2.6	A
	t < 10s	T _A = +25°C T _A = +70°C	I _D	-3.9 -3.1	A
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%)			I _{DM}	-30	A
Maximum Body Diode Forward Current (Note 6)			I _S	-2.0	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T _A = +25°C	P _D	1.1	W
	T _A = +70°C		0.7	
Thermal Resistance, Junction to Ambient (Note 5)	Steady state	R _{θJA}	166	°C/W
	t < 10s		118	
Total Power Dissipation (Note 6)	T _A = +25°C	P _D	1.8	W
	T _A = +70°C		1.1	
Thermal Resistance, Junction to Ambient (Note 6)	Steady state	R _{θJA}	98	°C/W
	t < 10s		71	
Thermal Resistance, Junction to Case (Note 6)		R _{θJC}	18	
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@ T_A = +25°C, unless otherwise stated.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	-30	-	-	V	V _{GS} = 0V, I _D = -250μA
Zero Gate Voltage Drain Current, T _J = +25°C	I _{DSS}	-	-	-1	μA	V _{DS} = -30V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	-	-	±100	nA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	-1.0	-1.5	-2.1	V	V _{DS} = V _{GS} , I _D = -250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	-	39	50	mΩ	V _{GS} = -10V, I _D = -4.1A
		-	56	72		V _{GS} = -4.5V, I _D = -3.0A
Forward Transfer Admittance	Y _{fs}	-	8.2	-	S	V _{DS} = -5V, I _D = -4A
Diode Forward Voltage	V _{SD}	-	-0.75	-1.1	V	V _{GS} = 0V, I _S = -1A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	466	582	700	pF	V _{DS} = -15V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	C _{oss}	80	114	148		
Reverse Transfer Capacitance	C _{rss}	47	76	105		
Gate Resistance	R _g	2	5	8	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge	Q _g	10.6	13.3	16	nC	V _{GS} = -10V, V _{DS} = -15V, I _D = -4A
Total Gate Charge	Q _g	5.2	6.5	8.5		
Gate-Source Charge	Q _{gs}	1.3	1.7	2		
Gate-Drain Charge	Q _{gd}	1.1	1.9	2.7	ns	V _{GS} = -4.5V, V _{DS} = -15V, I _D = -4A
Turn-On Delay Time	t _{D(ON)}	-	6.0	-		
Turn-On Rise Time	t _R	-	12.9	-		
Turn-Off Delay Time	t _{D(OFF)}	-	35.4	-		
Turn-Off Fall Time	t _F	-	30.7	-	ns	V _{GS} = -10V, V _{DS} = -15V, R _L = 3.6Ω, R _G = 3Ω
Reverse Recovery Time	t _{RR}	6.8	8.5	10.2		
Reverse Recovery Charge	Q _{RR}	5.5	7.0	8.5	nC	I _F = 4A, di/dt = 100A/μs

- Notes:
- Device mounted on FR-4 PCB with minimum recommended pad layout, single sided. The power dissipation P_D is based on t < 10s R_{θJA}.
 - Device mounted on 1" x 1" FR-4 PCB with high coverage 2 oz. copper, single sided. The power dissipation P_D is based on t < 10s R_{θJA}.
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to production testing.

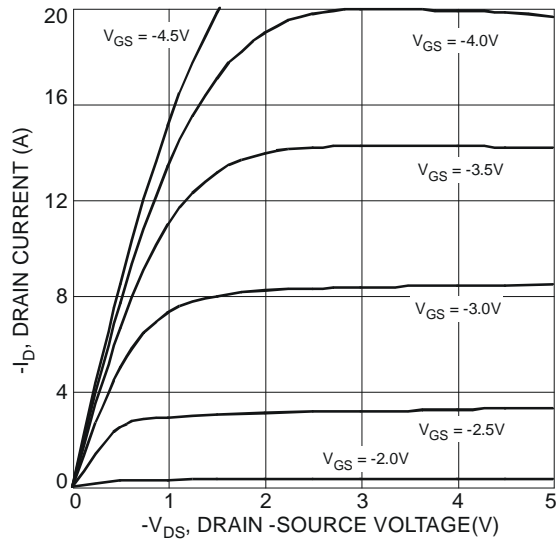


Fig. 1 Typical Output Characteristics

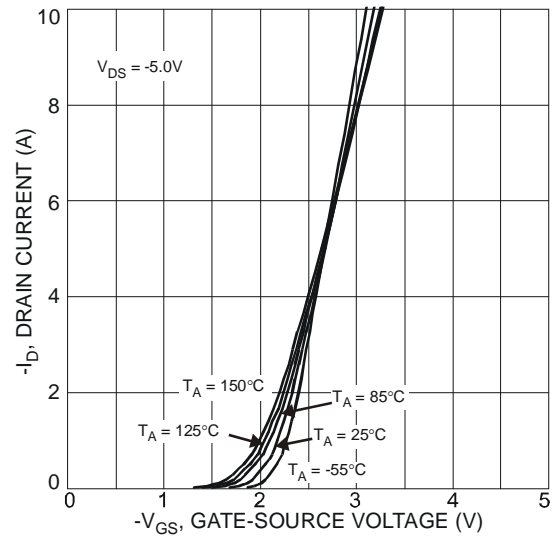


Fig. 2 Typical Transfer Characteristics

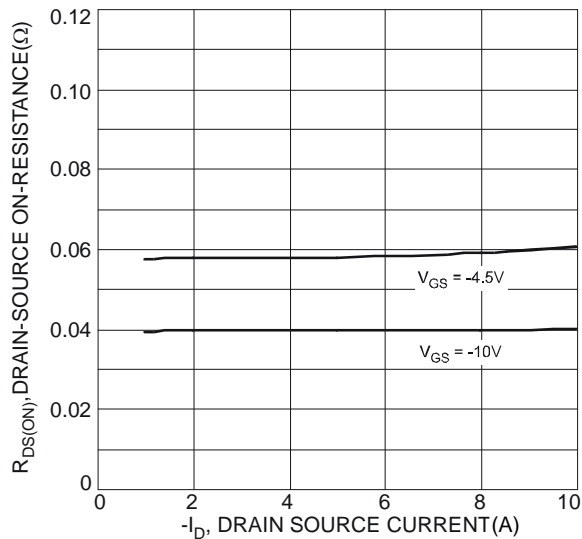


Fig. 3 Typical On-Resistance vs. Drain Current and Gate Voltage

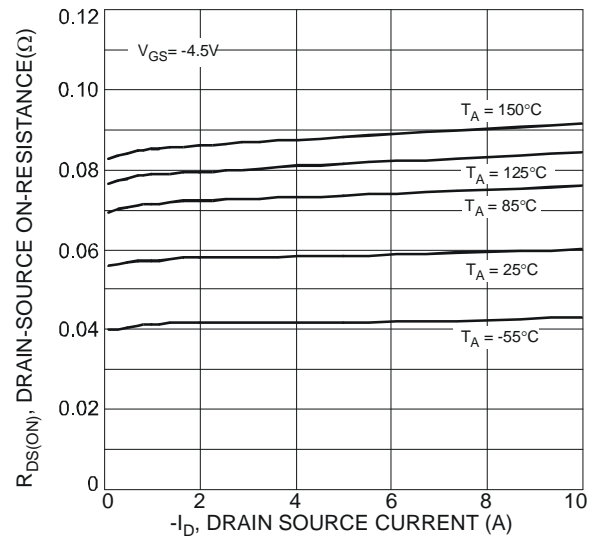


Fig. 4 Typical On-Resistance vs. Drain Current and Temperature

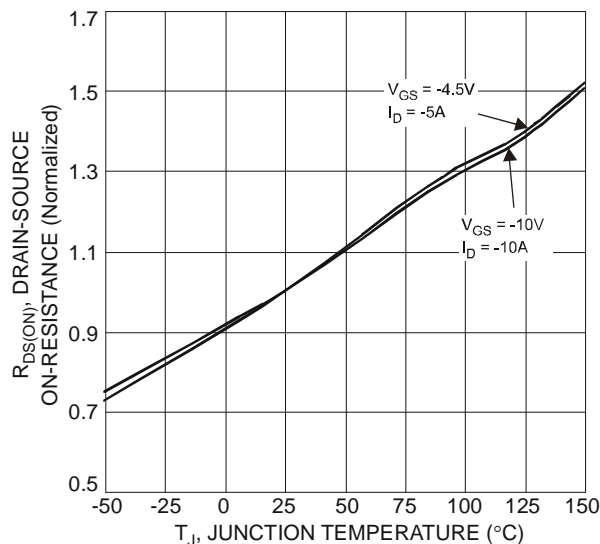


Fig. 5 On-Resistance Variation with Temperature

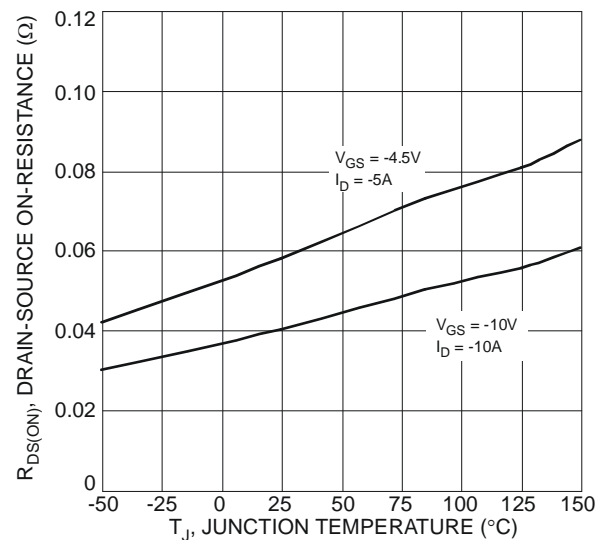


Fig. 6 On-Resistance Variation with Temperature

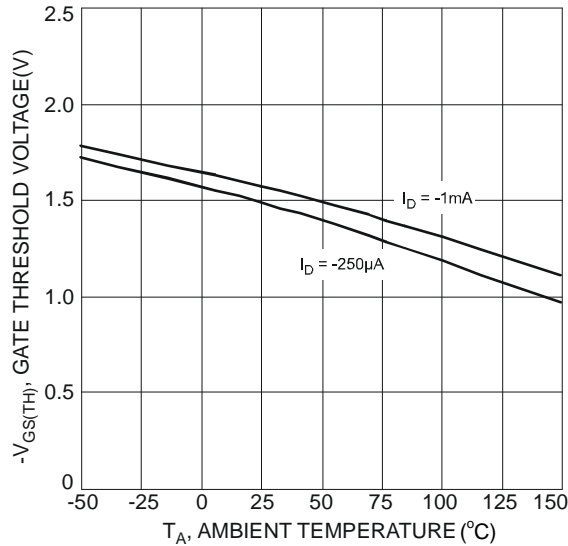


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

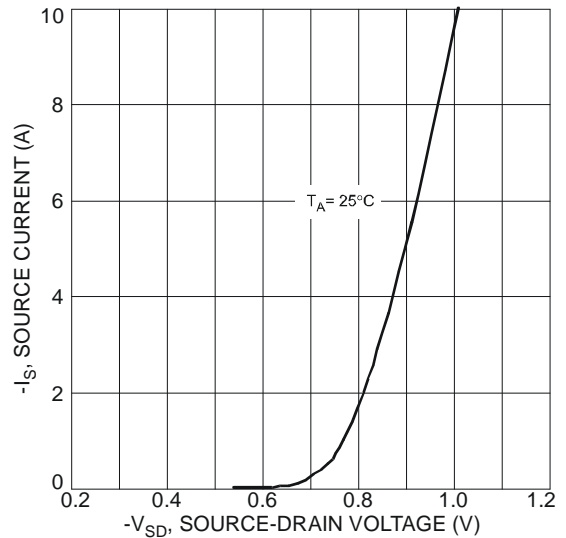


Fig. 8 Diode Forward Voltage vs. Current

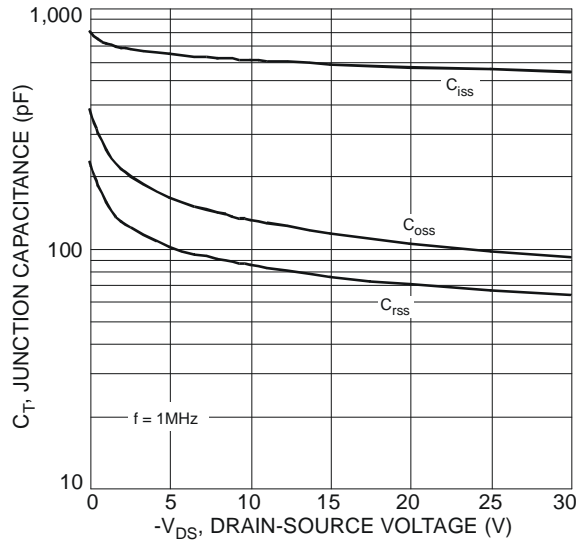


Fig. 9 Typical Junction Capacitance

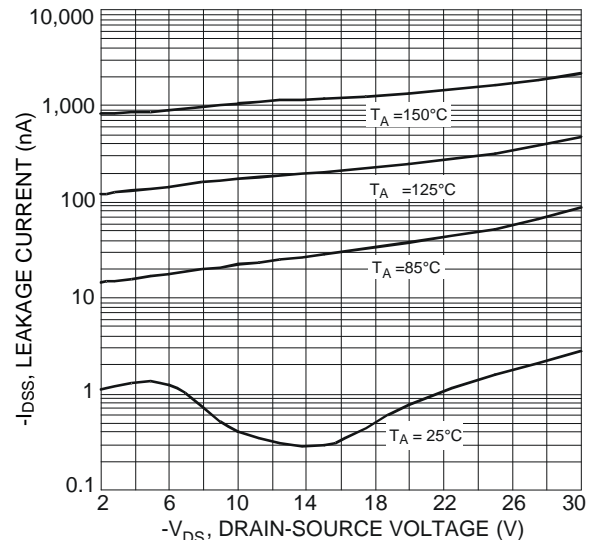


Fig. 10 Typical Drain-Source Leakage Current vs. Voltage

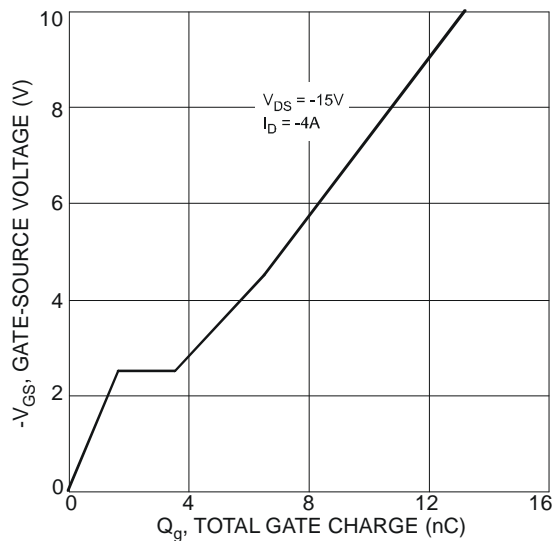


Fig. 11 Gate-Charge Characteristics

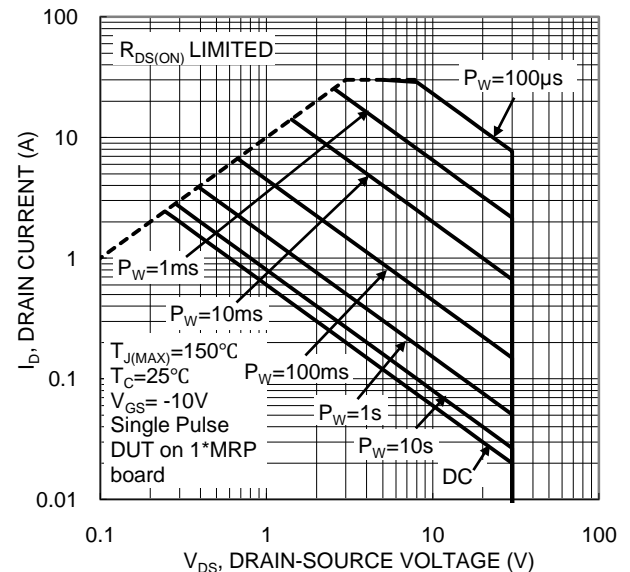


Fig. 12 SOA, Safe Operation Area

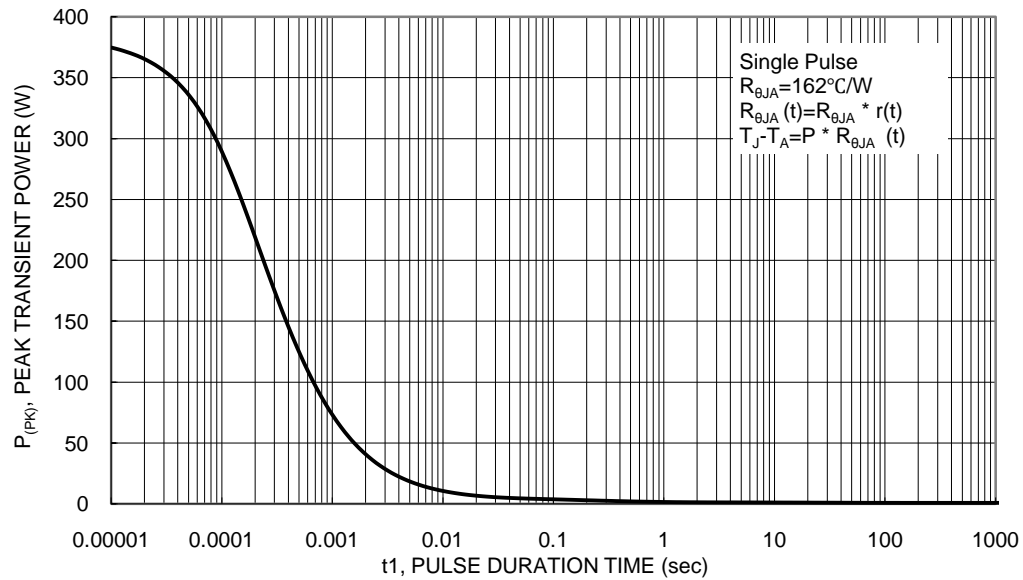


Fig. 13 Single Pulse Maximum Power Dissipation

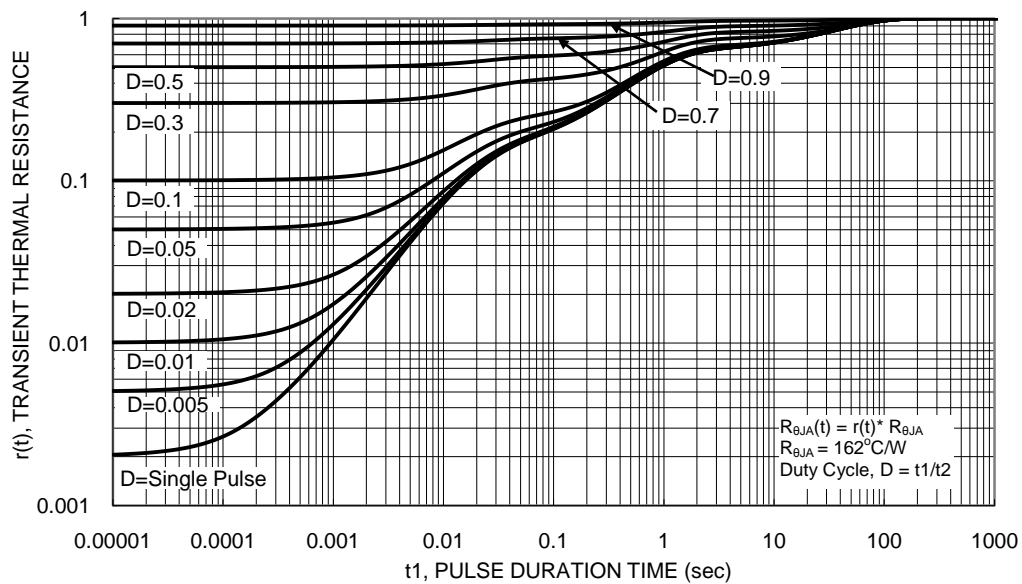
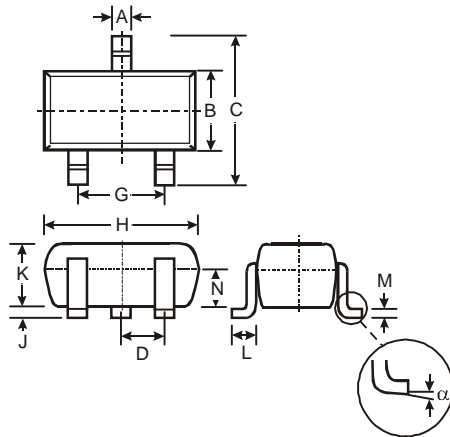


Fig. 14 Transient Thermal Resistance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

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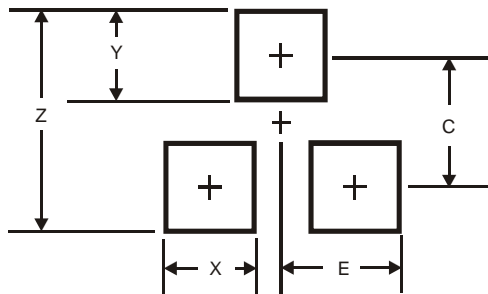


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Dim	Min	Max	Typ
A	0.35	0.50	0.38
B	1.50	1.70	1.60
C	2.70	3.00	2.80
D	-	-	0.95
G	-	-	1.90
H	2.90	3.10	3.00
J	0.013	0.10	0.05
K	1.00	1.30	1.10
L	0.35	0.55	0.40
M	0.10	0.20	0.15
N	0.70	0.80	0.75
α	0°	8°	-
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

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Dimensions	Value (in mm)
Z	3.4
X	0.8
Y	1.0
C	2.4
E	1.35

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