Characteristics CPL-WB-01D3

## 1 Characteristics

Table 1. Absolute maximum rating (limiting values)

Symbol	Parameter		Value			
	Farameter	Min.	Тур.	Max.	Unit	
P <sub>IN</sub>	Input power RF <sub>IN</sub>			35	dBm	
V <sub>ESD (IEC)</sub>	ESD ratings IEC 61000-4-2 (C = 150 pF, R = 330 $\Omega$ , 10 shots with both polarities and each condition, cumulative method) RF <sub>IN</sub> , RF <sub>OUT</sub> , air discharge RF <sub>IN</sub> , RF <sub>OUT</sub> , contact discharge	±15 ±8			kV kV	
V <sub>ESD (HBM)</sub>	Human body model, JESD22-A114-B, All I/O	2			kV	
V <sub>ESD (MM)</sub>	Machine model, JESD22-A115-A, All I/O				V	
V <sub>ESD (CDM)</sub>	Charge device model, JESD22-C101-C, All I/O	500			V	
T <sub>OP</sub>	Operating temperature	-30		+85	∘C	

Table 2. Electrical characteristics ( $T_{amb} = 25$  °C) - impedances

Symbol	Parameter		Value		
			Тур.	Max.	Unit
Z <sub>OUT</sub>	Nominal output impedance		50		Ω
Z <sub>IN</sub>	Nominal input impedance		50		Ω
Z <sub>CPLD</sub>	Z <sub>CPLD</sub> Nominal coupling impedance		50		Ω
Z <sub>ISO</sub>	Nominal ISO impedance		50		Ω

CPL-WB-01D3 Characteristics

Table 3. Electrical characteristics ( $T_{amb} = 25$  °C) - RF performance

Symbol	Parameter	Test condition			Value		
Cymbol	T drumeter	rest condition	Min. Typ. Max			Unit	
T <sub>OP</sub>	Operating temperature		-30		+85	°C	
f	Frequency range (bandwidth)		824		2170	MHz	
IL	Insertion loss in bandwidth	From 824 MHz to 2170 MHz		0.1	0.2	dB	
$R_{L}$	Return loss in bandwidth	From 824 MHz to 2170 MHz	15			dB	
CPLD	Coupling factor	From 824 MHz to 915 MHz	24	26	27	dB	
		From 1710 MHz to 2025 MHz	18	19	21	dB	
Ripple	Coupling ripple in individual band	(824 to 849 MHz) - (880 to 915 MHz) (1710 to 1785 MHz) - (1850 to 1910 MHz) (1880 to 2025 MHz) - (1920 to 1980 MHz)			0.5	dB	
DIR	Coupler directivity	From 824 MHz to 2025 MHz	15	20		dB	

Characteristics CPL-WB-01D3

#### 1.1 RF measurement

Figure 2. Insertion loss

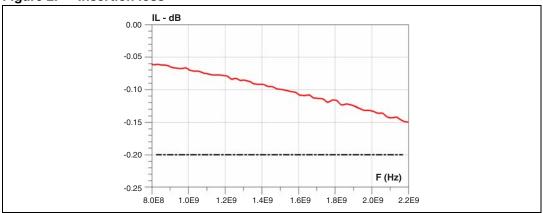


Figure 3. Coupling LB

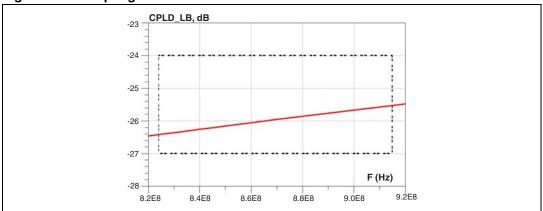
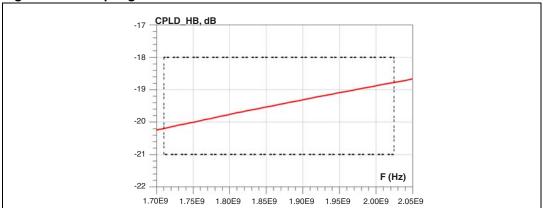


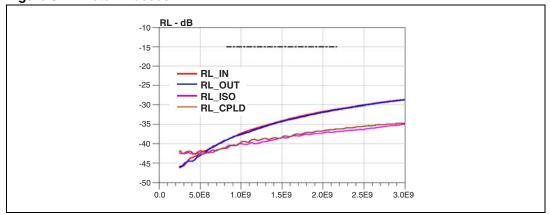
Figure 4. Coupling HB



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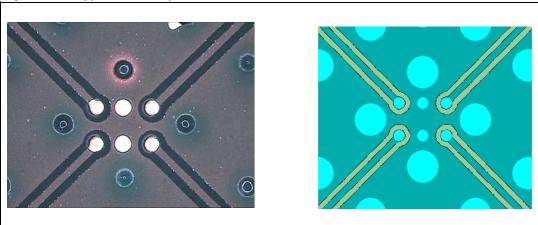
CPL-WB-01D3 Characteristics

Figure 5. Return losses



## 2 PCB layout recommendation

Figure 6. Typical PCB layout recommendation



CPL-WB-01D3

- Material: 4 layers FR4 with solder mask on top and bottom layer
- Substrate thickness: 0.8 mm
- 50  $\Omega$  line access
- Ground plane must be on PCB layer 1 as shown in Figure 6

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CPL-WB-01D3 Package information

### 3 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: <a href="www.st.com">www.st.com</a>. ECOPACK<sup>®</sup> is an ST trademark.

Figure 7. Package dimensions

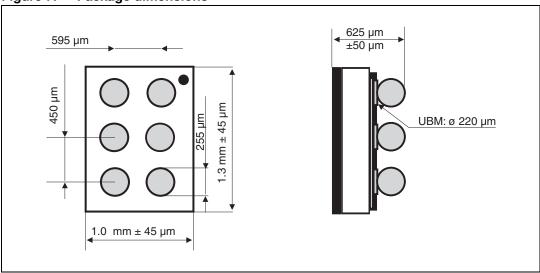


Figure 9. Marking

Copper pad diameter:
220 µm recommended
260 µm maximum

Solder mask opening:
300 µm minimum

Solder stencil opening:
220 µm recommended

Solder stencil opening:
220 µm recommended

Solder stencil opening:
220 µm recommended

Package information CPL-WB-01D3

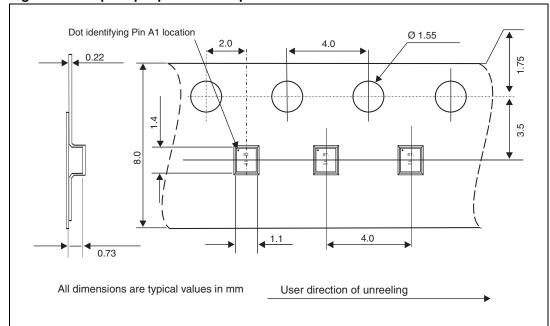


Figure 10. Flip Chip tape and reel specifications

Note: More information is available in the application note:

AN2348: "Flip Chip: package description and recommendations for use"

# 4 Ordering information

Table 4. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
CPL-WB-01D3	RV	Flip Chip	1.61 mg	5000	Tape and reel

# 5 Revision history

Table 5. Document revision history

Date	Revision	Changes
08-Jul-2011	1	Initial release
12-Sep-2011	2	Updated Figure 2, Figure 3, and Figure 4.
14-Feb-2012	3	Updated Figure 8.

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