

CPH6614

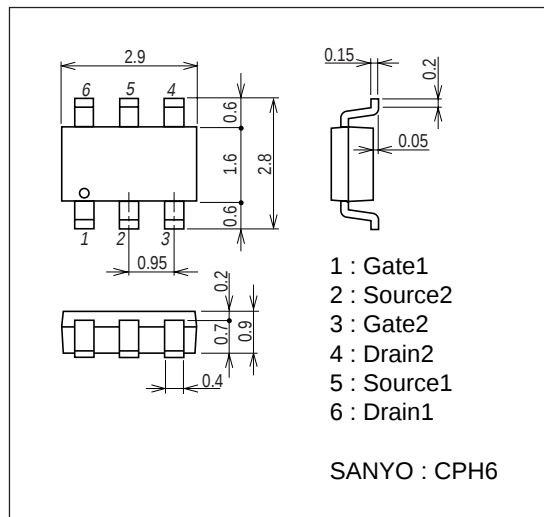
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Turn-ON Delay Time	$t_{d(on)}$	See specified Test Circuit.		6.2		ns
Rise Time	t_r	See specified Test Circuit.		4.5		ns
Turn-OFF Delay Time	$t_{d(off)}$	See specified Test Circuit.		13		ns
Fall Time	t_f	See specified Test Circuit.		6.4		ns
Total Gate Charge	Q_g	$V_{DS}=10V, V_{GS}=10V, I_D=1.8A$		3.2		nC
Gate-to-Source Charge	Q_{gs}	$V_{DS}=10V, V_{GS}=10V, I_D=1.8A$		0.74		nC
Gate-to-Drain "Miller" Charge	Q_{gd}	$V_{DS}=10V, V_{GS}=10V, I_D=1.8A$		0.42		nC
Diode Forward Voltage	V_{SD}	$I_S=1.8A, V_{GS}=0$		0.93	1.2	V
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Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D=-1mA, V_{GS}=0$	-30			V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS}=-30V, V_{GS}=0$			-1	μA
Gate-to-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 16V, V_{DS}=0$			± 10	μA
Cutoff Voltage	$V_{GS(off)}$	$V_{DS}=-10V, I_D=-1mA$	-1.2		-2.6	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS}=-10V, I_D=-0.6A$	0.6	1.0		S
Static Drain-to-Source On-State Resistance	$R_{DS(on)1}$	$I_D=-0.6A, V_{GS}=-10V$		320	420	$m\Omega$
	$R_{DS(on)2}$	$I_D=-0.3A, V_{GS}=-4V$		590	830	$m\Omega$
Input Capacitance	C_{iss}	$V_{DS}=-10V, f=1MHz$		104		pF
Output Capacitance	C_{oss}	$V_{DS}=-10V, f=1MHz$		22		pF
Reverse Transfer Capacitance	C_{rss}	$V_{DS}=-10V, f=1MHz$		17		pF
Turn-ON Delay Time	$t_{d(on)}$	See specified Test Circuit.		12.5		ns
Rise Time	t_r	See specified Test Circuit.		24		ns
Turn-OFF Delay Time	$t_{d(off)}$	See specified Test Circuit.		12		ns
Fall Time	t_f	See specified Test Circuit.		12.2		ns
Total Gate Charge	Q_g	$V_{DS}=-10V, V_{GS}=-10V, I_D=-1.2A$		3.3		nC
Gate-to-Source Charge	Q_{gs}	$V_{DS}=-10V, V_{GS}=-10V, I_D=-1.2A$		0.48		nC
Gate-to-Drain "Miller" Charge	Q_{gd}	$V_{DS}=-10V, V_{GS}=-10V, I_D=-1.2A$		0.45		nC
Diode Forward Voltage	V_{SD}	$I_S=-1.2A, V_{GS}=0$		-0.91	-1.5	V

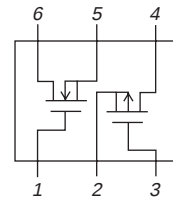
Package Dimensions

unit : mm

2238



Electrical Connection

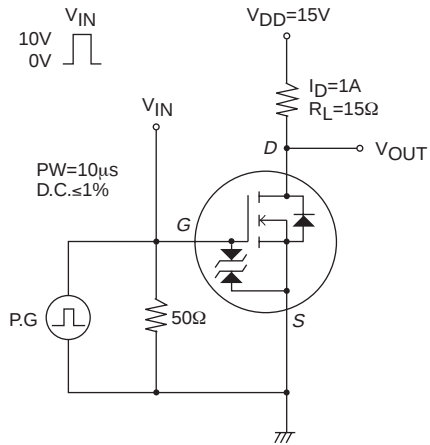


- 1 : Gate1
- 2 : Source2
- 3 : Gate2
- 4 : Drain2
- 5 : Source1
- 6 : Drain1

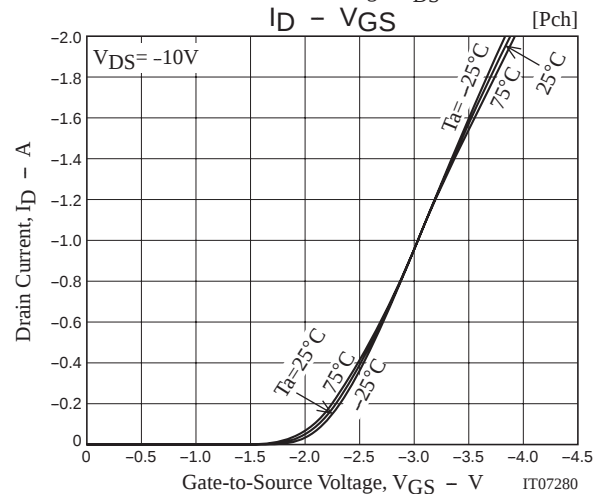
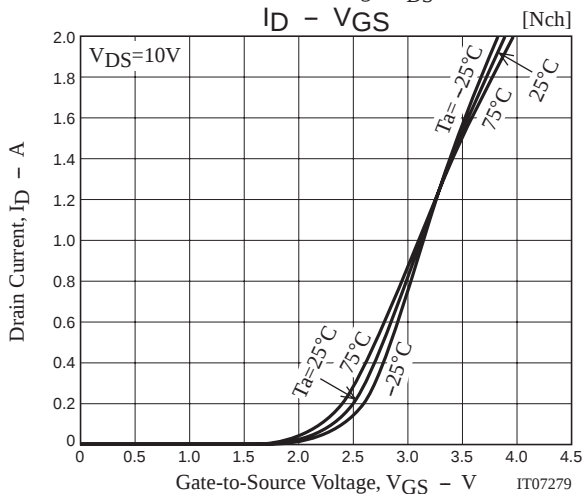
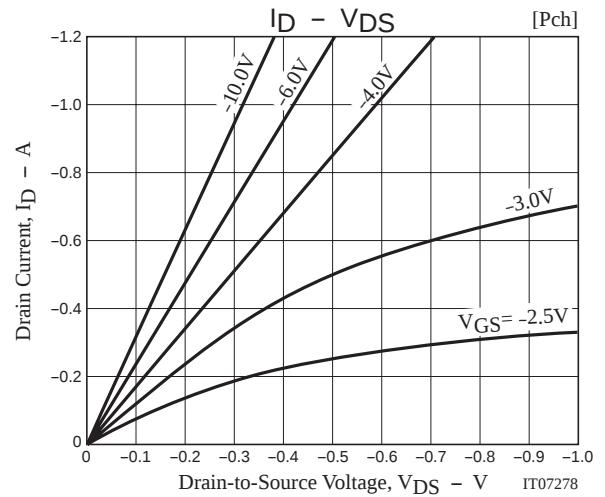
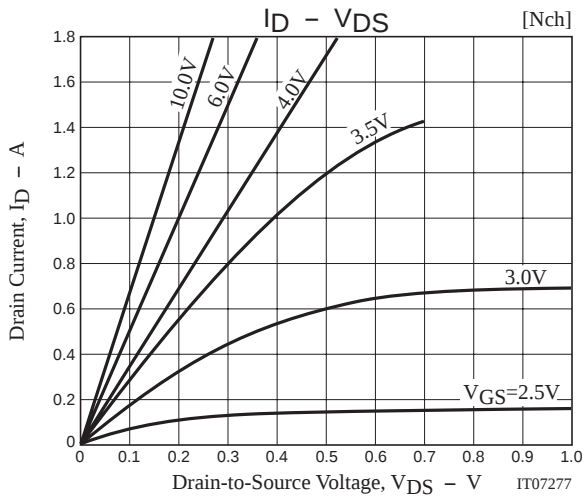
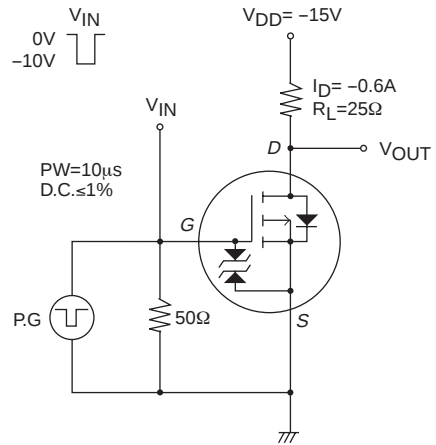
Top view

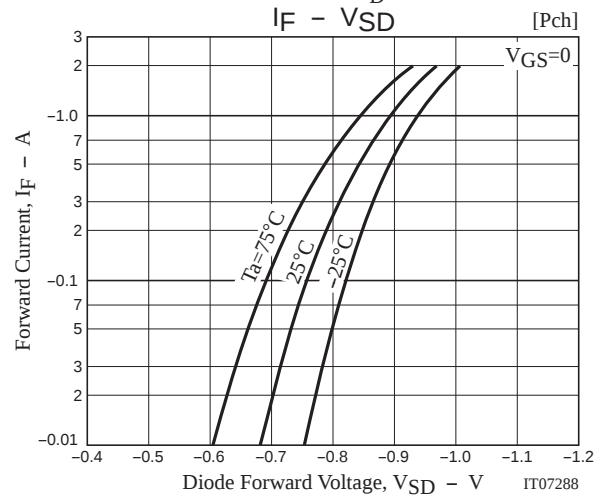
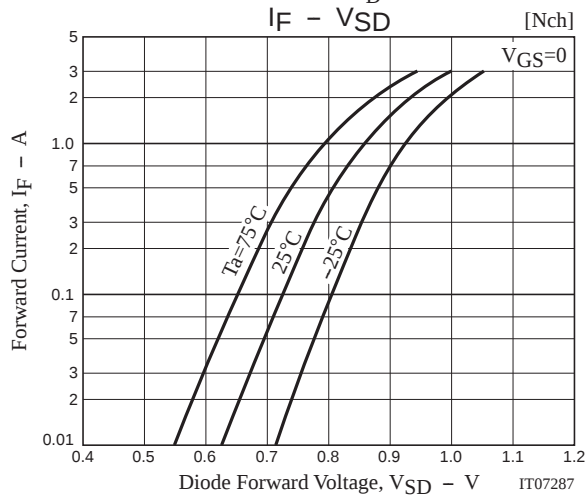
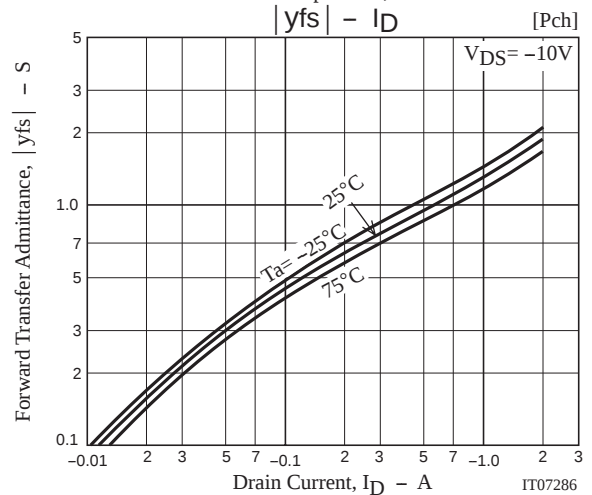
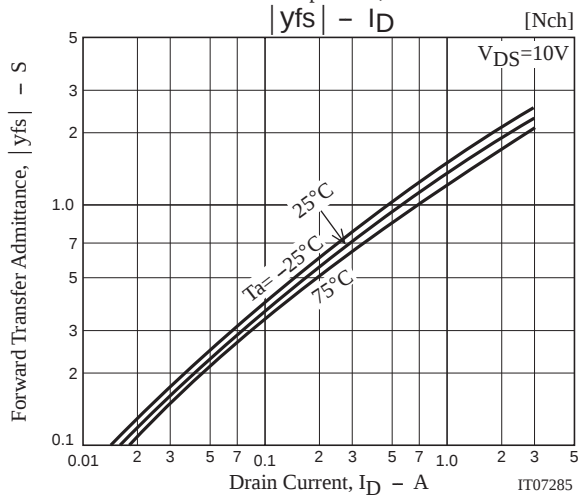
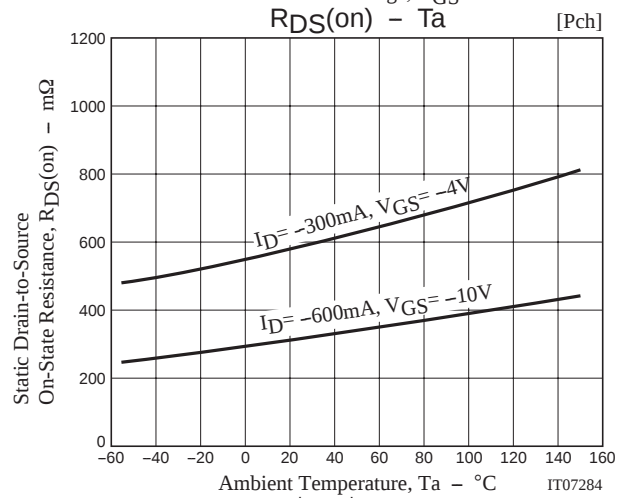
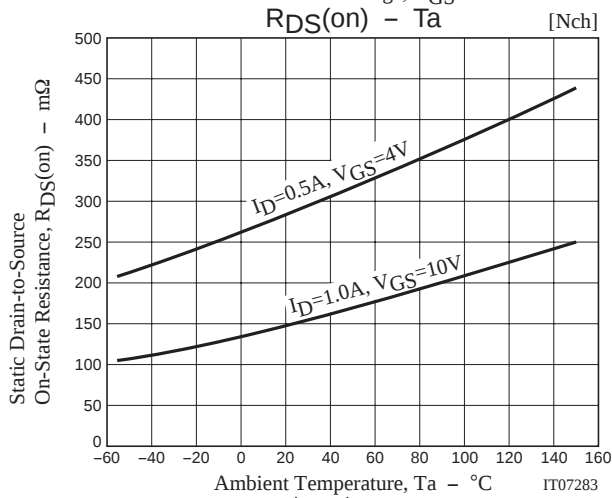
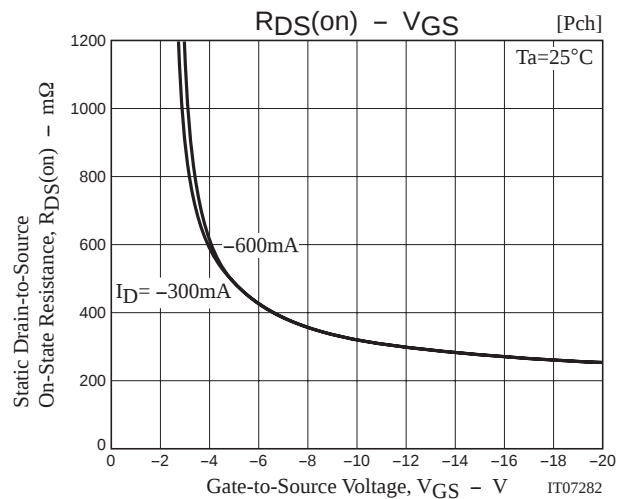
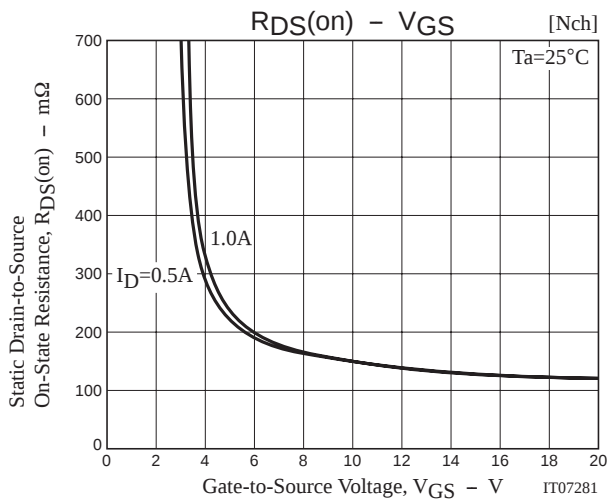
Switching Time Test Circuit

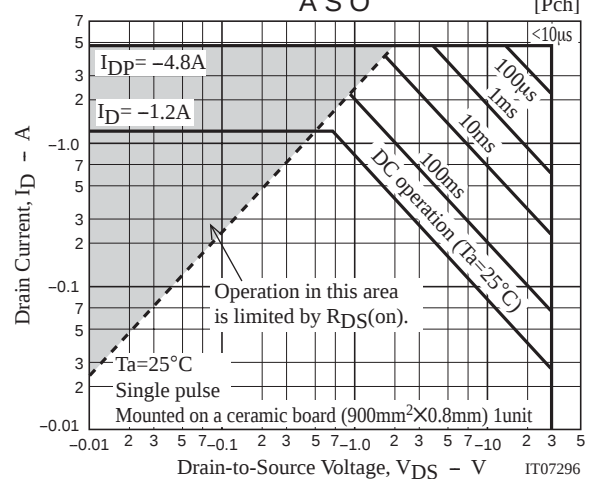
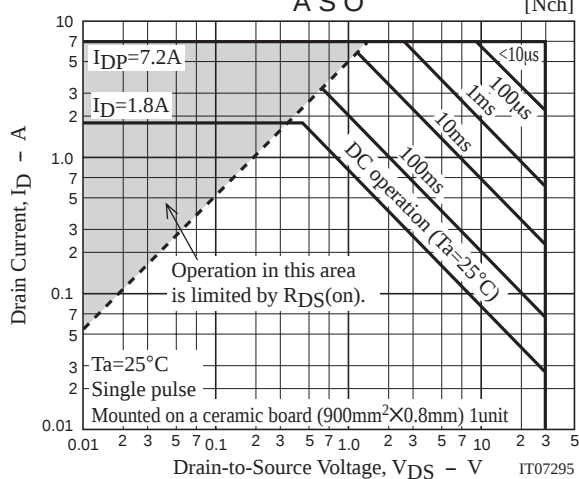
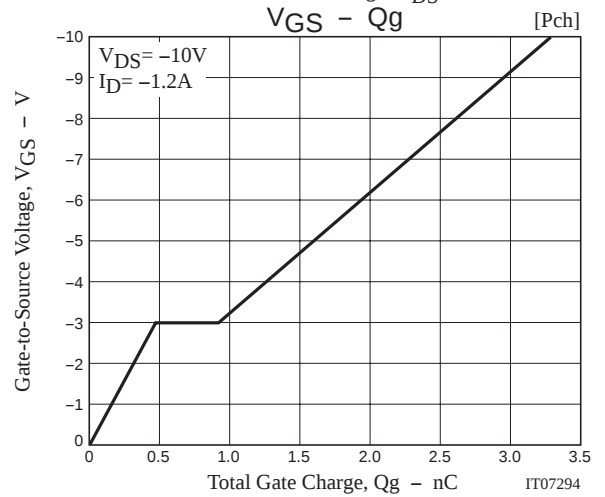
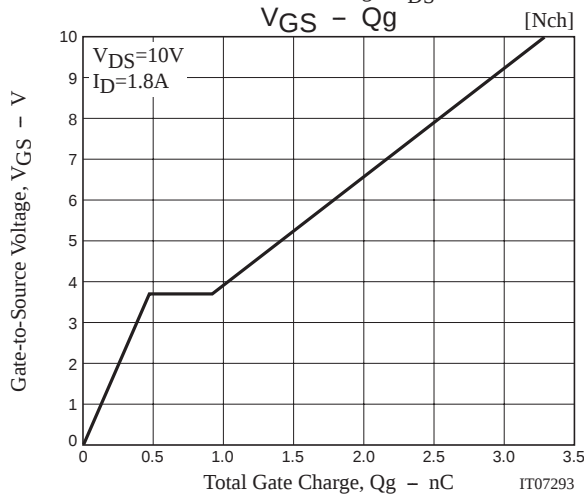
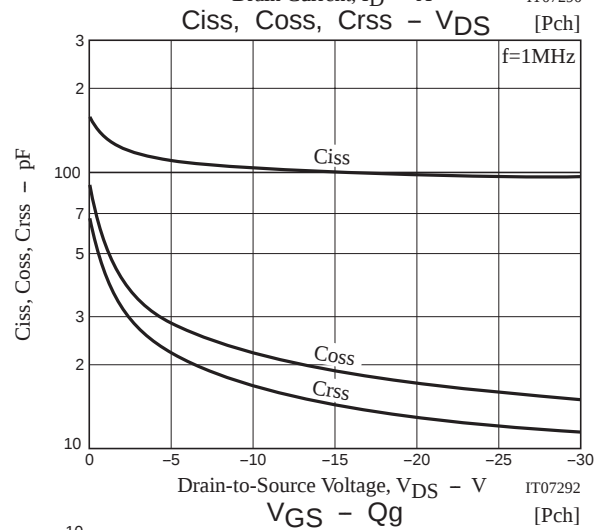
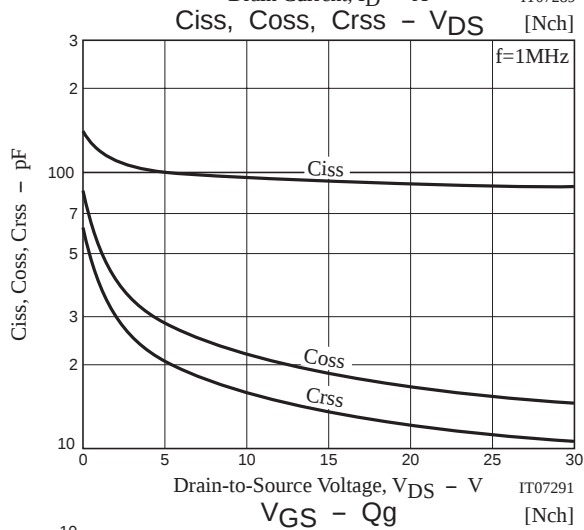
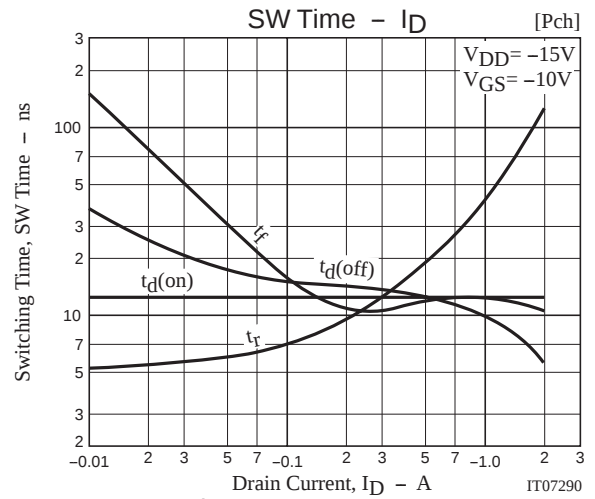
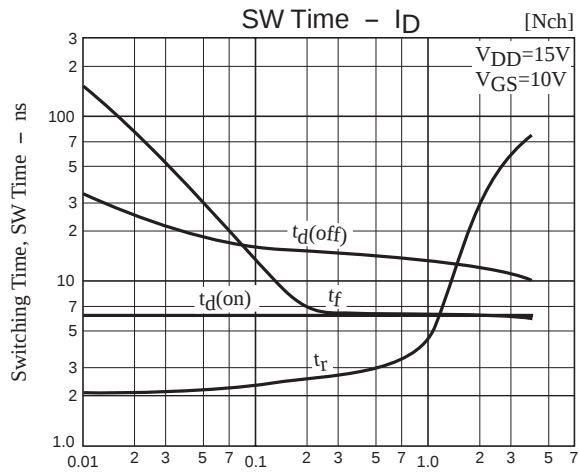
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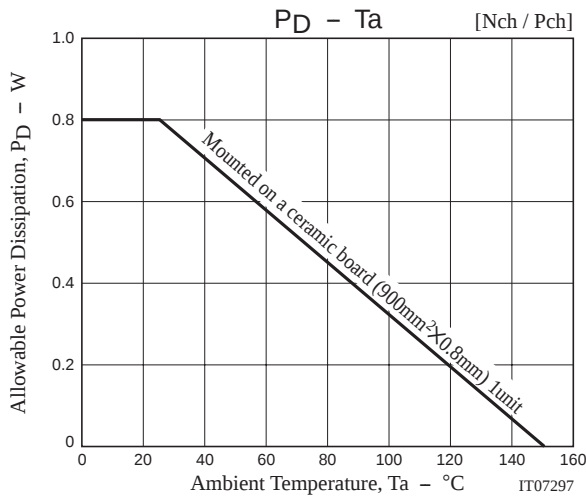


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