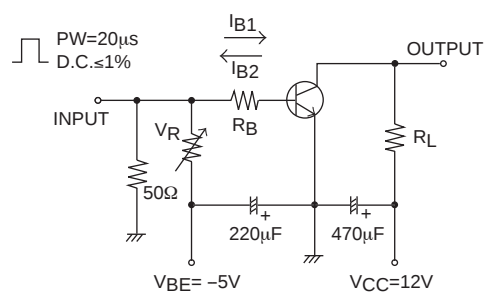


Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector Cutoff Current	ICBO	V _{CB} =(-)30V, I _E =0A			(-)100	nA
Emitter Cutoff Current	IEBO	V _{EB} =(-)4V, I _C =0A			(-)100	nA
DC Current Gain	h _{FE}	V _{CE} =(-)2V, I _C =(-)50mA	(200)300		(500)800	
Gain-Bandwidth Product	f _T	V _{CE} =(-)2V, I _C =(-)50mA		(520)540		MHz
Output Capacitance	Cob	V _{CB} =(-)10V, f=1MHz		(4.7)3.3		pF
Collector-to-Emitter Saturation Voltage	V _{CE(sat)}	I _C =(-)200mA, I _B =(-)10mA		(-110)85	(-220)190	mV
Base-to-Emitter Saturation Voltage	V _{BE(sat)}	I _C =(-)200mA, I _B =(-)10mA		(-)0.9	(-)1.2	V
Collector-to-Base Breakdown Voltage	V _{(BR)CBO}	I _C =(-)10μA, I _E =0A	(-30)40			V
Collector-to-Emitter Breakdown Voltage	V _{(BR)CEO}	I _C =(-)1mA, R _{BE} =∞	(-)30			V
Emitter-to-Base Breakdown Voltage	V _{(BR)EBO}	I _E =(-)10μA, I _C =0A	(-)5			V
Turn-On Time	t _{on}	See specified Test Circuit.		35		ns
Storage Time	t _{stg}			(125)255		ns
Fall Time	t _f			(25)40		ns

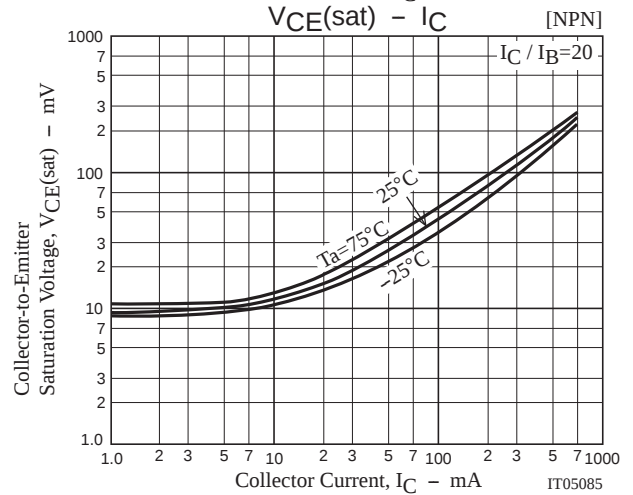
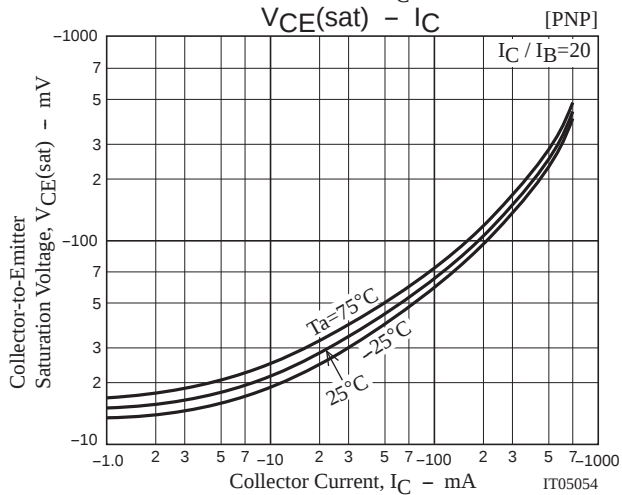
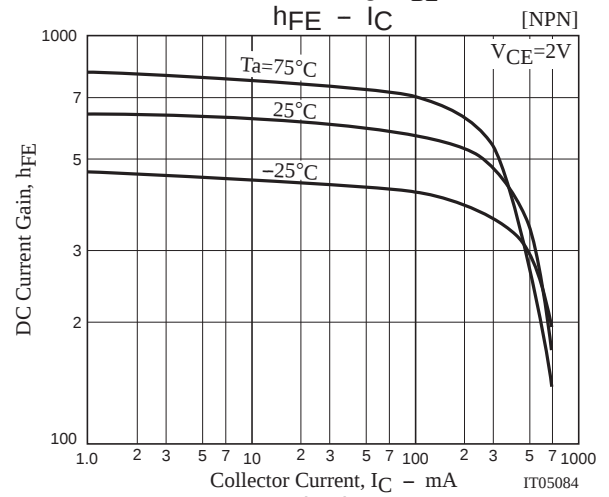
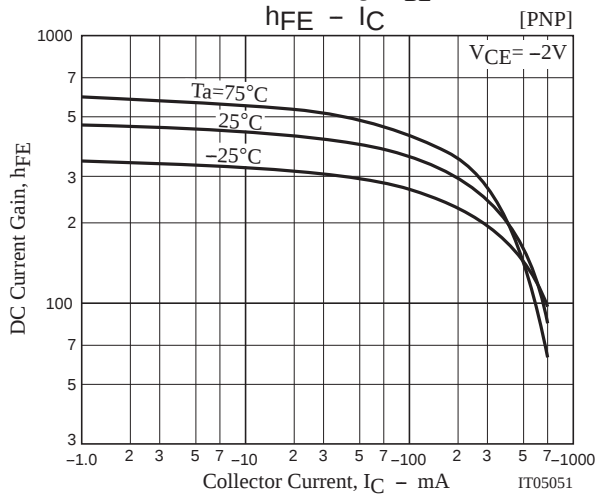
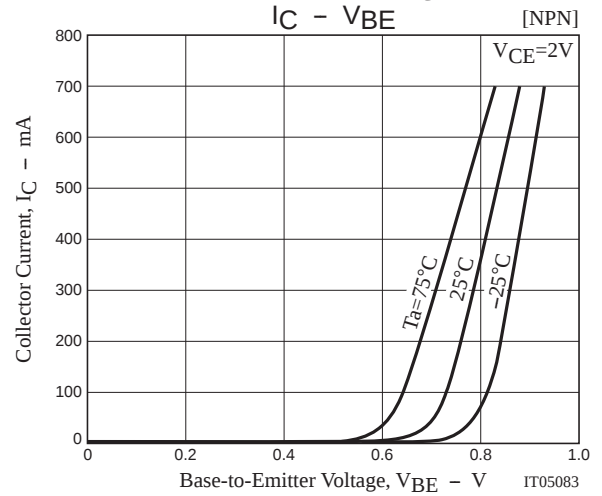
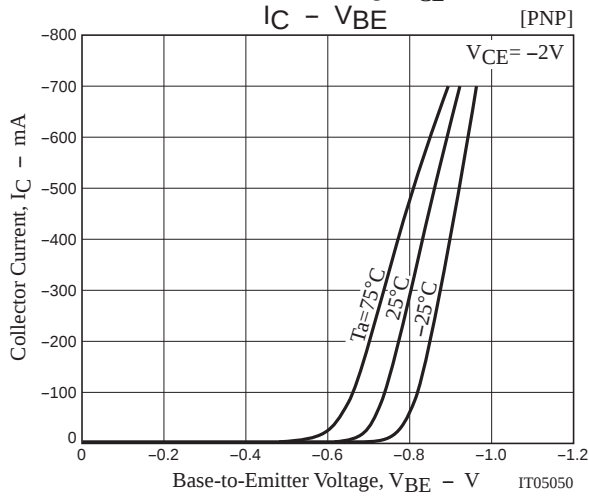
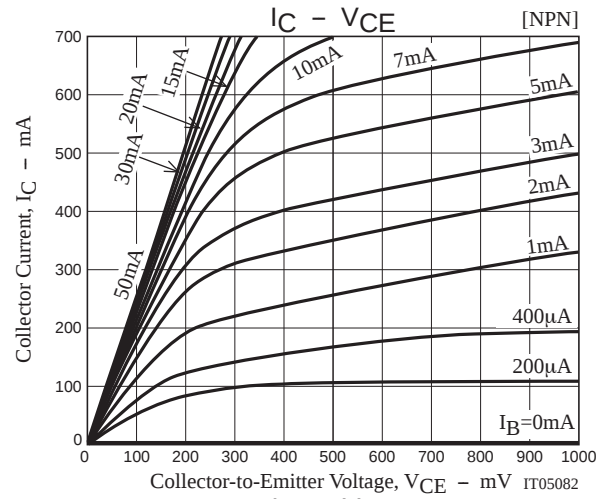
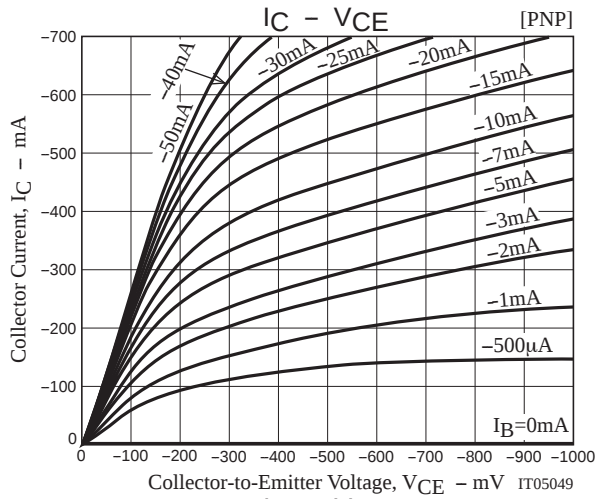
Switching Time Test Circuit

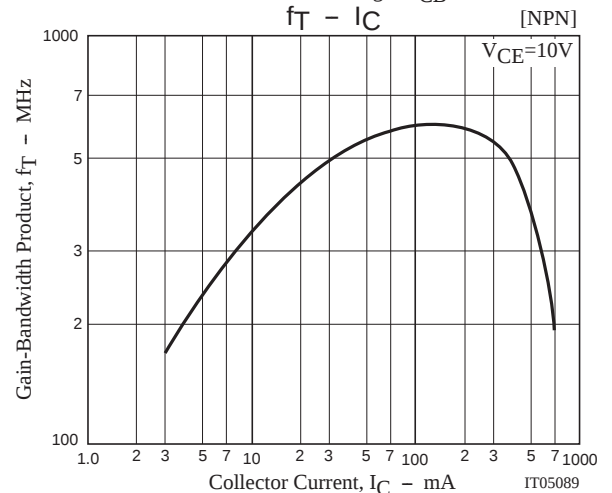
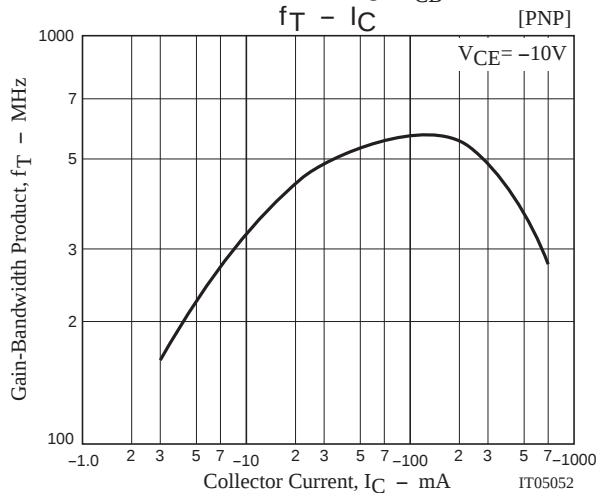
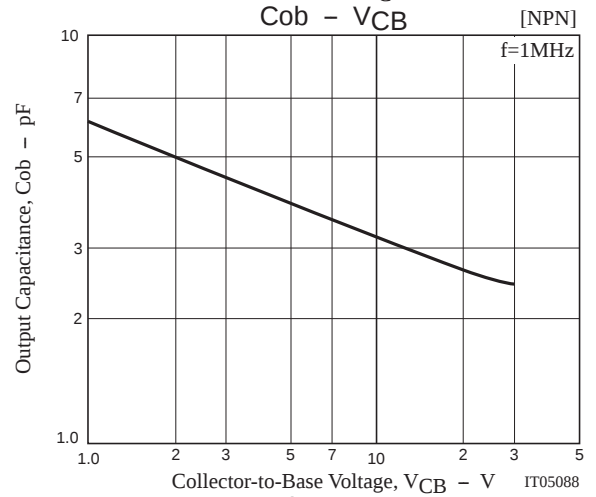
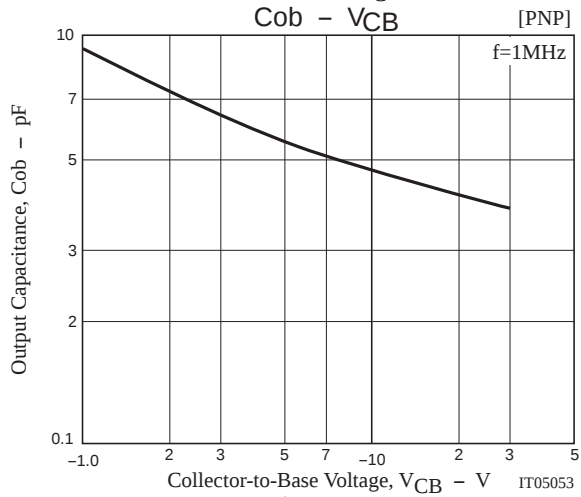
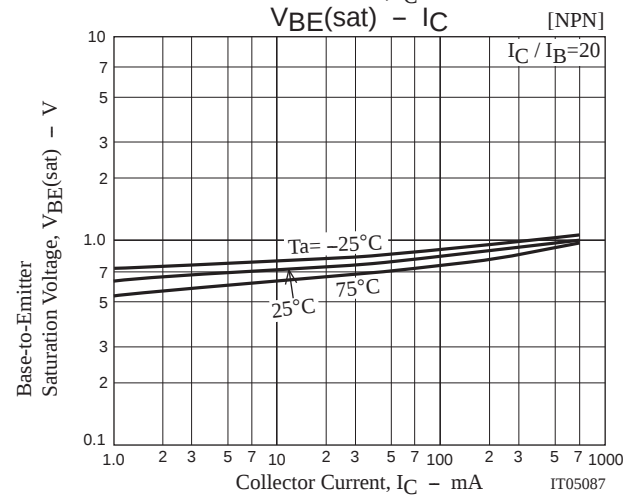
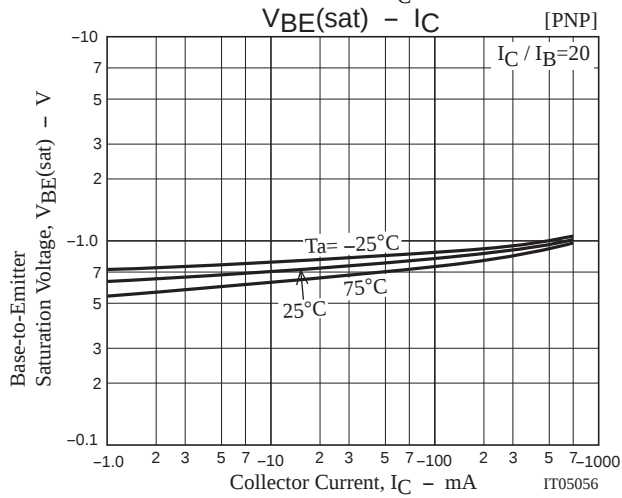
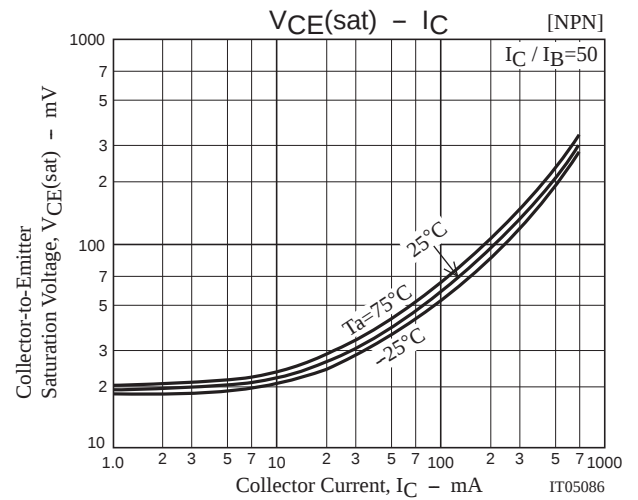
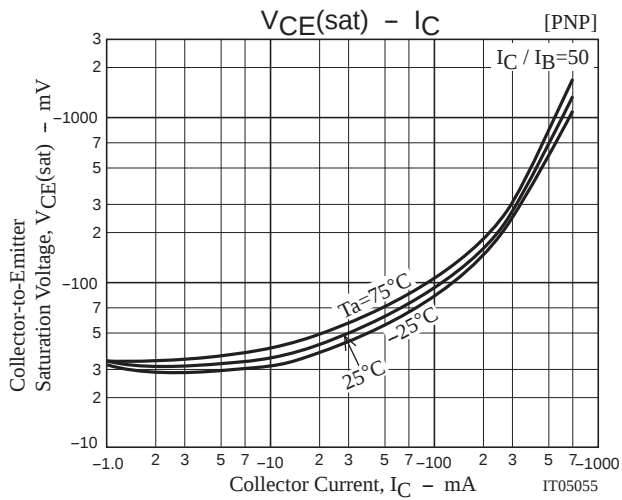


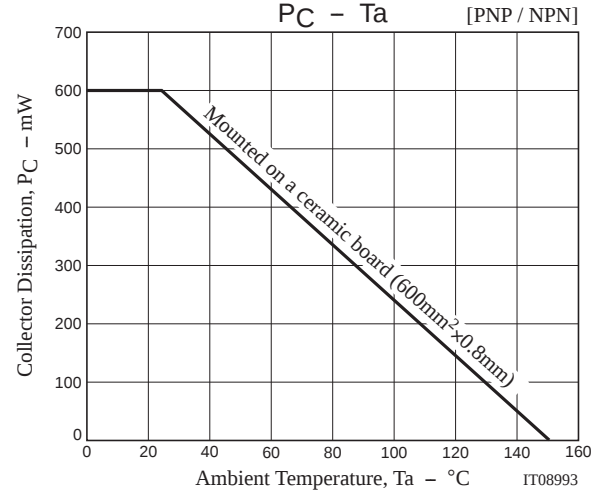
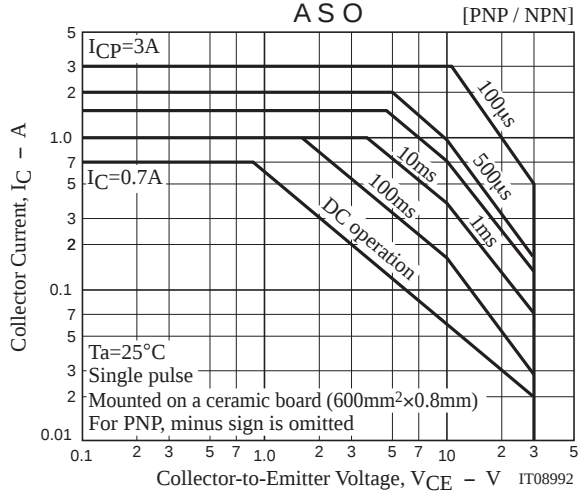
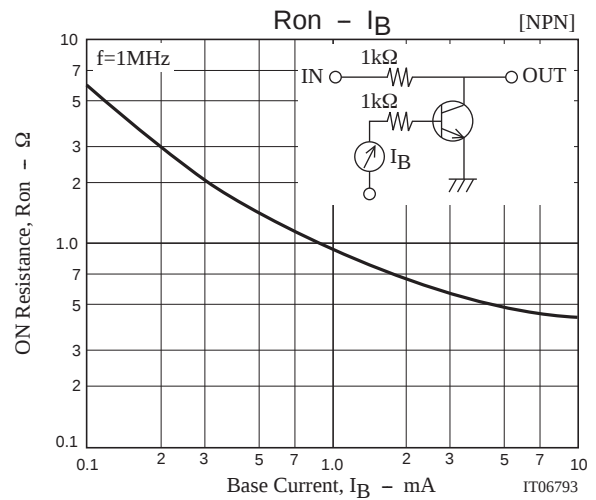
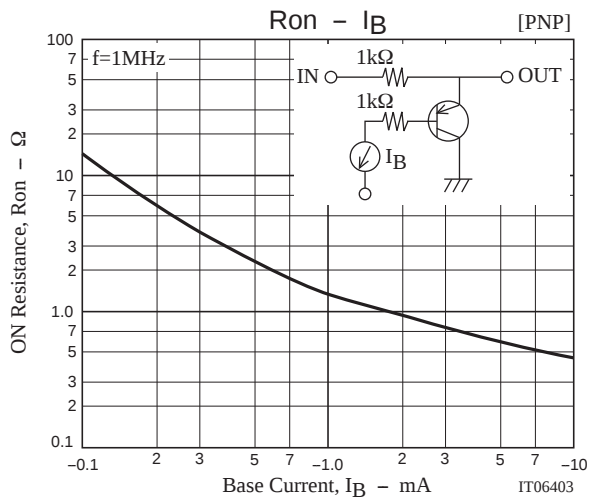
20I_{B1} = -20I_{B2} = I_C = 300mA
For PNP, minus sign is omitted.

Ordering Information

Device	Package	Shipping	memo
CPH5541-TL-E	CPH5	3,000pcs./reel	Pb Free







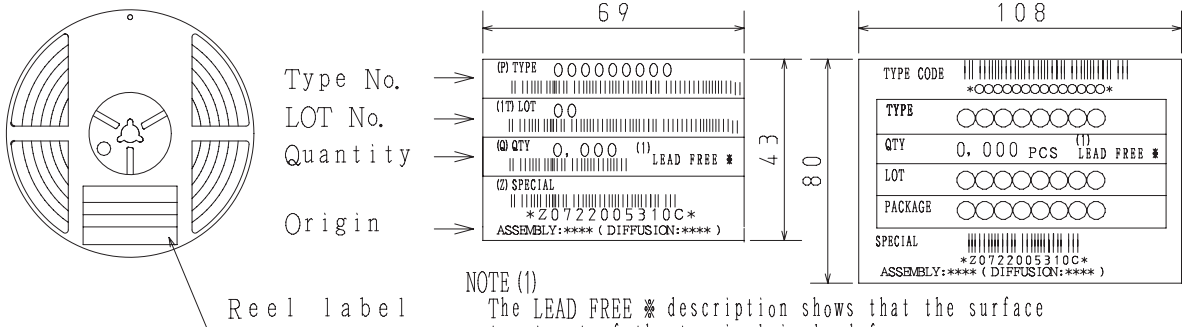
Embossed Taping Specification

CPH5541-TL-E

1. Packing Format

Package Name	Carrier Tape Type	Maximum Number of devices contained (pcs)			Packing format	
		Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)
CPH5	CPH6	3,000	15,000	90,000	5 reels contained Dimensions:mm (external) 183×72×185	6 inner boxes contained Dimensions:mm (external) 440×195×210

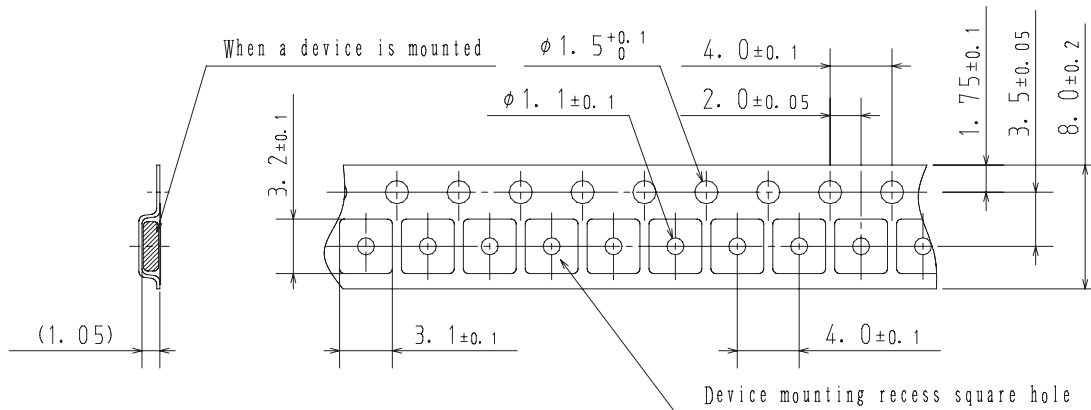
Packing method



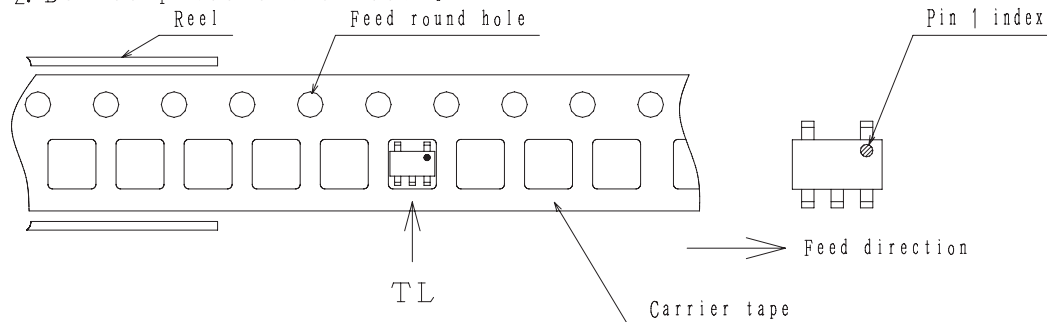
Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

2. Taping configuration

2-1. Carrier tape size (unit:mm)

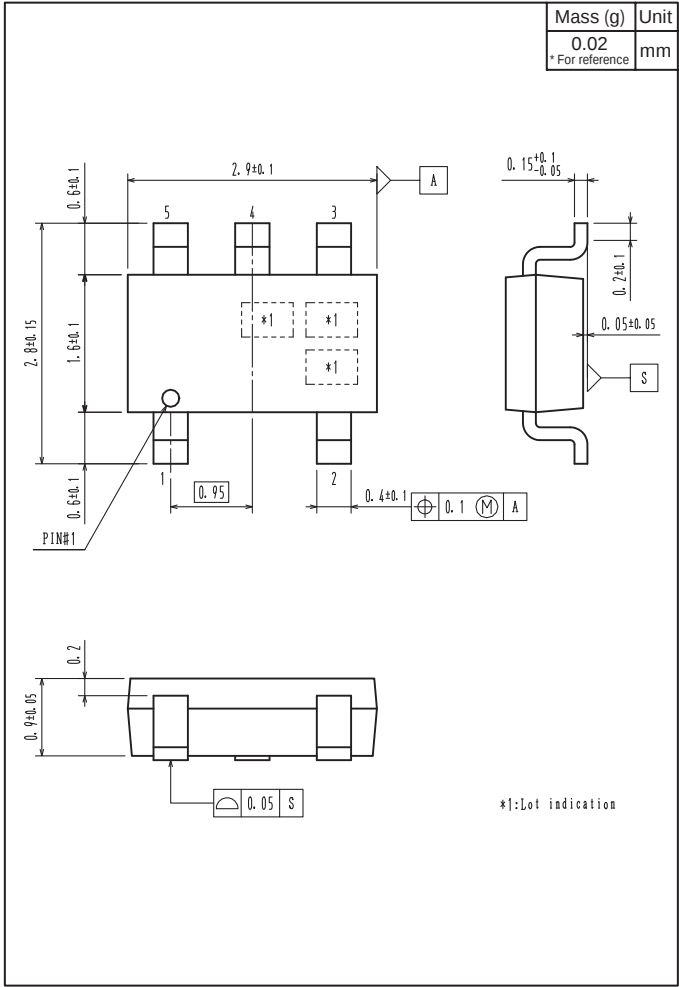


2-2. Device placement direction

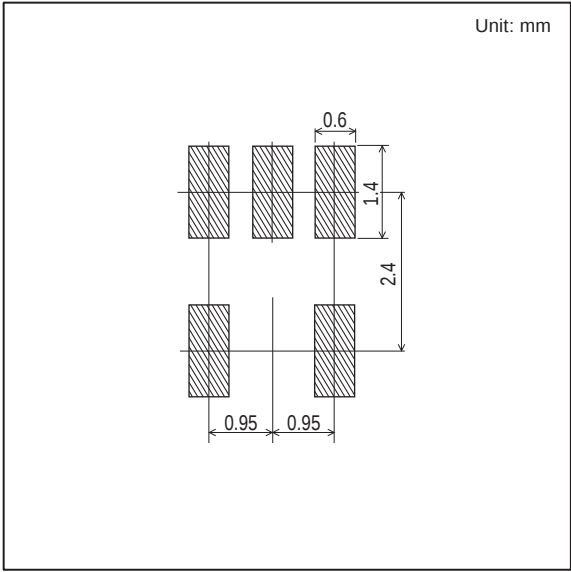


Those with pin 1 index on the feed hole side.....TL

Outline Drawing
CPH5541-TL-E



Land Pattern Example



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