

Taiwan Semiconductor

ELECTRICAL SPECIFICATIONS (T _A = 25°C unless otherwise noted)							
PARAMETER	CONDITIONS	SYMBOL	MIN	MAX	UNIT		
	$I_F = 0.1 \text{mA}, T_J = 25^{\circ}\text{C}$			0.24			
	$I_F = 1.0 \text{mA}, T_J = 25 ^{\circ}\text{C}$			0.32			
Forward voltage (1)	I _F = 10mA, T _J = 25°C	V _F	-	0.40	V		
	I _F = 30mA, T _J = 25°C	7		0.50			
	I _F = 100mA, T _J = 25°C			1.00			
Reverse voltage	I _R = 100 μA, T _J = 25°C	V _R	30	-	V		
Reverse current (2)	verse current $^{(2)}$ $V_R = 25 \text{ V}, T_J = 25^{\circ}\text{C}$		-	2	μA		
Total capacitance	f=1 MHz, V _R =1V	Ст	-	10	pF		
	I _F = 10mA,						
Reverse recovery time	$I_R = 10 \text{mA} \text{ to } 1 \text{mA},$ $R_L = 100 \Omega$	t _{rr}	-	5	ns		

Notes:

- 1. Pulse test with PW=0.3 ms
- 2. Pulse test with PW=30 ms

ORDERING INFORMATION				
ORDERING CODE	PACKAGE	PACKING		
BAT54GW RHG	SOD-123	3K / 7" Reel		



CHARACTERISTICS CURVES

(T_A = 25°C unless otherwise noted)

Fig.1 Typical Forward Characteristics

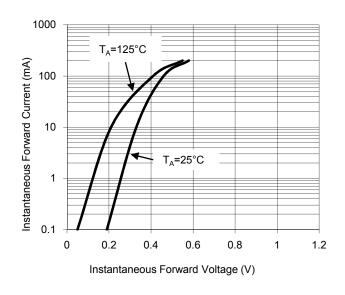


Fig.2 Typical Reverse Characteristics

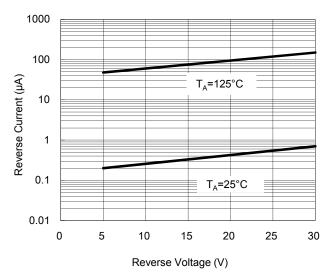


Fig.3 Typical Capacitance Characteristics

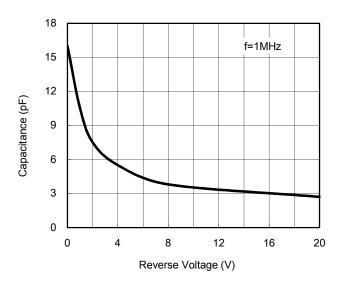
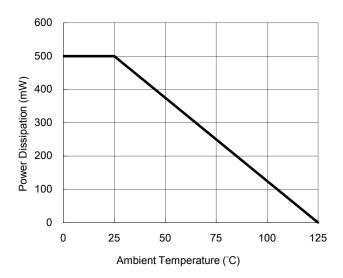


Fig.4 Power Derating Curve



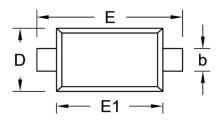
3 Version: A2004

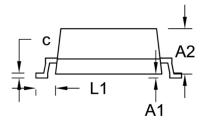




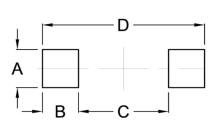
PACKAGE OUTLINE DIMENSION

SOD-123





SUGGEST PAD LAYOUT



DIM.	Unit (mm)		Unit (inch)		
DIN.	Min.	Max.	Min.	Max.	
A1	-	0.10	-	0.004	
A2	0.95	1.35	0.037	0.053	
b	0.45	0.70	0.018	0.028	
С	0.05	0.15	0.002	0.006	
D	1.40	1.80	0.055	0.071	
E	3.55	3.85	0.140	0.152	
E1	2.55	2.85	0.100	0.112	
L1	0.50	(REF)	0.020	(REF)	

Symbol	Unit (mm)	Unit (inch)
Α	0.95	0.037
В	0.90	0.035
С	2.25	0.089
D	4.05	0.159





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