

Electrical Specifications ($-40^{\circ}\text{C} \leq T_A \leq +85^{\circ}\text{C}$ unless otherwise specified)

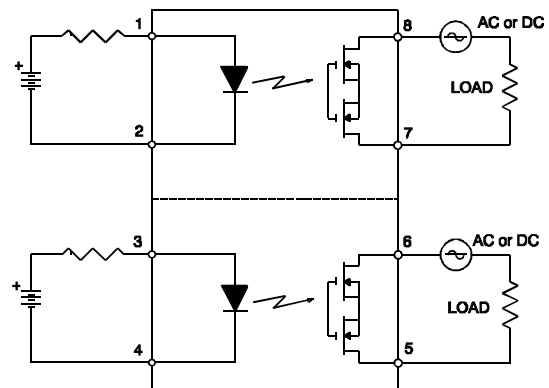
INPUT CHARACTERISTICS	Limits	Units
Minimum Control Current (See figure 1)	2.0	mA
Maximum Control Current for Off-State Resistance @ $T_A=+25^{\circ}\text{C}$	0.4	mA
Control Current Range (Caution: current limit input LED, see figure 5)	2.0 to 25	mA
Maximum Reverse Voltage	6.0	V

OUTPUT CHARACTERISTICS	Limits	Units
Operating Voltage Range	0 to ± 250	$V_{(\text{DC or AC peak})}$
Maximum Load Current @ $T_A=+40^{\circ}\text{C}$ 5mA Control (See figure 1) (single and dual channel operation)	170	mA
Maximum Peak Load Current (10ms maximum duration) (single and dual channel operation)	500	mA
Maximum On-State Resistance @ $T_A=+25^{\circ}\text{C}$ For 50mA Pulsed load, 5mA Control (see figure 3)	10	Ω
Maximum Off-State Leakage @ $T_A=+25^{\circ}\text{C}$, $\pm 250\text{V}$ (see figure 4)	1.0	μA
Maximum Turn-On Time @ $T_A=+25^{\circ}\text{C}$ (see figure 6) For 50mA, 100 V_{DC} load, 5mA Control	3.0	ms
Maximum Turn-Off Time @ $T_A=+25^{\circ}\text{C}$ (see figure 6) For 50mA, 100 V_{DC} load, 5mA Control	0.5	ms
Maximum Output Capacitance @ 50V _{DC}	50	pF

GENERAL CHARACTERISTICS	Limits	Units
Minimum Dielectric Strength, Input-Output	4000	V_{RMS}
Minimum Dielectric Strength, Pole-to-Pole	1000	V_{DC}
Minimum Insulation Resistance, Input-Output, @ $T_A=+25^{\circ}\text{C}$, 50%RH, 100V _{DC}	10^{12}	Ω
Maximum Capacitance, Input-Output	1.0	pF
Maximum Pin Soldering Temperature (10 seconds maximum)	+260	$^{\circ}\text{C}$
Ambient Temperature Range:	Operating	
	Storage	
	-40 to +85	
	-40 to +100	

International Rectifier does not recommend the use of this product in aerospace, avionics, military or life support applications. Users of this International Rectifier product in such applications assume all risks of such use and indemnify International Rectifier against all damages resulting from such use.

Connection Diagram



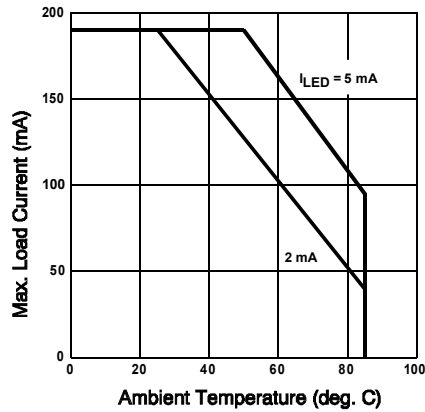


Figure 1. Typical Current Derating Curve

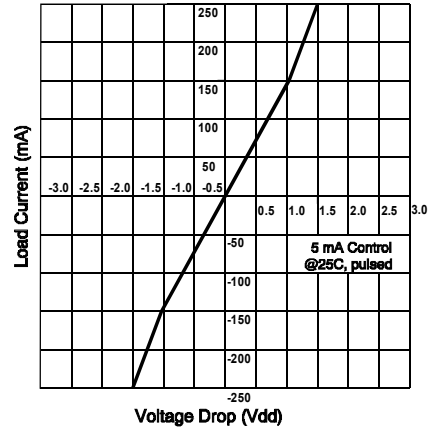


Figure 2. Linearity Characteristics

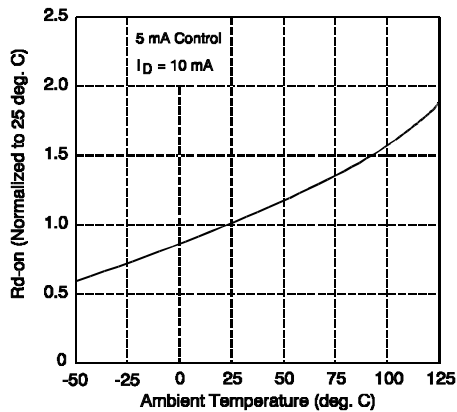


Figure 3. Typical Normalized On-Resistance

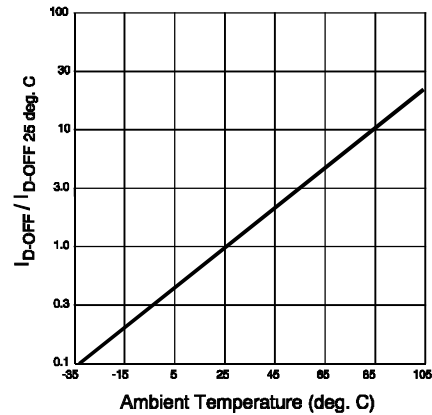


Figure 4. Typical Normalized Off-State Leakage

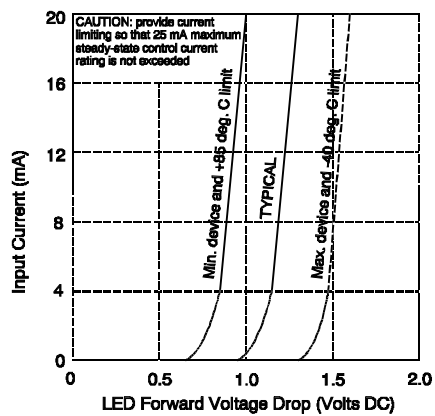


Figure 5. Input Characteristics (Current Controlled)

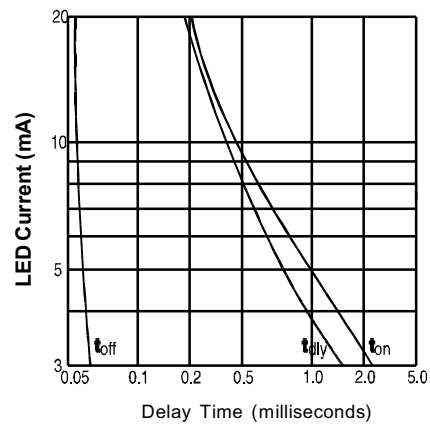


Figure 6. Typical Delay Times

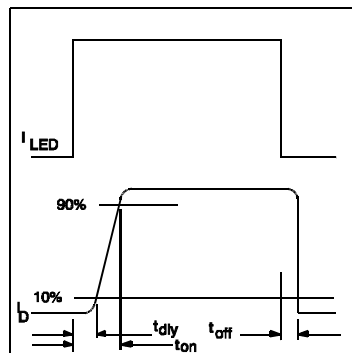


Figure 7. Delay Time Definitions

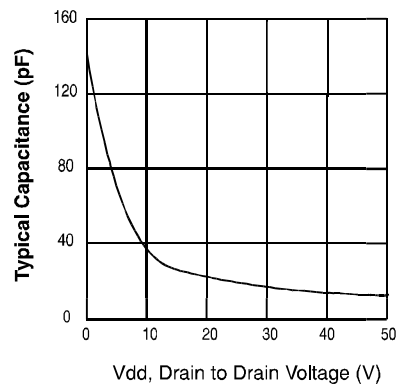
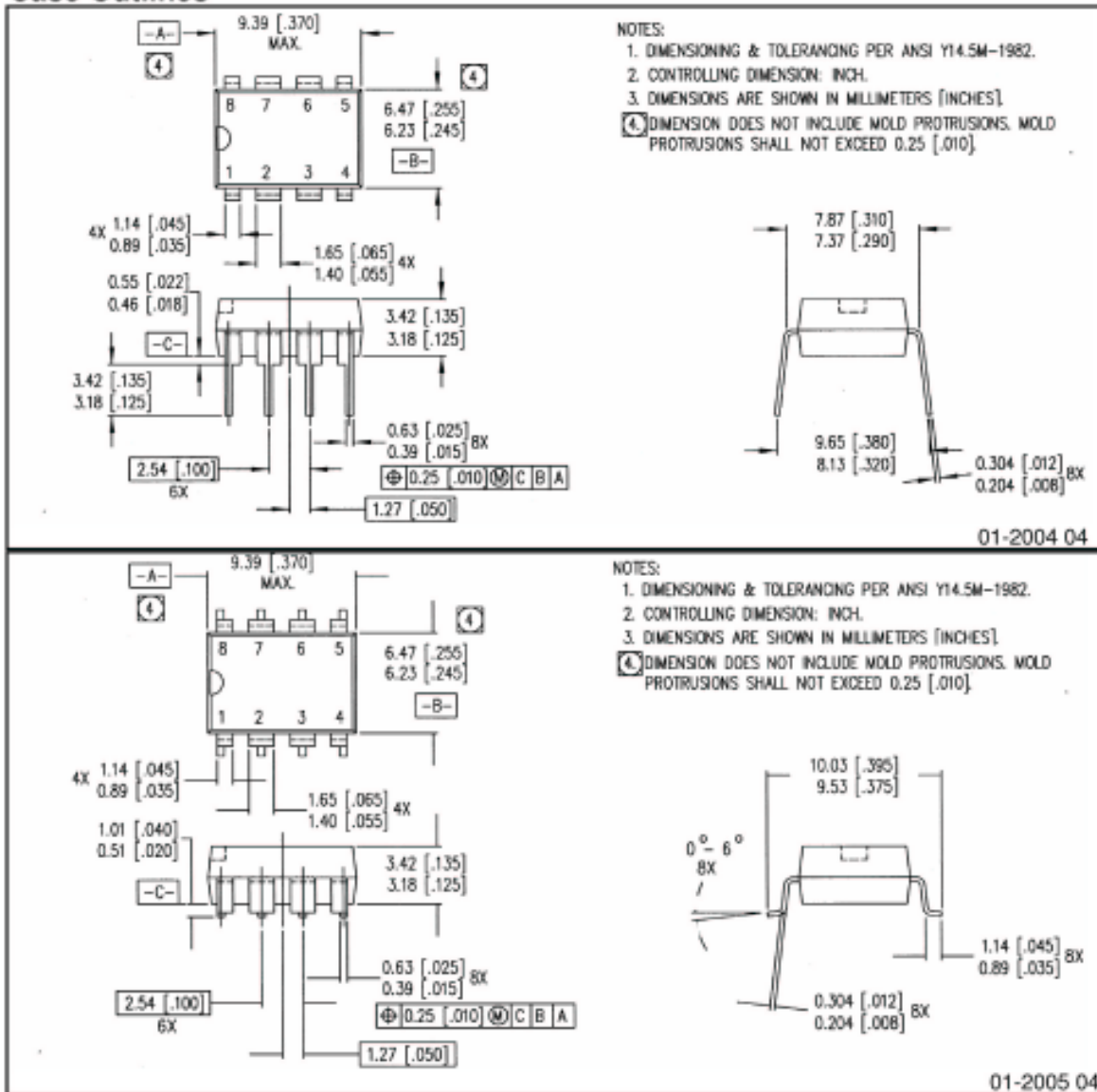


Figure 8. Typical Output Capacitance

Case Outlines



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