

SC70, Low-Power, General-Purpose, Dual-Supply, Rail-to-Rail Op Amps

ABSOLUTE MAXIMUM RATINGS

Supply Voltage (V_{CC} to V_{EE}).....	+12V	8-Pin SOT23 (derate 9.1mW/°C above +70°C).....	727mW
Voltage from Any Pin to Ground or Any Other Pin.....($V_{EE} - 0.3V$) to ($V_{CC} + 0.3V$)		8-Pin μ MAX® (derate 4.5mW/°C above +70°C).....	362mW
Output Short-Circuit Duration to V_{CC} , V_{EE} , or Ground	Continuous	8-Pin SO (derate 5.9mW/°C above +70°C).....	471mW
Continuous Power Dissipation ($T_A = +70^\circ\text{C}$)		14-Pin TSSOP (derate 9.1mW/°C above +70°C).....	727mW
5-Pin SC70 (derate 3.1mW/°C above +70°C).....	247mW	14-Pin SO (derate 8.3mW/°C above +70°C).....	667mW
5-Pin SOT23 (derate 7.1mW/°C above +70°C).....	571mW	Operating Temperature Range	-40°C to +125°C
		Storage Temperature Range	-65°C to +150°C
		Lead Temperature (soldering, 10s)	+300°C

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Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

DC ELECTRICAL CHARACTERISTICS

($V_{CC} = +5V$, $V_{EE} = -5V$, $R_L = 100k\Omega$ to ground, $T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$. Typical values are at $T_A = +25^\circ\text{C}$, unless otherwise noted.)
(Note 1)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Operating Supply Voltage Range	V_S	Guaranteed by PSRR test	± 2.25		± 5.5	V
Quiescent Supply Current per Amplifier	I_S			770	1100	μA
Input Offset Voltage	V_{OS}	$T_A = +25^\circ\text{C}$		0.3	5	mV
		$T_A = T_{MIN}$ to T_{MAX}			10	
Input Offset Voltage Drift	TCV_{OS}			3		$\mu\text{V}/^\circ\text{C}$
Input Offset Voltage Channel Matching		MAX4494 and MAX4495		1		mV
Input Bias Current	I_B			0.2	1	μA
Input Offset Current	I_{OS}			5	300	nA
Input Resistance	R_{IN}	Differential mode ($-1V \leq V_{IN} \leq +1V$)		250		$k\Omega$
		Common mode ($V_{EE} - 0.2V \leq V_{CM} \leq V_{CC} - 1.5V$)		110		$M\Omega$
Common-Mode Input Voltage Range	V_{CM}	Guaranteed by CMRR test	$V_{EE} - 0.2V$		$V_{CC} - 1.5V$	V
Common-Mode Rejection Ratio	CMRR	$V_{EE} - 0.2V \leq V_{CM} \leq V_{CC} - 1.5V$	65	90		dB
Power-Supply Rejection Ratio	PSRR	$V_S = \pm 2.25V$ to $\pm 5.5V$	65	80		dB
Large-Signal Voltage Gain	A_{VOL}	$R_L = 100k\Omega$, $V_{EE} + 0.25V \leq V_{OUT} \leq V_{CC} - 0.25V$	85	110		dB
		$R_L = 1k\Omega$, $V_{EE} + 0.5V \leq V_{OUT} \leq V_{CC} - 0.5V$	65	90		
Output Voltage Swing	V_{OUT}	$R_L = 100k\Omega$, $V_{CC} - V_{OH}$ and $V_{OL} - V_{EE}$		10	150	mV
		$R_L = 1k\Omega$, $V_{CC} - V_{OH}$ and $V_{OL} - V_{EE}$		200	450	
Output Short-Circuit Current	I_{SC}	Sourcing or sinking		15		mA

Note 1: All devices are 100% production tested at $T_A = +25^\circ\text{C}$. Limits over the operating temperature range are guaranteed by design and not production tested.

SC70, Low-Power, General-Purpose, Dual-Supply, Rail-to-Rail Op Amps

MAX4493/MAX4494/MAX4495

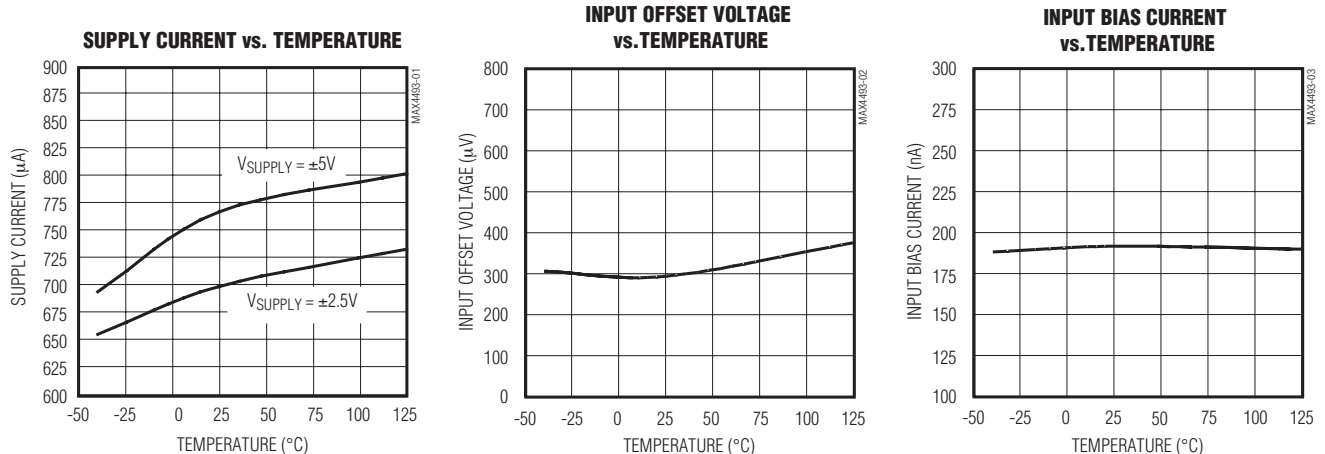
AC ELECTRICAL CHARACTERISTICS

($V_{CC} = +5V$, $V_{EE} = -5V$, $R_L = 100k\Omega$ to ground, $C_L = 15pF$, $T_A = +25^\circ C$, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Gain-Bandwidth Product	GBWP			5		MHz
Full-Power Bandwidth	FPBW	$V_{OUT} = 5Vp-p$		190		kHz
Slew Rate	SR	$V_{OUT} = 5Vp-p$		3		V/ μs
Phase Margin				75		degrees
Gain Margin				15		dB
Total Harmonic Distortion Plus Noise	THD+N	$f = 1kHz$, $V_{OUT} = 5Vp-p$, $A_V = +1V/V$		0.002		%
Settling Time to 0.01%	t_s	$A_V = +1V/V$, $V_{OUT} = 5V$ step		4		μs
Input Capacitance	C_{IN}			2		pF
Input Noise Voltage Density	e_{IN}	$f = 1kHz$		8		nV/ $\sqrt{Hz}$
Input Noise Current Density	i_{IN}	$f = 1kHz$		0.2		pA/ $\sqrt{Hz}$
All-Hostile Crosstalk		$f = 1kHz$, MAX4494 and MAX4495		-100		dB
Capacitive-Load Stability		$A_V = +1V/V$, no sustained oscillations		300		pF
Power-Up Time	t_{ON}	$V_{OUT} = 1V$, $1\mu s$ power supply rise-time		3		μs

Typical Operating Characteristics

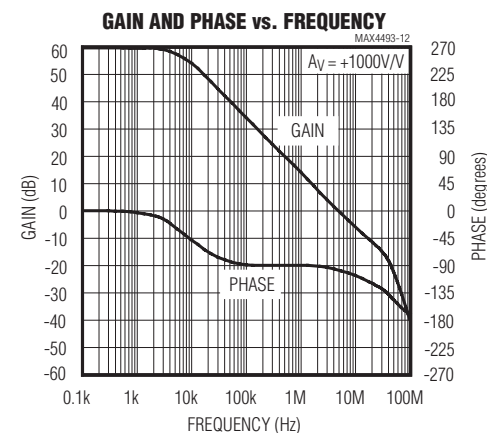
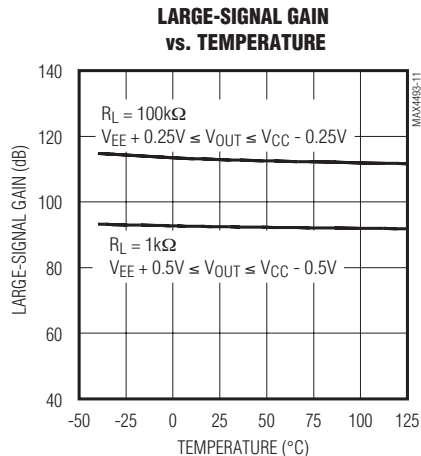
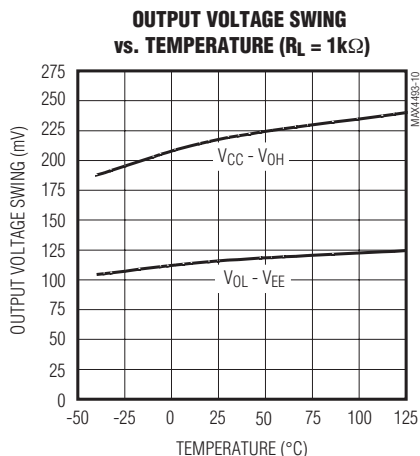
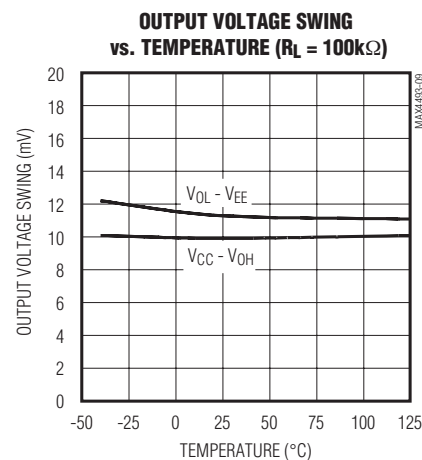
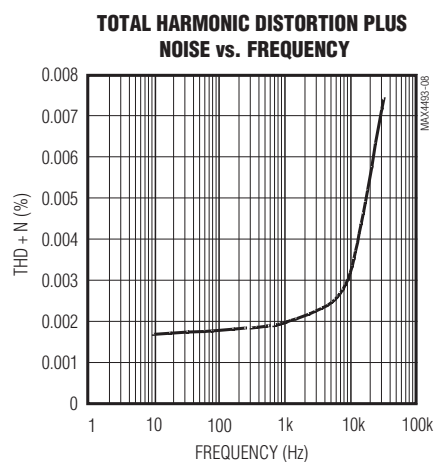
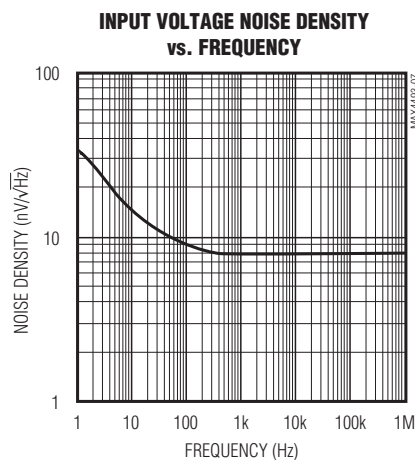
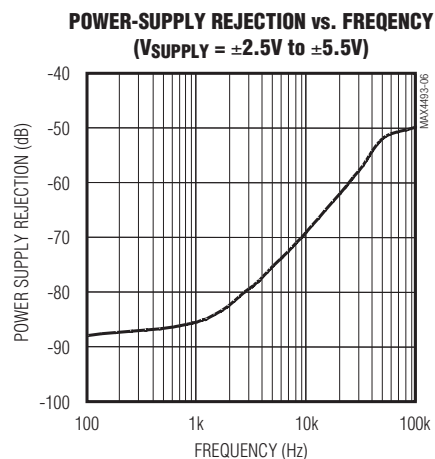
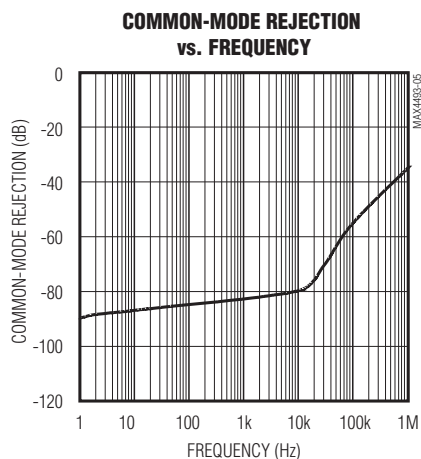
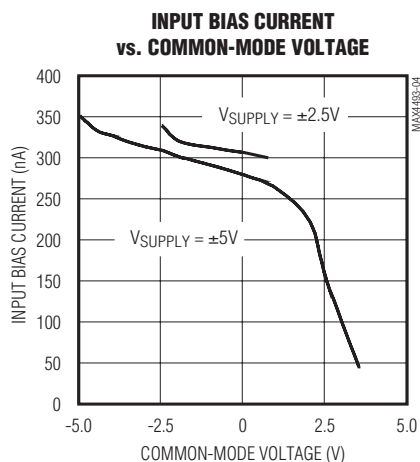
($V_{CC} = +5V$, $V_{EE} = -5V$, $V_{CM} = 0V$, $R_L = 100k\Omega$ to ground, $C_L = 15pF$, $T_A = +25^\circ C$, unless otherwise noted.)



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Typical Operating Characteristics (continued)

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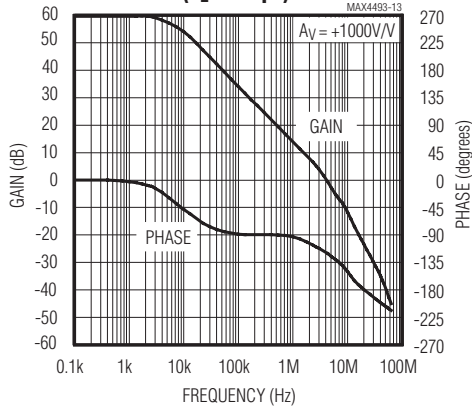


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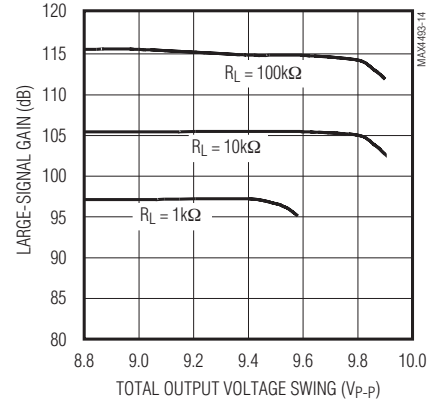
Typical Operating Characteristics (continued)

($V_{CC} = +5V$, $V_{EE} = -5V$, $V_{CM} = 0V$, $R_L = 100k\Omega$ to ground, $C_L = 15pF$, $T_A = +25^\circ C$, unless otherwise noted.)

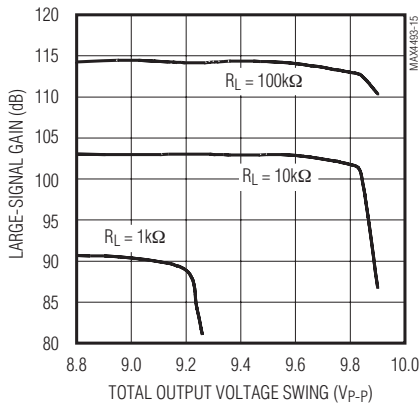
GAIN AND PHASE vs. FREQUENCY
($C_L = 300pF$)



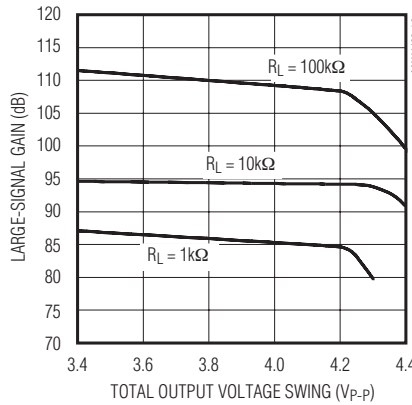
LARGE-SIGNAL GAIN vs. OUTPUT VOLTAGE SWING
(R_L TO V_{CC} , $V_{CC} = +5V$, $V_{EE} = -5V$)



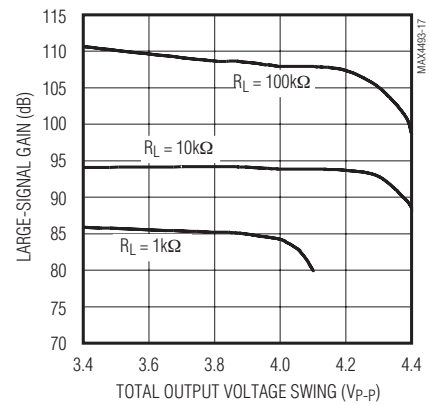
LARGE-SIGNAL GAIN vs. OUTPUT VOLTAGE SWING
(R_L TO V_{EE} , $V_{CC} = +5V$, $V_{EE} = -5V$)



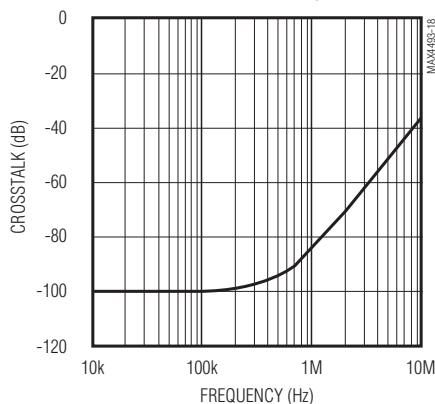
LARGE-SIGNAL GAIN vs. OUTPUT VOLTAGE SWING
(R_L TO V_{CC} , $V_{CC} = +2.25V$, $V_{EE} = -2.25V$)



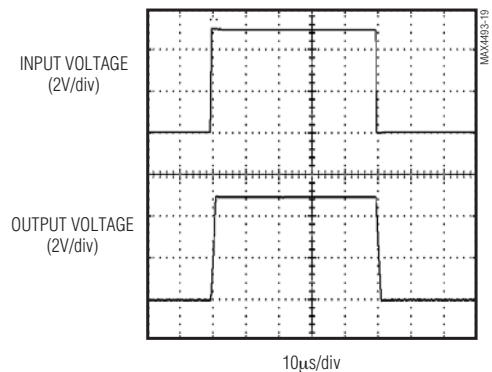
LARGE-SIGNAL GAIN vs. OUTPUT VOLTAGE SWING
(R_L TO V_{EE} , $V_{CC} = +2.25V$, $V_{EE} = -2.25V$)



MAX4494/MAX4495
CROSSTALK vs. FREQUENCY



LARGE-SIGNAL TRANSIENT RESPONSE



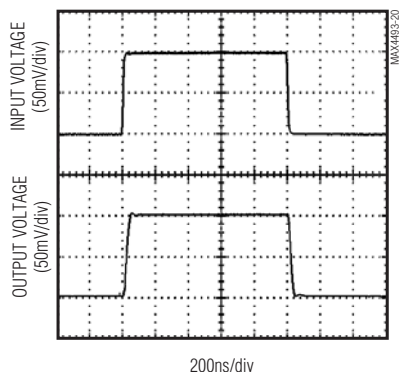
MAX4493/MAX4494/MAX4495

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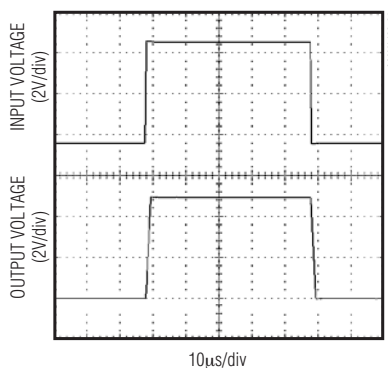
Typical Operating Characteristics (continued)

($V_{CC} = +5V$, $V_{EE} = -5V$, $V_{CM} = 0V$, $R_L = 100k\Omega$ to ground, $C_L = 15pF$, $T_A = +25^\circ C$, unless otherwise noted.)

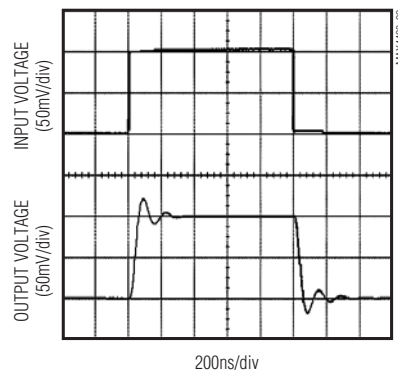
SMALL-SIGNAL TRANSIENT RESPONSE



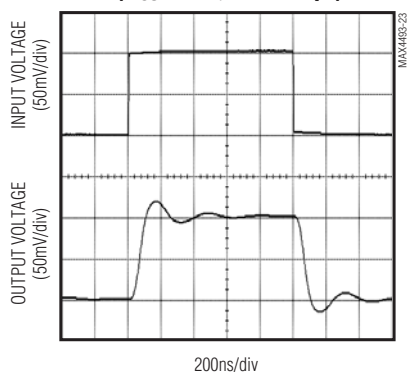
LARGE-SIGNAL CAPACITIVE-LOAD STABILITY
($C_L = 1200pF$)



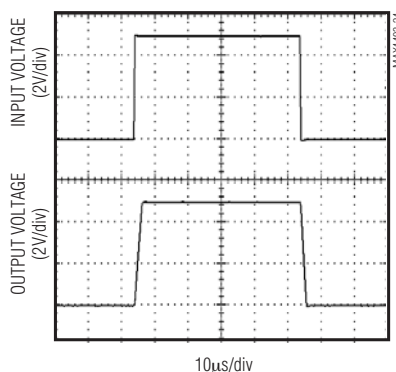
SMALL-SIGNAL CAPACITIVE-LOAD STABILITY
($C_L = 300pF$)



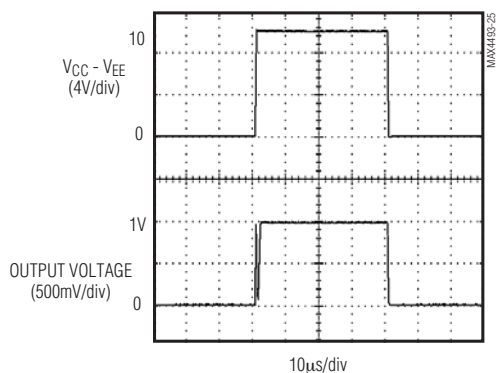
SMALL-SIGNAL TRANSIENT RESPONSE
($R_{ISO} = 15\Omega$, $C_L = 1000pF$)



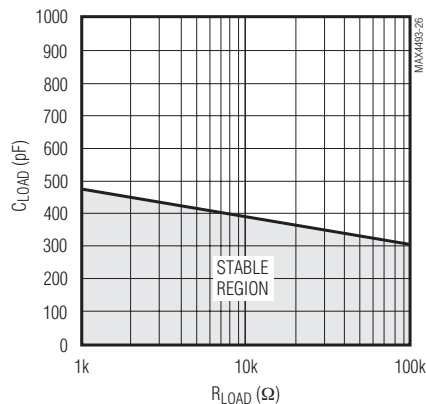
LARGE-SIGNAL TRANSIENT RESPONSE
($R_{ISO} = 15\Omega$, $C_L = 0.01\mu F$)



POWER-UP TIME ($V_{IN} = +1V$)



STABILITY vs. CAPACITIVE AND RESISTIVE LOADS



SC70, Low-Power, General-Purpose, Dual-Supply, Rail-to-Rail Op Amps

MAX4493/MAX4494/MAX4495

Pin Description

PIN			NAME	FUNCTION
MAX4493	MAX4494	MAX4495		
—	1	1	OUTA	Channel A Output
—	2	2	INA-	Channel A Inverting Input
—	3	3	INA+	Channel A Noninverting Input
—	7	7	OUTB	Channel B Output
—	6	6	INB-	Channel B Inverting Input
—	5	5	INB+	Channel B Noninverting Input
—	—	8	OUTC	Channel C Output
—	—	9	INC-	Channel C Inverting Input
—	—	10	INC+	Channel C Noninverting Input
—	—	14	OUTD	Channel D Output
—	—	13	IND-	Channel D Inverting Input
—	—	12	IND+	Channel D Noninverting Input
4	—	—	OUT	Output
1	—	—	IN+	Noninverting Input
3	—	—	IN-	Inverting Input
5	8	4	VCC	Positive Supply
2	4	11	VEE	Negative Supply

Applications Information

Rail-to-Rail Output Stage

The MAX4493/MAX4494/MAX4495 output stage can drive up to 1k Ω and still swing within 200mV of the rails.

Capacitive-Load Stability

Driving large capacitive loads can cause instability in many op amps. The MAX4493/MAX4494/MAX4495 are

stable with capacitive loads up to 300pF. The Capacitive-Load Stability graph in the *Typical Operating Characteristics* gives the stable operation region for capacitive versus resistive load. Stability with higher capacitive loads can be improved by adding an isolation resistor in series with the op-amp output, as shown in Figure 1. This resistor improves the circuit's phase margin by isolating the load capacitor from the amplifier's output. As seen in the *Typical Operating Characteristics*, driving capacitive loads with an isolation resistor exhibits some overshoot, but no oscillation.

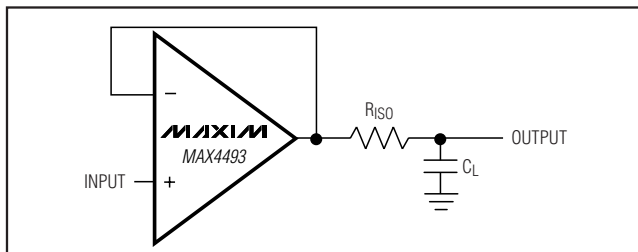


Figure 1. Capacitive Load Driving Circuit

Full-Power Bandwidth

The FPBW is given by:

$$\text{FPBW(Hz)} = \frac{\text{SR}}{\pi [V_{\text{OUTp-p(max)}}]}$$

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where the slew rate (SR) is $3\text{V}/\mu\text{s}$. Figure 2 shows the full-power bandwidth as a function of the peak-to-peak AC output voltage.

Power-Up Conditions

The MAX4493/MAX4494/MAX4495 typically settle within $3\mu\text{s}$ after power-up. See Power-Up Time in *Typical Operating Characteristics*.

Power Supplies and Layout

The MAX4493/MAX4494/MAX4495 operate with dual supplies from $\pm 2.25\text{V}$ to $\pm 5.5\text{V}$. Bypass both V_{CC} and V_{EE} with their own $0.1\mu\text{F}$ capacitor to ground.

Good layout technique helps optimize performance by decreasing the amount of stray capacitance at the op amp's inputs and outputs. To decrease stray capacitance, minimize trace lengths by placing external components close to the op amp's pins.

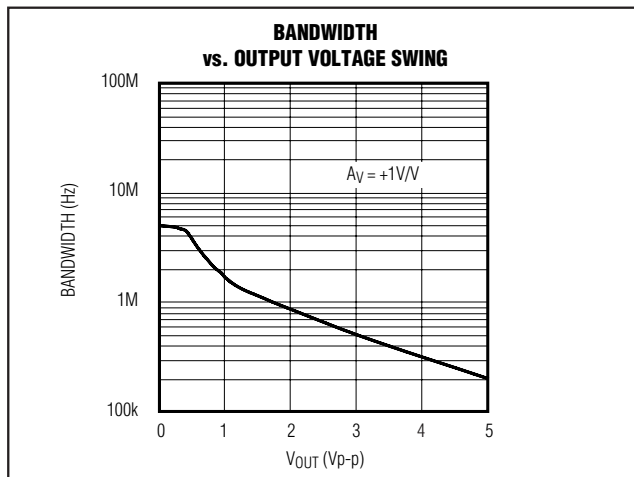
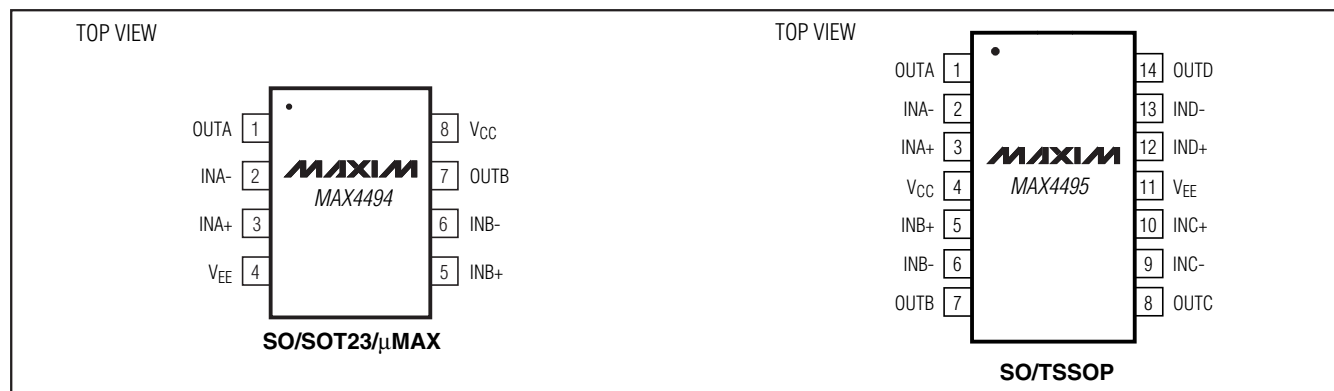
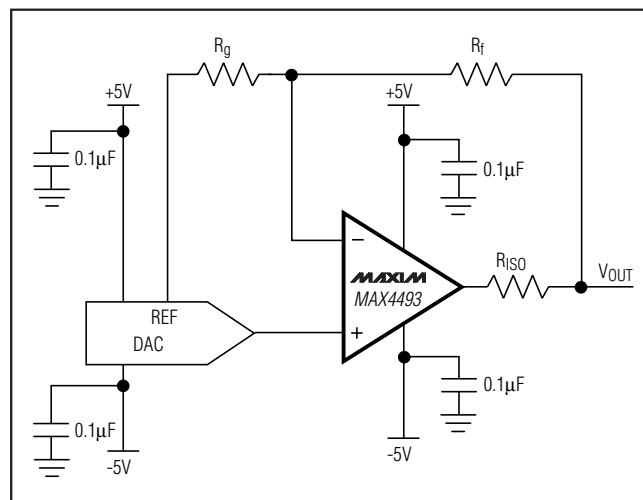


Figure 2. Bandwidth vs. Peak-to-Peak AC Voltage Plot

Pin Configurations (continued)



Typical Operating Circuit



Chip Information

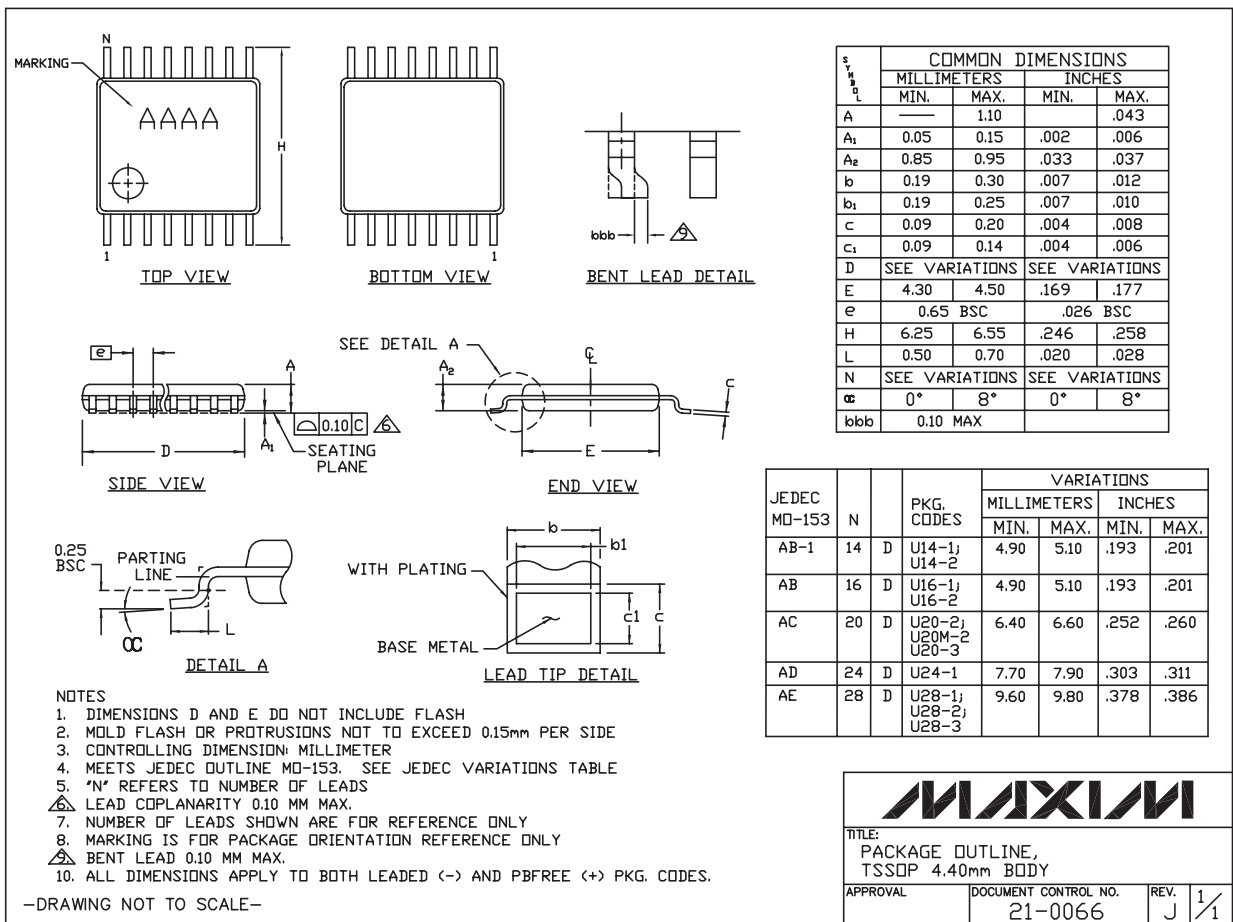
PROCESS: Bipolar

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Package Information

For the latest package outline information and land patterns, go to www.maxim-ic.com/packages. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

PACKAGE TYPE	PACKAGE CODE	DOCUMENT NO.
14 TSSOP	U14-1	21-0066
5 SOT23	U5-1	21-0057
5 SC70	X5-1	21-0076
8 SO	S8-2	21-0041
8 SOT23	K8-5	21-0078
8 μ MAX	U8-1	21-0036
14 SO	S14-1	21-0041



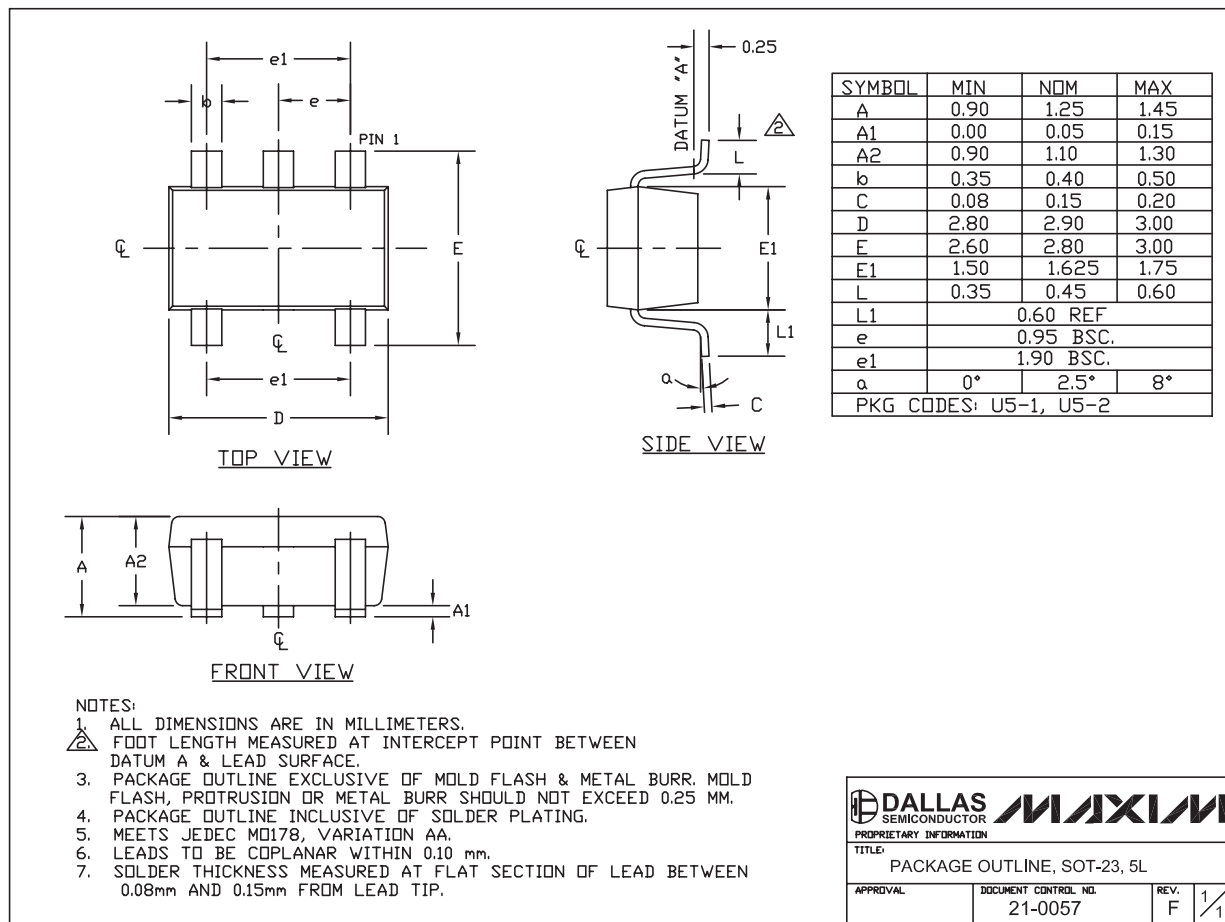
MAX4493/MAX4494/MAX4495

TSSOP 4.40mm EPS

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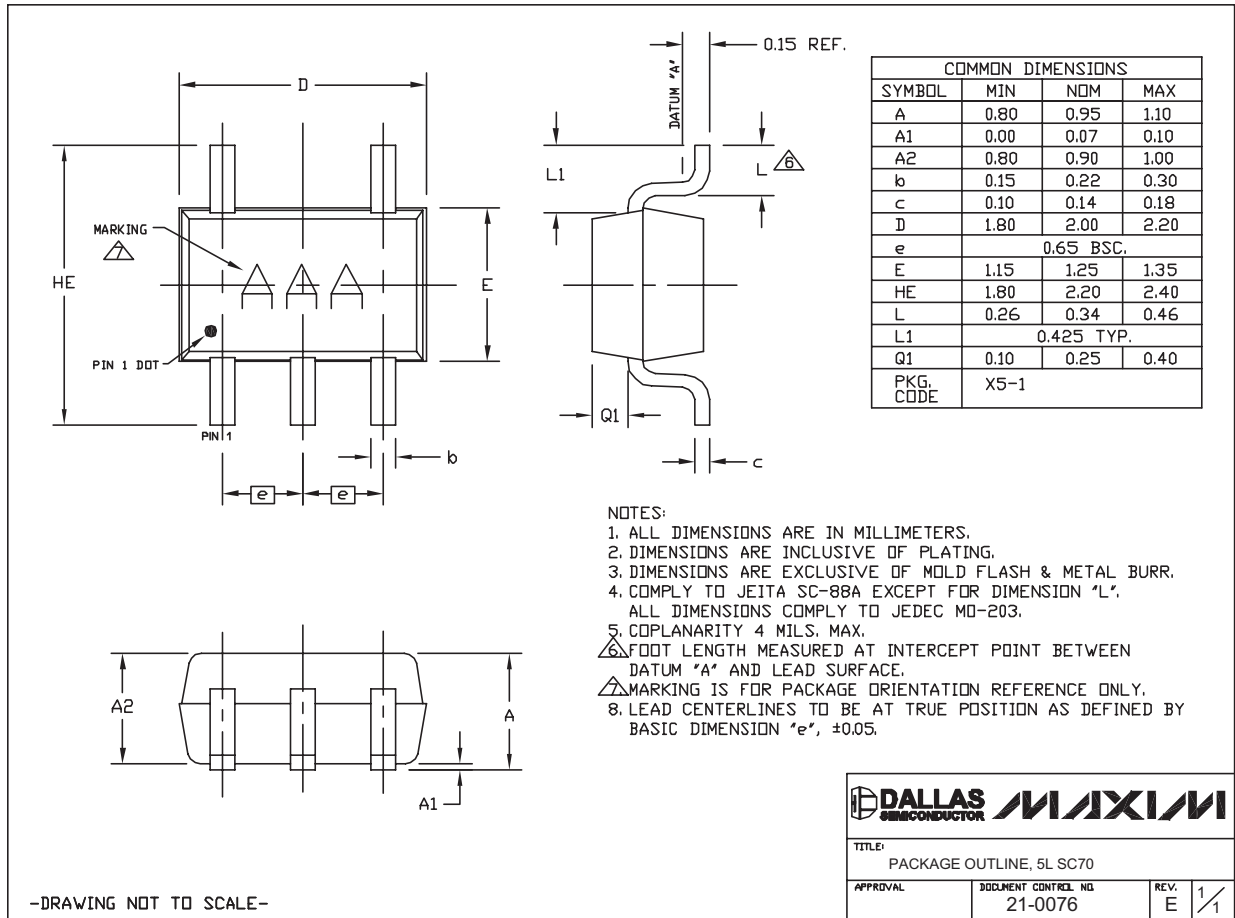


SOT-23 5L EPSS

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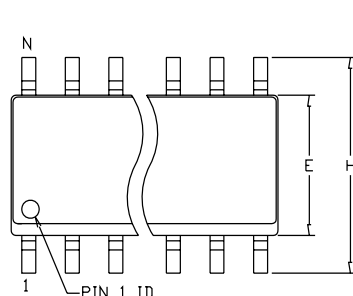
SC70, 5LEPS

MAX4493/MAX4494/MAX4495

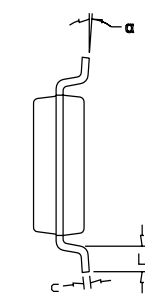
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Package Information (continued)

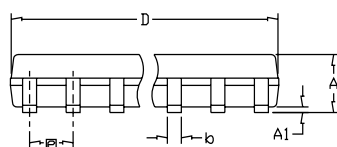
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TOP VIEW



END VIEW



SIDE VIEW

COMMON DIMENSIONS				
SYMBOL	INCHES		MM	
	MIN.	MAX.	MIN.	MAX.
A	.053	.069	1.35	1.75
A1	.004	.010	0.10	0.25
b	.014	.019	0.35	0.49
c	.007	.010	0.19	0.25
E	.150	.157	3.80	4.00
e	.050	BSC	1.27	BSC
H	.228	.244	5.80	6.20
L	.016	.050	0.40	1.27
α	0°	8°	0°	8°

VARIATION A				
SYMBOL	INCHES		MM	
	MIN.	MAX.	MIN.	MAX.
D	.189	.197	4.80	5.00
N	8			
MS012	AA			
PKG. CODE	S8-2, S8-4, S8-5, S8-6F, S8-7F, S8-8F, S8-10F, S8-11F, S8-16F			

VARIATION B				
SYMBOL	INCHES		MM	
	MIN.	MAX.	MIN.	MAX.
D	.337	.344	8.55	8.75
N	14			
MS012	AB			
PKG. CODE	S14-1, S14-4, S14-5, S14-6; S14M-4, S14M-5, S14M-6. S14M-7			

VARIATION C				
SYMBOL	INCHES		MM	
	MIN.	MAX.	MIN.	MAX.
D	.386	.394	9.80	10.00
N	16			
MS012	AC			
PKG. CODE	S16-1, S16-3, S16-5, S16-6, S16-8, S16-7F, S16-9F, S16-10F; S16M-3, S16M-6			

NOTES:

- ALL DIMENSIONS ARE IN MILLIMETERS UNLESS OTHERWISE SPECIFIED.
- MATERIAL MUST COMPLY WITH BANNED AND RESTRICTED SUBSTANCES SPEC # 10-0131.
- DIMENSIONS D AND E DO NOT INCLUDE MOLD PROTRUSION. ALLOWABLE MOLD PROTRUSION IS 0.15 MM (.006") PER SIDE.
- LEADS TO BE COPLANAR WITHIN 0.10mm (.004").
- MEETS JEDEC MS012
- ALL DIMENSIONS APPLY TO BOTH LEADED (-) AND PbFREE (+) PKG. CODES.

-DRAWING NOT TO SCALE-

MAXIM

TITLE:
PACKAGE OUTLINE,
8L, 14L, 16L SOIC .150 INCH

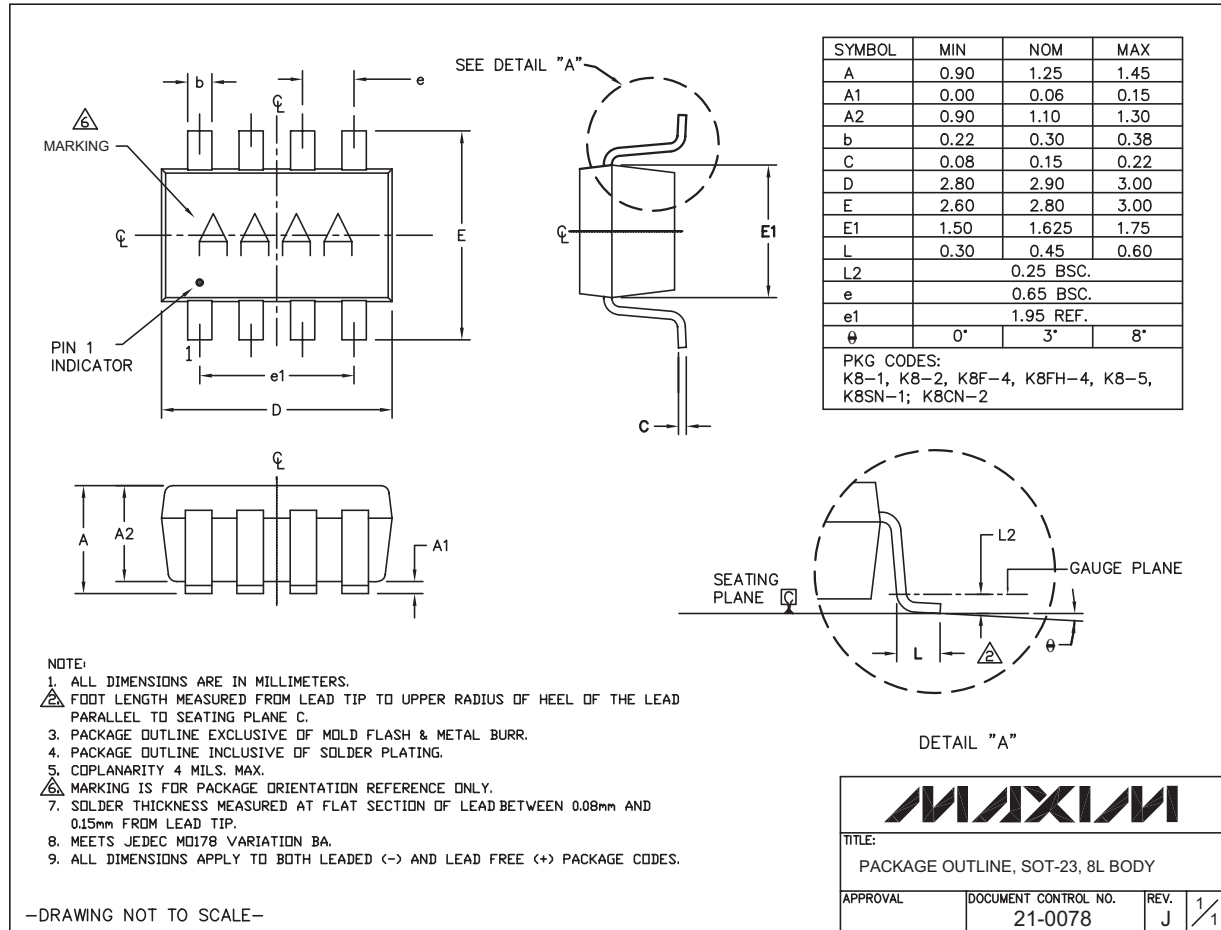
APPROVAL	DOCUMENT CONTROL NO. 21-0041	REV. C	1/1
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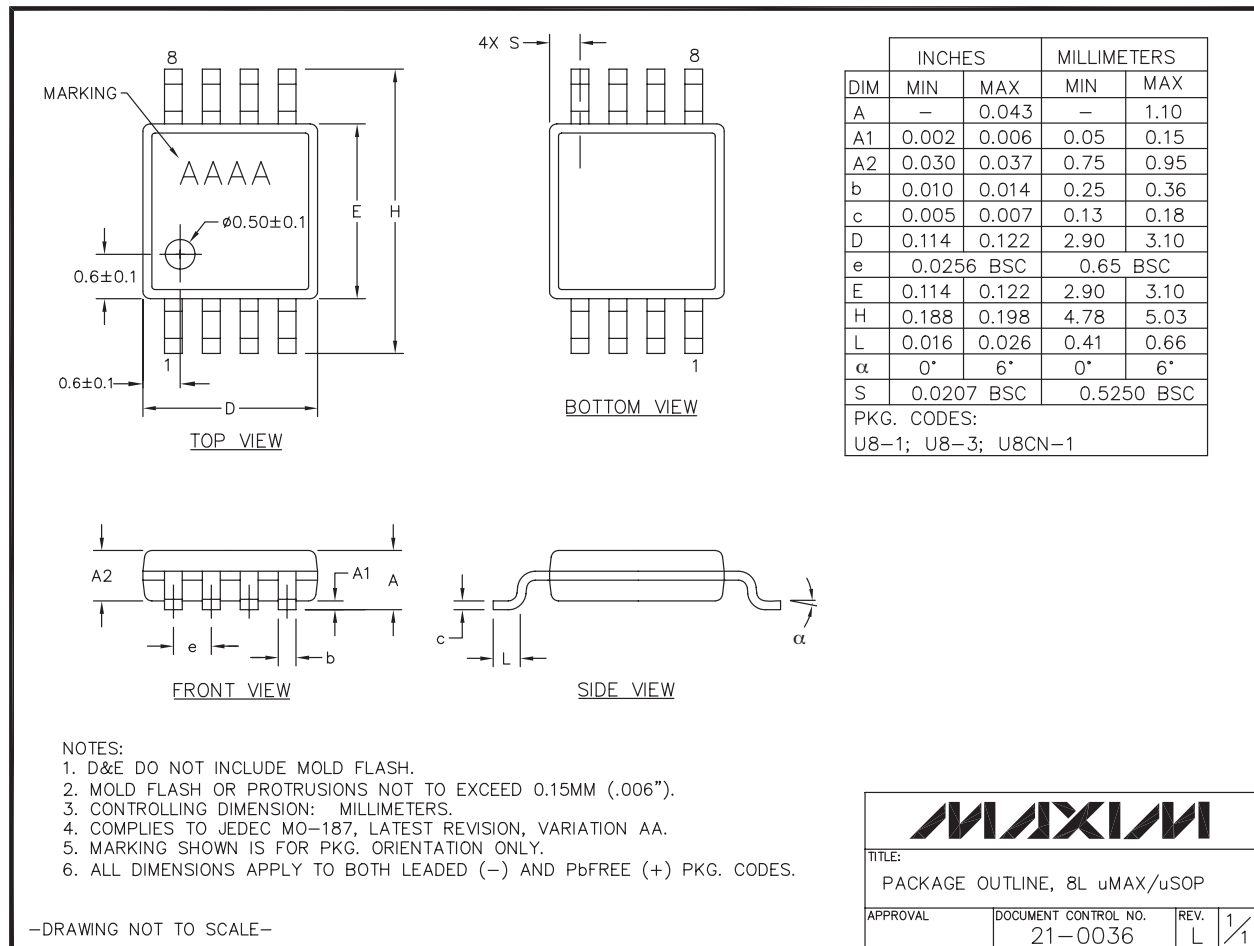
MAX4493/MAX4494/MAX4495



SC70, Low-Power, General-Purpose, Dual-Supply, Rail-to-Rail Op Amps

Package Information (continued)

For the latest package outline information and land patterns, go to www.maxim-ic.com/packages. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.



SC70, Low-Power, General-Purpose, Dual-Supply, Rail-to-Rail Op Amps

Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
2	10/08	Updated first paragraph of the <i>General Description</i> section	1
3	12/09	Added lead-free and automotive parts to <i>Ordering Information</i>	1

MAX4493/MAX4494/MAX4495

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