

# LT1280A/LT1281A

## ABSOLUTE MAXIMUM RATINGS (Note 1)

Supply Voltage ( $V_{CC}$ )	6V	Short-Circuit Duration	
$V^+$	13.2V	$V^+$	30 sec
$V^-$	-13.2V	$V^-$	30 sec
Input Voltage		Driver Output	Indefinite
Driver	$V^-$ to $V^+$	Receiver Output	Indefinite
Receiver	-30V to 30V	Operating Temperature Range	
ON/OFF	-0.3V to 12V	LT1280AC/LT1281AC	0°C to 70°C
Output Voltage		LT1280AI/LT1281AI	-40°C to 85°C
Driver	$V^+ - 30V$ to $V^- + 30V$	Storage Temperature Range	-65°C to 150°C
Receiver	-0.3V to $V_{CC} + 0.3V$	Lead Temperature (Soldering, 10 sec)	300°C

## PACKAGE/ORDER INFORMATION

<p>TOP VIEW</p> <p>N PACKAGE 18-LEAD PDIP SW PACKAGE 18-LEAD PLASTIC SO WIDE</p> <p><math>T_{JMAX} = 125^\circ\text{C}</math>, <math>\theta_{JA} = 80^\circ\text{C/W}</math>, <math>\theta_{JC} = 36^\circ\text{C/W}</math> (N) <math>T_{JMAX} = 125^\circ\text{C}</math>, <math>\theta_{JA} = 90^\circ\text{C/W}</math>, <math>\theta_{JC} = 26^\circ\text{C/W}</math> (SW)</p>	<p>ORDER PART NUMBER</p> <p>LT1280ACN LT1280AIN LT1280ACSW LT1280AISW</p>	<p>TOP VIEW</p> <p>N PACKAGE 16-LEAD PDIP SW PACKAGE 16-LEAD PLASTIC SO WIDE</p> <p><math>T_{JMAX} = 125^\circ\text{C}</math>, <math>\theta_{JA} = 90^\circ\text{C/W}</math>, <math>\theta_{JC} = 46^\circ\text{C/W}</math> (N) <math>T_{JMAX} = 125^\circ\text{C}</math>, <math>\theta_{JA} = 95^\circ\text{C/W}</math>, <math>\theta_{JC} = 27^\circ\text{C/W}</math> (SW)</p>	<p>ORDER PART NUMBER</p> <p>LT1281ACN LT1281AIN LT1281ACSW LT1281AISW</p>
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Consult factory for Military grade parts.

## ELECTRICAL CHARACTERISTICS (Note 2)

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
<b>Power Supply Generator</b>					
$V^+$ Output			7.9		V
$V^-$ Output			-7.0		V
Supply Current ( $V_{CC}$ )	(Note 3), $T_A = 25^\circ\text{C}$		8	10	mA
Supply Current When OFF ( $V_{CC}$ )	Shutdown (Note 4) LT1280A Only		1	10	$\mu\text{A}$
Supply Rise Time	$C1 = C2 = C3 = C4 = 0.1\mu\text{F}$		0.2		ms
Shutdown to Turn-On	LT1280A Only		0.2		ms
ON/OFF Pin Thresholds	Input Low Level (Device Shutdown)	0.8	1.2		V
	Input High Level (Device Enabled)		1.6	2.4	V
ON/OFF Pin Current	$0V \leq V_{ON/OFF} \leq 5V$	-15		80	$\mu\text{A}$
Oscillator Frequency			130		kHz
<b>Driver</b>					
Output Voltage Swing	Load = 3k to GND	5.0	7.5		V
	Positive		-6.3	-5.0	V
	Negative				V
Logic Input Voltage Level	Input Low Level ( $V_{OUT} = \text{High}$ )	2.0	1.4	0.8	V
	Input High Level ( $V_{OUT} = \text{Low}$ )		1.4		V

**ELECTRICAL CHARACTERISTICS** (Note 2)

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Logic Input Current	$0.8V \leq V_{IN} \leq 2.0V$		5	20	$\mu A$
Output Short-Circuit Current	$V_{OUT} = 0V$	$\pm 9$	17		mA
Output Leakage Current	Shutdown $V_{OUT} = \pm 30V$ (Note 4)		10	100	$\mu A$
Data Rate (Note 7)	$R_L = 3k, C_L = 2500pF$ $R_L = 3k, C_L = 1000pF$	120 250			kBaud kBaud
Slew Rate	$R_L = 3k, C_L = 51pF$ $R_L = 3k, C_L = 2500pF$	4	15 7	30	V/ $\mu s$ V/ $\mu s$
Propagation Delay	Output Transition $t_{HL}$ High-to-Low (Note 5) Output Transition $t_{LH}$ Low-to-High		0.6 0.5	1.3 1.3	$\mu s$ $\mu s$

**Receiver**

Input Voltage Thresholds	Input Low Threshold ( $V_{OUT} = \text{High}$ )	C Grade	●	0.8	1.3		V
	Input High Threshold ( $V_{OUT} = \text{Low}$ )	C Grade	●		1.7	2.4	V
Hysteresis	Input Low	I Grade	●	0.2	1.3		V
	Input High	I Grade	●		1.7	3.0	V
Hysteresis			●	0.1	0.4	1.0	V
Input Resistance	$V_{IN} = \pm 10V$			3	5	7	k $\Omega$
Output Leakage Current	Shutdown (Note 4) $0 \leq V_{OUT} \leq V_{CC}$		●		1	10	$\mu A$
Output Voltage	Output Low, $I_{OUT} = -1.6mA$		●		0.2	0.4	V
	Output High, $I_{OUT} = 160\mu A$ ( $V_{CC} = 5V$ )		●	3.5	4.2		V
Output Short-Circuit Current	Sinking Current, $V_{OUT} = V_{CC}$				-20	-10	mA
	Sourcing Current, $V_{OUT} = 0V$			10	20		mA
Propagation Delay	Output Transition $t_{HL}$ High-to-Low (Note 6)				250	600	ns
	Output Transition $t_{LH}$ Low-to-High				350	600	ns

The ● denotes specifications which apply over the operating temperature range ( $0^\circ C \leq T_A \leq 70^\circ C$  for commercial grade, and  $-40^\circ C \leq T_A \leq 85^\circ C$  for industrial grade).

**Note 1:** Absolute Maximum Ratings are those values beyond which the life of the device may be impaired.

**Note 2:** Testing done at  $V_{CC} = 5V$  and  $V_{ON/OFF} = 3V$ , unless otherwise specified.

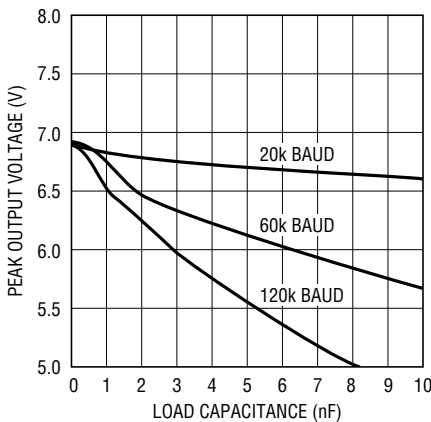
**Note 3:** Supply current is measured as the average over several charge pump cycles.  $C^+ = C^- = C1 = C2 = 0.1\mu F$ . All outputs are open, with all driver inputs tied high.

**Note 4:** Supply current measurements in shutdown are performed with  $V_{ON/OFF} \leq 0.1V$ .

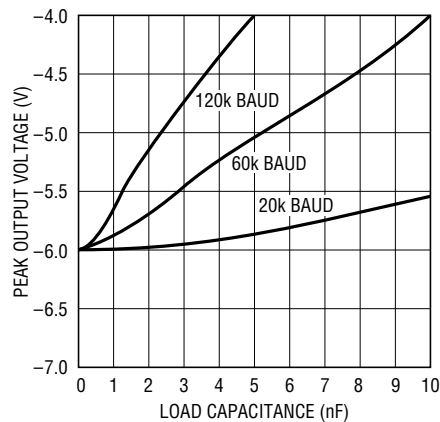
**Note 5:** For driver delay measurements,  $R_L = 3k$  and  $C_L = 51pF$ . Trigger points are set between the driver's input logic threshold and the output transition to the zero crossing ( $t_{HL} = 1.4V$  to  $0V$  and  $t_{LH} = 1.4V$  to  $0V$ ).

**Note 6:** For receiver delay measurements,  $C_L = 51pF$ . Trigger points are set between the receiver's input logic threshold and the output transition to standard TTL/CMOS logic threshold ( $t_{HL} = 1.3V$  to  $2.4V$  and  $t_{LH} = 1.7V$  to  $0.8V$ ).

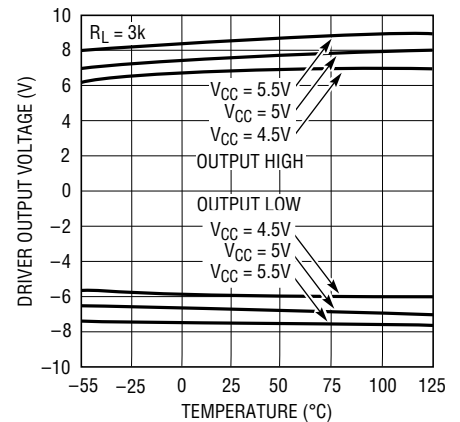
**Note 7:** Data rate operation guaranteed by slew rate, short-circuit current and propagation delay tests.

**TYPICAL PERFORMANCE CHARACTERISTICS****Driver Maximum Output Voltage vs Load Capacitance**

LT1280A • TPC01

**Driver Minimum Output Voltage vs Load Capacitance**

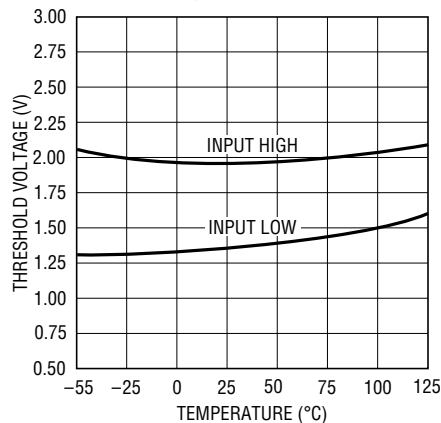
LT1280A • TPC02

**Driver Output Voltage**

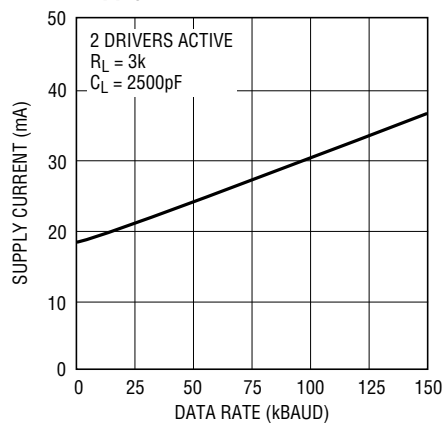
LT1280A • TPC03

## TYPICAL PERFORMANCE CHARACTERISTICS

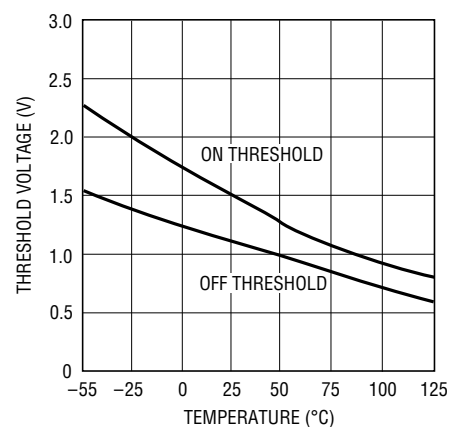
Receiver Input Thresholds



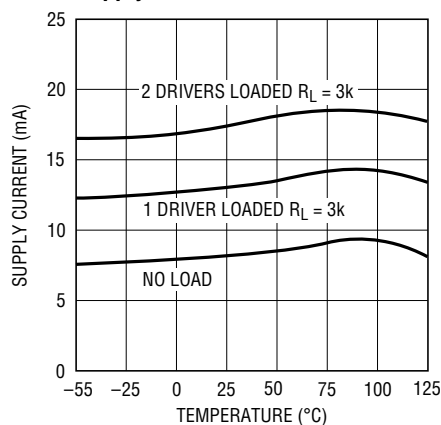
Supply Current vs Data Rate



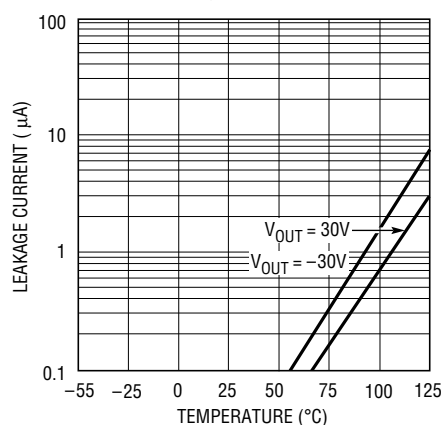
ON/OFF Thresholds



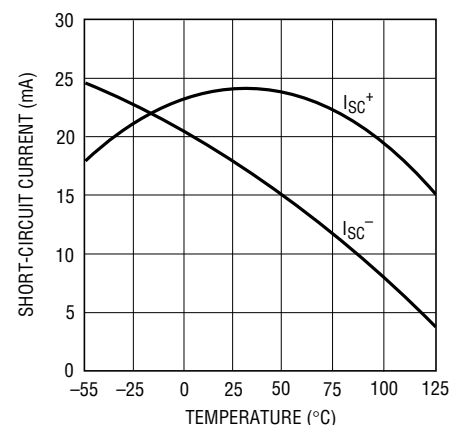
Supply Current



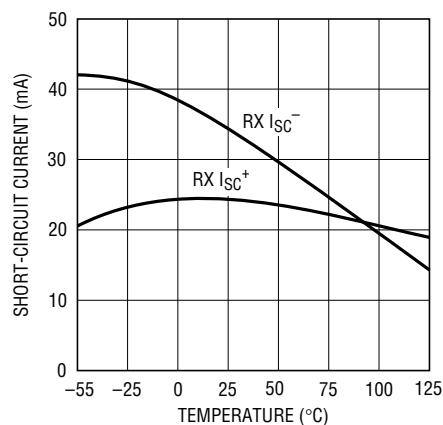
Driver Leakage in Shutdown



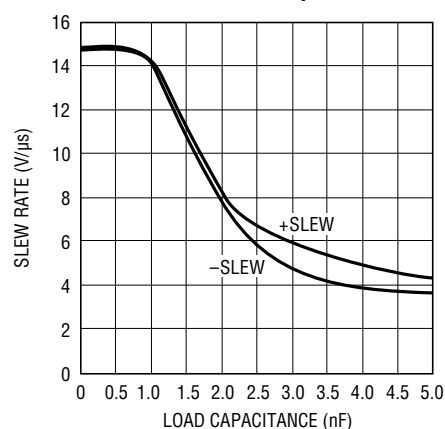
Driver Short-Circuit Current



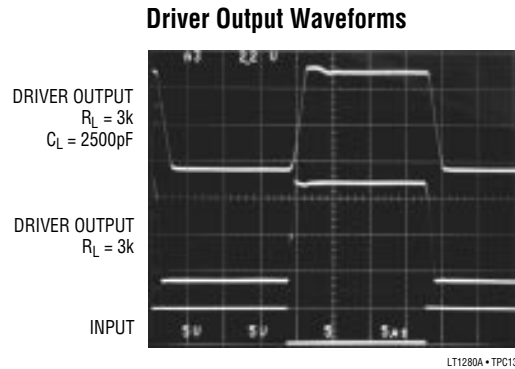
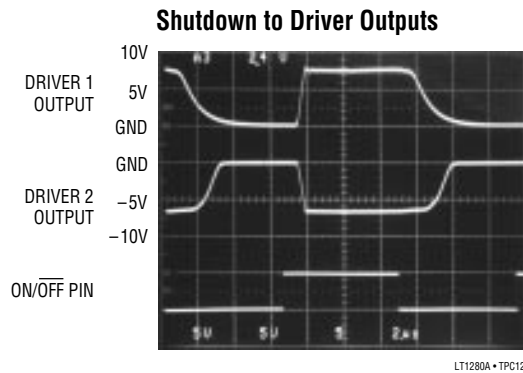
Receiver Short-Circuit Current



Slew Rate vs Load Capacitance



## TYPICAL PERFORMANCE CHARACTERISTICS



## PIN FUNCTIONS

**V<sub>CC</sub>:** 5V Input Supply Pin. This pin should be decoupled with a 0.1μF ceramic capacitor close to the package pin. Insufficient supply bypassing can result in low output drive levels and erratic charge pump operation.

**GND:** Ground Pin.

**ON/OFF:** A TTL/CMOS Compatible Operating Mode Control. A logic low puts the LT1280A in shutdown mode. Supply current drops to zero and both driver and receiver outputs assume a high impedance state. A logic high fully enables the device.

**V<sup>+</sup>:** Positive Supply Output (RS232 Drivers).  $V^+ \approx 2V_{CC} - 1.5V$ . This pin requires an external charge storage capacitor  $C \geq 0.1\mu F$ , tied to ground or  $V_{CC}$ . Larger value capacitors may be used to reduce supply ripple. With multiple transceivers, the  $V^+$  and  $V^-$  pins may be paralleled into common capacitors.

**V<sup>-</sup>:** Negative Supply Output (RS232 Drivers).  $V^- \approx -(2V_{CC} - 2.5V)$ . This pin requires an external charge storage capacitor  $C \geq 0.1\mu F$ . Larger value capacitors may be used to reduce supply ripple. With multiple transceivers, the  $V^+$  and  $V^-$  pins may be paralleled into common capacitors.

**TR1 IN, TR2 IN:** RS232 Driver Input Pins. These inputs are TTL/CMOS compatible. Inputs should not be allowed to float. Tie unused inputs to  $V_{CC}$ .

**TR1 OUT, TR2 OUT:** Driver Outputs at RS232 Voltage Levels. Driver output swing meets RS232 levels for loads up to 3k. Slew rates are controlled for lightly loaded lines.

Output current capability is sufficient for load conditions up to 2500pF. Outputs are in a high impedance state when in shutdown mode or  $V_{CC} = 0V$ . Outputs are fully short-circuit protected from  $V^- + 30V$  to  $V^+ - 30V$ . Applying higher voltages will not damage the device if the overdrive is moderately current limited. Short circuits on one output can load the power supply generator and may disrupt the signal levels of the other outputs. The driver outputs are protected against ESD to  $\pm 10kV$  for human body model discharges.

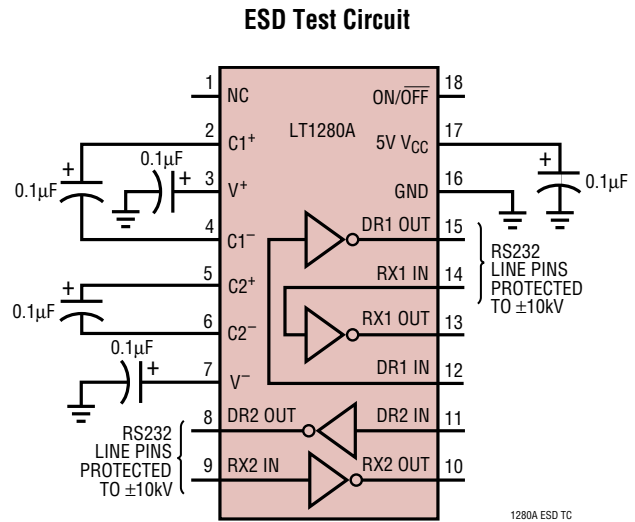
**REC1 IN, REC2 IN:** Receiver Inputs. These pins accept RS232 level signals ( $\pm 30V$ ) into a protected 5k terminating resistor. The receiver inputs are protected against ESD to  $\pm 10kV$  for human body model discharges. Each receiver provides 0.4V of hysteresis for noise immunity. Open receiver inputs assume a logic low state.

**REC1 OUT, REC2 OUT:** Receiver outputs with TTL/CMOS Voltage Levels. Outputs are in a high impedance state when in shutdown mode to allow data line sharing. Outputs are fully short-circuit protected to ground or  $V_{CC}$  with the power ON, OFF or in the shutdown mode.

**C1<sup>+</sup>, C1<sup>-</sup>, C2<sup>+</sup>, C2<sup>-</sup>:** Commutating Capacitor Inputs. These pins require two external capacitors  $C \geq 0.1\mu F$ : one from C1<sup>+</sup> to C1<sup>-</sup> and another from C2<sup>+</sup> to C2<sup>-</sup>. C1 should be deleted if a separate 12V supply is available and connected to pin C1<sup>+</sup>. Similarly, C2 should be deleted if a separate -12V supply is connected to pin V<sup>-</sup>.

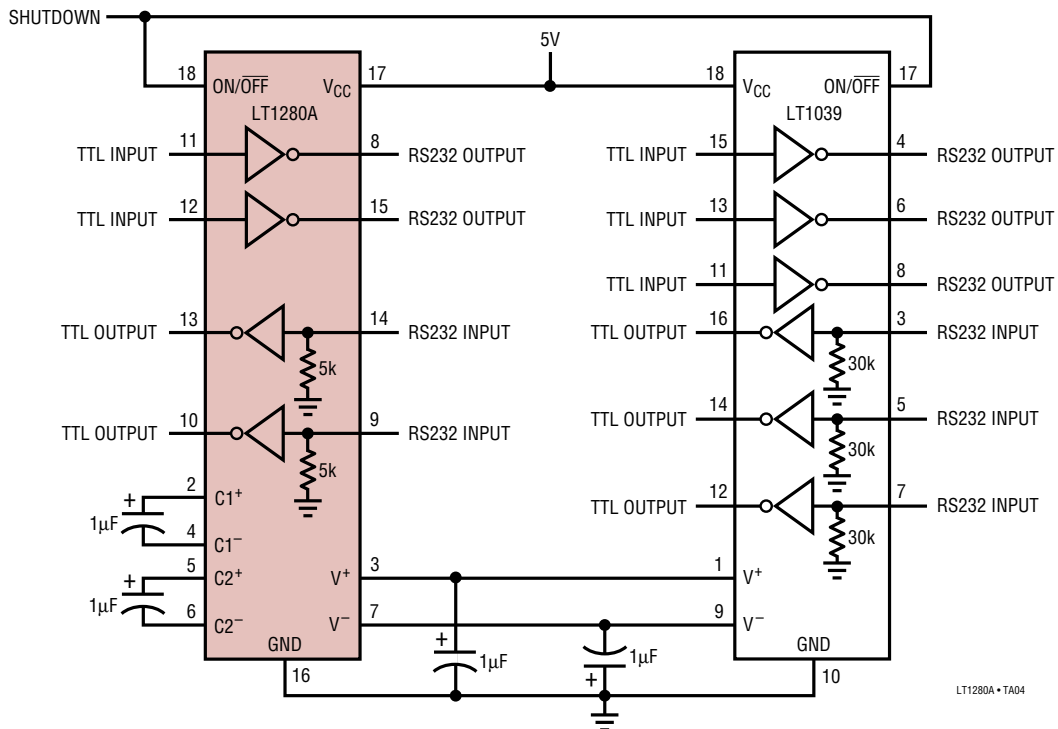
## ESD PROTECTION

The RS232 line inputs of the LT1280A/LT1281A have on-chip protection from ESD transients up to  $\pm 10\text{kV}$ . The protection structures act to divert the static discharge safely to system ground. In order for the ESD protection to function effectively, the power supply and ground pins of the circuit must be connected to ground through low impedances. The power supply decoupling capacitors and charge pump storage capacitors provide this low impedance in normal application of the circuit. The only constraint is that low ESR capacitors must be used for bypassing and charge storage. ESD testing must be done with pins  $V_{CC}$ ,  $V_L$ ,  $V^+$ ,  $V^-$ , and GND shorted to ground or connected with low ESR capacitors.



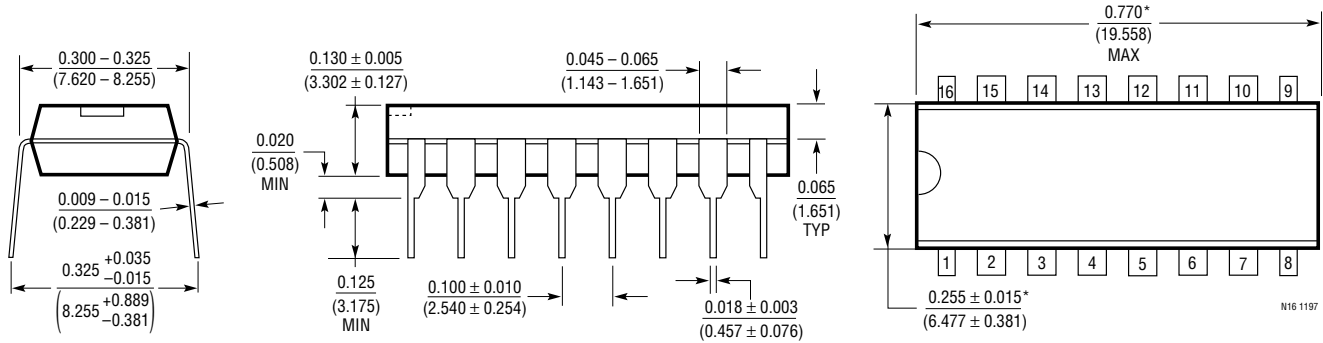
## TYPICAL APPLICATION

### Supporting an LT1039 (Triple Driver/Receiver)



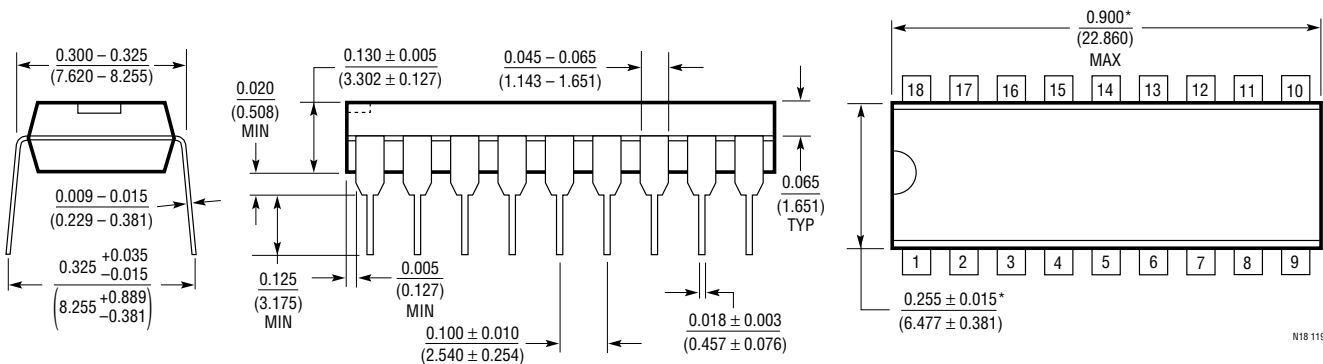
# PACKAGE DESCRIPTION Dimensions in inches (millimeters) unless otherwise noted.

## N Package 16-Lead PDIP (Narrow 0.300) (LTC DWG # 05-08-1510)



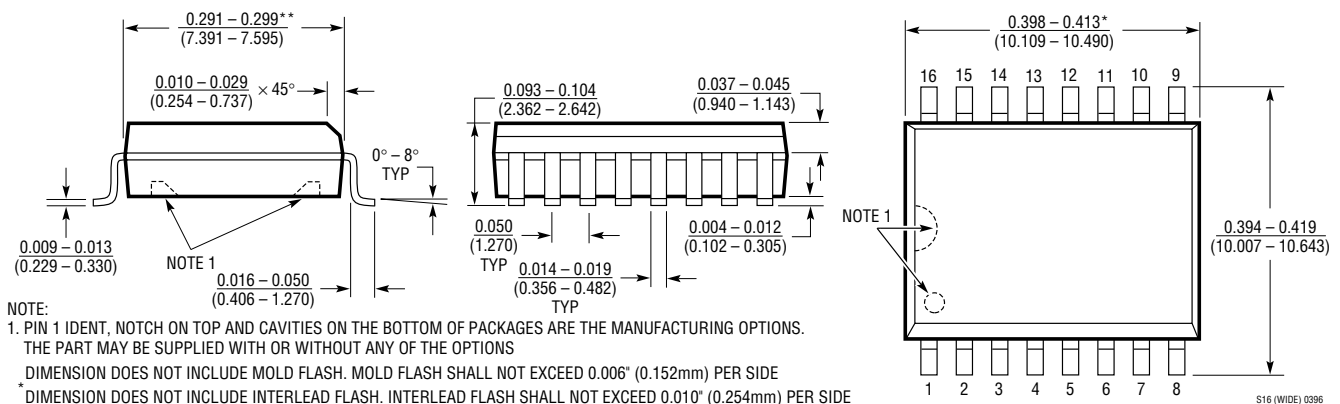
\*THESE DIMENSIONS DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS.  
MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED 0.010 INCH (0.254mm)

## N Package 18-Lead PDIP (Narrow 0.300) (LTC DWG # 05-08-1510)



\*THESE DIMENSIONS DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS.  
MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED 0.010 INCH (0.254mm)

## SW Package 16-Lead Plastic Small Outline (Wide 0.300) (LTC DWG # 05-08-1620)



NOTE:

1. PIN 1 IDENT, NOTCH ON TOP AND CAVITIES ON THE BOTTOM OF PACKAGES ARE THE MANUFACTURING OPTIONS.  
THE PART MAY BE SUPPLIED WITH OR WITHOUT ANY OF THE OPTIONS

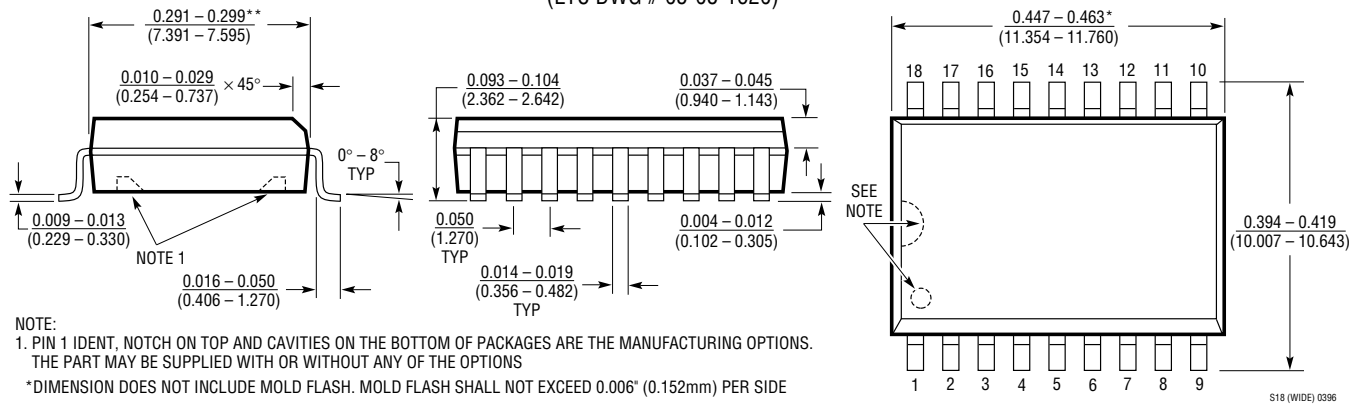
\*DIMENSION DOES NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.006" (0.152mm) PER SIDE

\*\*DIMENSION DOES NOT INCLUDE INTERLEAD FLASH. INTERLEAD FLASH SHALL NOT EXCEED 0.010" (0.254mm) PER SIDE

# LT1280A/LT1281A

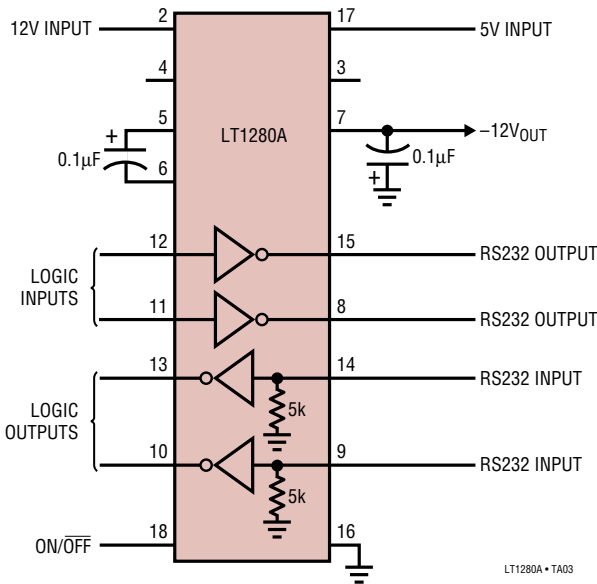
## PACKAGE DESCRIPTION Dimensions in inches (millimeters) unless otherwise noted.

### SW Package 18-Lead Plastic Small Outline (Wide 0.300) (LTC DWG # 05-08-1620)



## TYPICAL APPLICATION

### Operation Using 5V and 12V Power Supplies



## RELATED PARTS

PART NUMBER	DESCRIPTION	COMMENTS
LT1180A/LT1181A	5V 2-Driver/2-Receiver RS232 Transceivers	Pin Compatible with LT1280A/LT1280A
LT1381	5V 2-Driver/2-Receiver RS232 Transceiver	Narrow 16-Pin SO Package
LT1780/LT1781	5V 2-Driver/2-Receiver RS232 Transceiver	IEC 1000-4-2 Level 4 Compliance