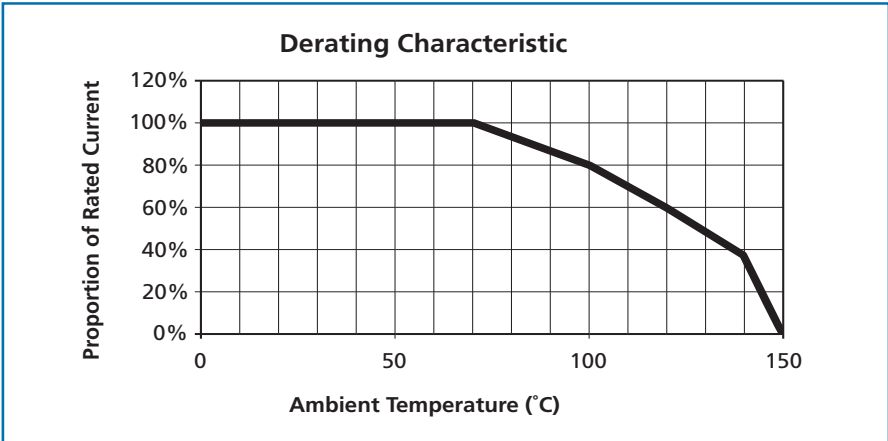


LRZ Series

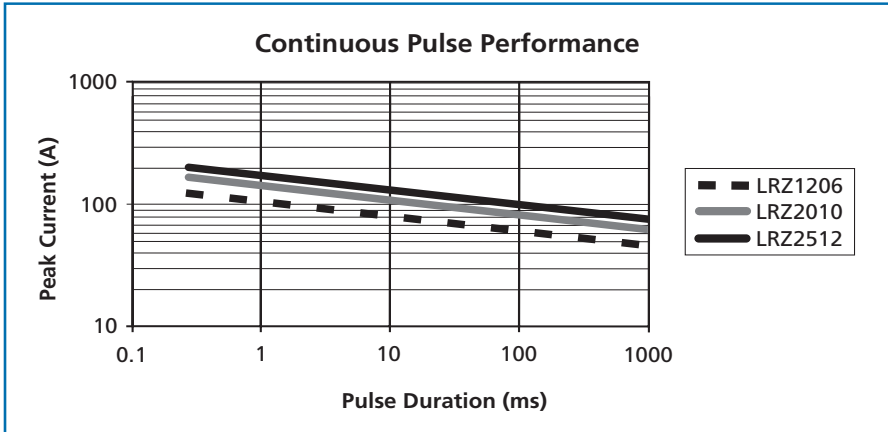
AEC-Q200 Table 7		Method	Result
ref	Test		
3	High Temp. Exposure	MIL-STD-202 Method 108	Pass (see note 1)
4	Temperature Cycling	JESD22 Method JA-104	Pass (see note 1)
6	Moisture Resistance	MIL-STD-202 Method 106	Pass (see note 1)
7	Biased Humidity	MIL-STD-202 Method 103	Pass (see note 1)
8	Operational Life (Cyclic Load)	MIL-STD-202 Method 108	Pass (see note 1)
14	Vibration	MIL-STD-202 Method 204	Pass (see note 1)
15	Resistance to Soldering Heat	MIL-STD-202 Method 210	Pass (see note 1)
16	Thermal Shock	MIL-STD-202 Method 107	Pass (see note 1)
18	Solderability	J-STD-002	>95% coverage
21	Board Flex	AEC-Q200-005	Pass (see note 1)
22	Terminal Strength	AEC-Q200-006	Pass (see note 1)
	Leach Resistance	Solder dip at 250°C	90s minimum

- Notes:
- 1. AEC qualification based on testing of structurally similar LRF Series low value chip resistors, of which LRZ is the zero-ohm version. ΔR measurements are not applicable to the zero-ohm version.
 - 2. Although 2010 and 2512 sizes have passed temperature cycling and thermal shock, it is in general not recommended that ceramic chips this large be used on FR4 in a severe temperature cycle environment due to the possibility of solder joint fatigue.
 - 3. Full AEC-Q200 qualification applies to sizes 0603, 1206, 2010 and 2512

Temperature Derating



Pulse Performance



General Note
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BI Technologies IRC Welwyn

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LRZ Series

Application Notes

Conventional thick film “zero-Ohm” jumper chips typically have up to 50mΩ resistance values and 1 to 2A current ratings. LRZ jumper chips offer a solution for currents over an order of magnitude greater by combining lower resistance values with better thermal conductivity.

Care should be taken when designing the associated printed circuit board tracks to ensure that they can carry the required current without excessive heating, for example by using multiple layers thermally linked with many vias. Any temperature rise caused by power dissipated in the PCB tracks themselves should be allowed for when calculating the ambient temperature in order to determine whether power de-rating should be applied. The minimum recommended pad and trace areas close to the resistor stated under Electrical Data should be provided at each terminal.

Pad and trace area close to the resistor is defined as being the total copper area within two squares of the edge of the solder pad, plus the solder pad area.

For multi-layer PCB's, this minimum area requirement should be met by surface layers rather than buried layers. The actual solder pad area follows the normal design rules for chip resistors.

LRZ jumper chips themselves can operate at a maximum temperature of 150°C (see performance above). For conventionally soldered jumper chips, the joint temperature should not exceed 110°C. This condition is met when the stated current levels at 70°C are used.

Ordering Procedure

This product has two valid part numbers:

European (Welwyn) Part Number: LRZ1206-R000 (1206, Pb-free)

L	R	Z	1	2	0	6	-	R	0	0	0		
1			2				3					4	
1	2	3	6										
Type	Size	Value	Termination & Packing										
LRZ	0603	R000	Omit for Pb-free, standard packing										
	0805		T1	Pb-free, 1000/reel (non-standard)									
	1206		PB	SnPb finish, standard packing									
	2010		T1PB	SnPb finish, 1000/reel (non-standard)									
	2512		Standard packing is tape & reel										
			0603	5000/reel									
		0805, 1206 & 2010	3000/reel										
		2512	1800/reel										

USA (IRC) Part Number: LRC-LRZ1206LF-R000 (1206, Pb-free)

L	R	C	-	L	R	Z	1	2	0	6	L	F	-	R	0	0	0
1			2			3			4			5					

1 Family ²	2 Model	3 Size ¹	4 Termination	5 Value	Packing	
LRC	LRZ	1206	Omit for SnPb	R000	Standard packing is tape & reel	
		2010	LF = Pb-free		1206 & 2010	3000/reel
		2512			2512	1800/reel

Note 1: Sizes 0603 & 0805 are only available under European part numbering.

Note 2: It is advisable to include the family in the USA part number, and it is essential to do so when ordering SnPb termination parts.

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