



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop See fig. 1	$V_{FM}^{(1)}$	3 A	$T_J = 25\text{ }^{\circ}\text{C}$	0.53	V
		6 A		0.67	
		3 A	$T_J = 125\text{ }^{\circ}\text{C}$	0.49	
		6 A		0.62	
Maximum reverse leakage current See fig. 2	$I_{RM}^{(1)}$	$T_J = 25\text{ }^{\circ}\text{C}$	$V_R = \text{Rated } V_R$	2	mA
		$T_J = 125\text{ }^{\circ}\text{C}$		24	
Threshold voltage	$V_{F(TO)}$	$T_J = T_J \text{ maximum}$		0.34	V
Forward slope resistance	r_t			37.33	mΩ
Typical junction capacitance	C_T	$V_R = 5\text{ V}_{DC}$ (test signal range 100 kHz to 1 MHz), $25\text{ }^{\circ}\text{C}$		189	pF
Typical series inductance	L_S	Measured lead to lead 5 mm from package body		5.0	nH
Maximum voltage rate of change	dV/dt	Rated V_R		10 000	V/μs

Note

(1) Pulse width < 300 μs , duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		-40 to +150	°C
Maximum thermal resistance, junction to case	R _{thJC}	DC operation See fig. 4	4.7	°C/W
Approximate weight			0.3	g
			0.01	oz.
Marking device		Case style D-PAK (similar to TO-252AA)	30WQ04FN	

Note

(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

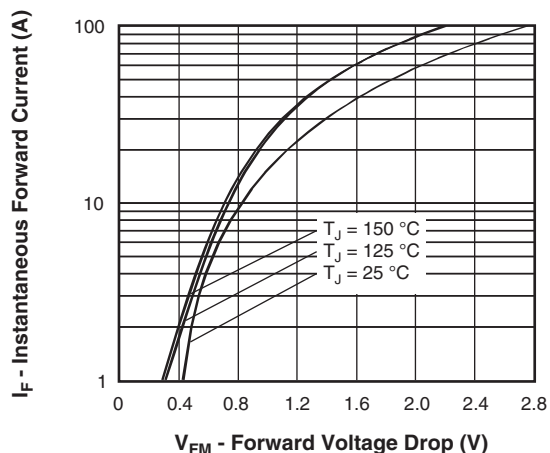


Fig. 1 - Maximum Forward Voltage Drop Characteristics

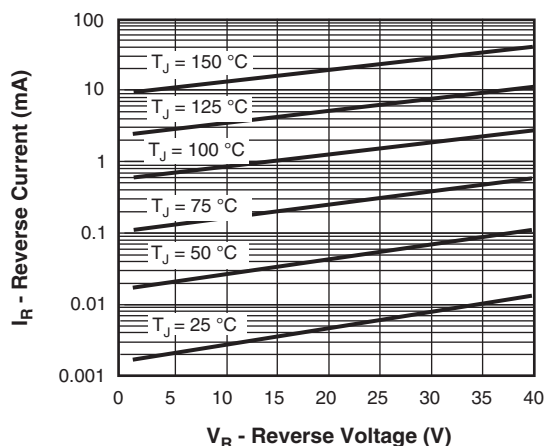


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

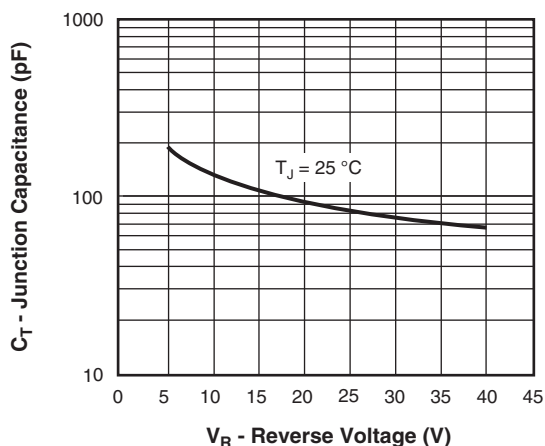
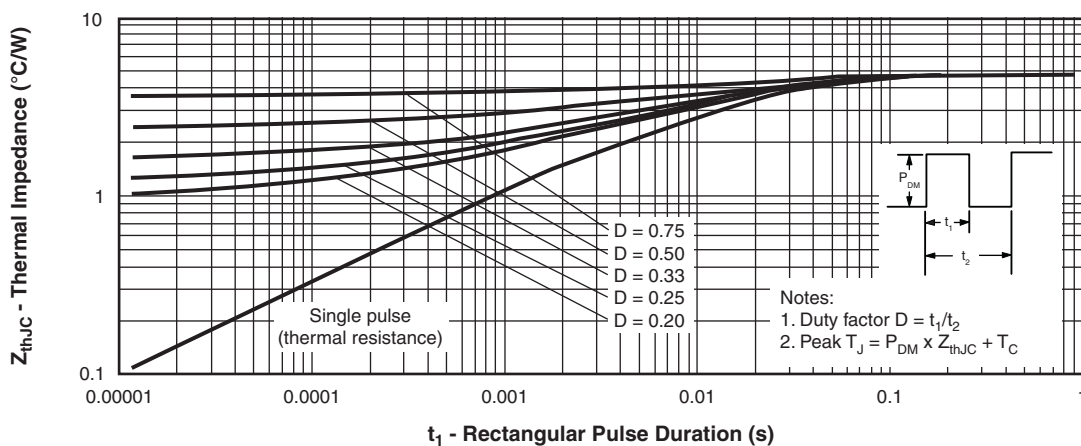


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

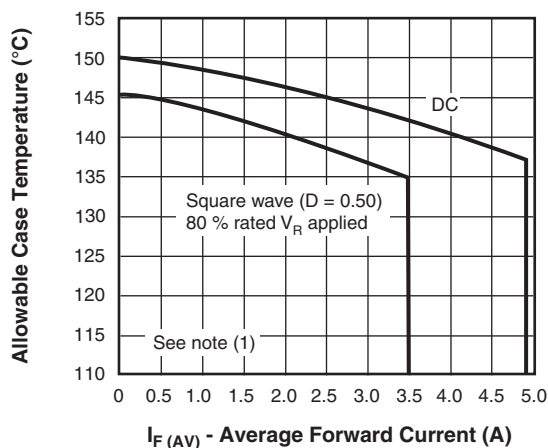


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

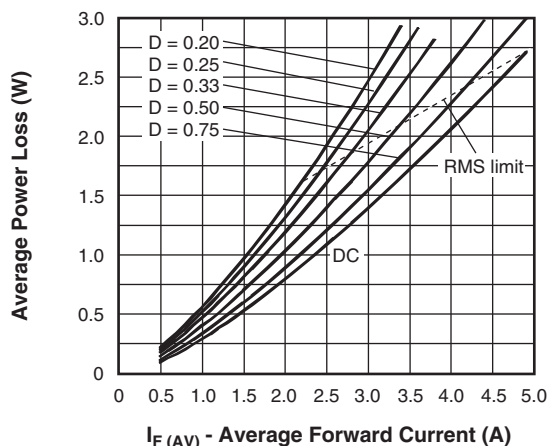


Fig. 6 - Forward Power Loss Characteristics

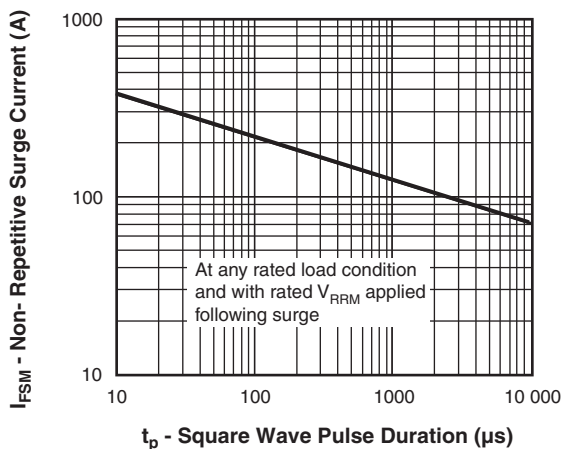


Fig. 7 - Maximum Non-Repetitive Surge Current

Note

- (1) Formula used: $T_C = T_J - (P_d + P_{d_{REV}}) \times R_{thJC}$;
 P_d = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6);
 $P_{d_{REV}}$ = Inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 80\%$ rated V_R



ORDERING INFORMATION TABLE

Device code	VS-	30	W	Q	04	FN	TRL	PbF
	1	2	3	4	5	6	7	8

- | | | |
|---|---|---|
| 1 | - | Vishay Semiconductors product |
| 2 | - | Current rating (3.5 A) |
| 3 | - | Package identifier:
W = D-PAK |
| 4 | - | Schottky "Q" series |
| 5 | - | Voltage rating (04 = 40 V) |
| 6 | - | FN = TO-252AA (D-PAK) |
| 7 | - | <ul style="list-style-type: none">• None = Tube (50 pieces)• TR = Tape and reel• TRL = Tape and reel (left oriented)• TRR = Tape and reel (right oriented) |
| 8 | - | PbF = Lead (Pb)-free |

LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?95016
Part marking information	www.vishay.com/doc?95059
Packaging information	www.vishay.com/doc?95033
SPICE model	www.vishay.com/doc?95288
	www.vishay.com/doc?95630



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