Absolute Maximum Ratings (Ta = 25°C)

	Characteristic	Symbol	Rating	Unit	
LED	Forward current		lF	20	mA
	Forward current derating	(Ta ≥ 70 °C)	∆IF/°C	-0.36	mA/°C
	Pulse forward current	(Note 1)	IFP	40	mA
	Peak transient forward current	(Note 2)	IFPT	1	А
	Reverse voltage		VR	5	V
	Input power dissipation		PD	45	mW
	Input power dissipation derating	$(Ta \ge 70 \text{ °C})$	∆PD/°C	-0.82	mW/°C
	Output current		10	8	mA
	Output current derating	(Ta ≥ 70 °C)	∆10/°C	-0.3	mA/°C
or	Peak output current		lop	16	mA
Detector	Supply voltage	\langle	Vcc	-0.5 to 30	$\langle v \rangle$
ð	Output voltage	(a)	Vo	-0.5 to 20	Vy -
	Output power dissipation))Po <		mW
	Output power dissipation derating	(Ta ≥ 70 °C)	Po/°C	-1.8	mW/°C
Ope	rating temperature range	Topr	-55 to 100	°C	
Stor	Storage temperature range			-55 to 125	°C
Lead solder temperature(10 s)			T _{sol}	260	°C
Isolation Voltage (AC,60 s., R.H.≤ 60°%)		(Note 3)	BVS	3750	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

(Note 1): 50 % duty cycle, 1 ms pulse width. Derate 0.72mA / °C above 70 °C.

(Note 2): Pulse width $\leq 1 \mu s$, 300 pps.

(Note 3): Device considered a two-terminal device: Pins 1 and 3 shorted together, and pins 4, 5 and 6 shorted together.

Electrical Characteristics (Ta = 25°C)

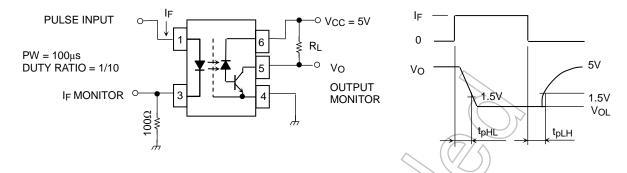
Characteristic		Symbol	Test Condition	Min.	Тур.	Max.	Unit	
LED	Forward voltage	VF	I _F = 16 mA	1.22	1.42	1.72	V	
	Forward voltage temperature coefficient	$\Delta V_F / \Delta Ta$	IF = 16 mA	_	-2	_	mV /°C	
	Reverse current	IR	VR = 3 V	/	_	10	μΑ	
	Capacitance between terminals	Ст	VF = 0 V, f = 1 MHz		30	_	pF	
Detector	High level output current	IOH (1)	$I_F = 0 \text{ mA}, V_{CC} = V_0 = 5.5 \text{ V}$	\bigcirc	3	500	nA	
		IOH (2)	$I_F = 0 \text{ mA}, V_{CC} = 30 \text{ V}$ $V_O = 20 \text{ V}$	5)-	_	5	μΑ	
		Юн	IF = 0 mA, V _{CC} = 30 V V _O = 20 V, Ta = 70 °C	_	_	50		
	High level supply current	Іссн	IF = 0 mA, VCC = 30 V	_	0.01	1	μΑ	
Current transfer ratio		IO / IF	$I_F = 16 \text{ mA}, V_{CC} = 4.5 \text{ V}$ $V_O = 0.4 \text{ V}$	20		>-	%	
Low level output voltage		Vol	IF = 16 mA, VCC = 4.5 V IO = 2.4 mA	Θ	26	0.4	V	
Isolation resistance		Rs	R.H.≤ 60 %, Vs = 500 V	5×10 ¹⁰	1014	_	Ω	
Stray capacitance between input to output		Cs	Vs=0 V; f=1 MHz	∂	0.8	_	pF	
Isolation		BVs	AC, 60 s	3750	_	_	Vrms	

Switching Characteristics (Ta = 25°C, VCC = 5V)

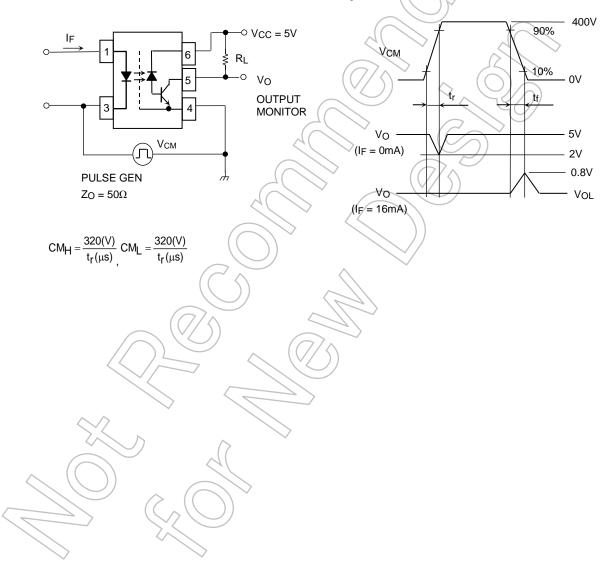
Characteristic	Symbol	Test Cir– cuit	Test Condition	Min.	Тур.	Max.	Unit
Propagation delay time $(H \rightarrow L)$	tpHL	1	$I_F = 0 \rightarrow 16 \text{ mA}$ $V_{CC} = 5 \text{ V}, \text{ RL} = 1.9 \text{ k}\Omega$	_	_	0.8	μS
Propagation delay time $(L \rightarrow H)$	tpLH		IF = 16→ 0 mA Vcc = 5 V, RL = 1.9 kΩ	_	—	0.8	μs
Common mode transient immunity at high output level	Смн	2	IF = 0 mA, $V_{CM} = 400 \text{ V}_{p-p}$ $R_L = 4.1 \text{ k}\Omega$	5000	10000		V / μs
Common mode transient immunity at low output level	CML		$f_{F} = 16 \text{ mA},$ V _{CM} = 400 V _P -p R _L = 4.1 kΩ	-5000	-10000		V / μs

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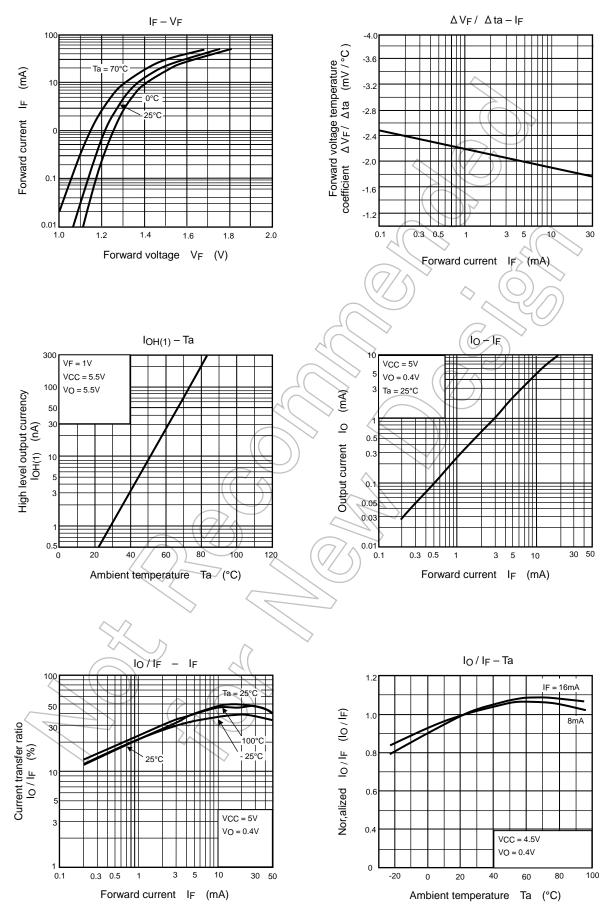
Test Circuit 1: Switching Time Test Circuit



Test Circuit 2: Common Mode Transient Immunity Test Circuit



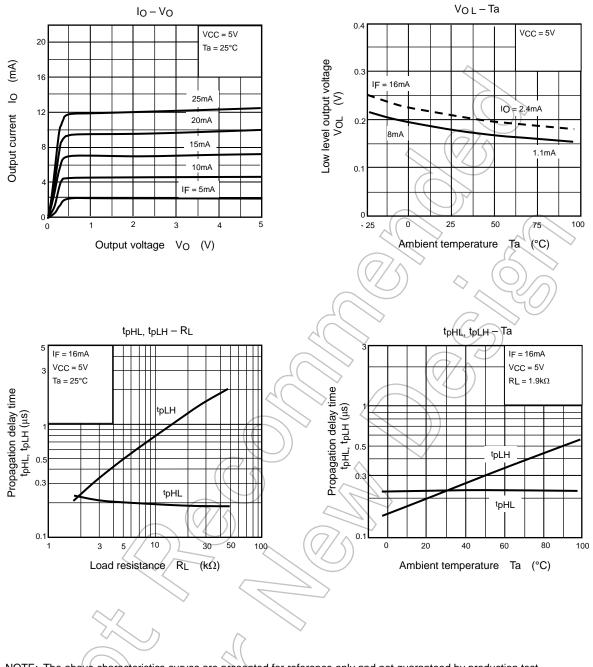
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