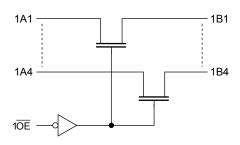
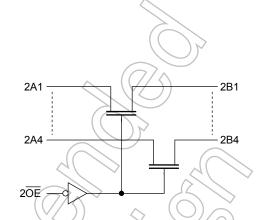
#### **Truth Table**

Inputs	Function				
ŌĒ	Function				
L	A port = B port				
Н	Disconnect				

### **System Diagram**





## **Absolute Maximum Ratings (Note)**

Characteristics	Symbol	Rating	Unit
Power supply range	V <sub>CC</sub>	-0.5 to 7.0	V
DC input voltage	V <sub>IN</sub>	-0.5 to 7.0	У
DC switch voltage	Vs	-0.5 to 7.0	/[/
Input diode current	IIK	50	mA
Continuous channel circuit	Is	)) 128	mA
Power dissipation	PD	180	mW
DC V <sub>CC</sub> /ground current	ICC/IGND	±100	mA
Storage temperature	7 T <sub>stg</sub>	-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

# Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	4.5 to 5.5	V
Input voltage	V <sub>IN</sub>	0 to 5.5	V
Switch voltage	Vs	0 to 5.5	V
Operating temperature	T <sub>opr</sub>	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 10	ns/V

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V<sub>CC</sub> or GND.



#### **Electrical Characteristics**

### DC Characteristics ( $Ta = -40 \text{ to } 85^{\circ}\text{C}$ )

Charac	teristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Min	Typ. (Note 1)	Max	Unit
Input voltage	"H" level	$V_{IH}$	_	4.5 to 5.5	2.0	_	_	V
Input voltage	"L" level	$V_{IL}$	_	4.5 to 5.5		_	0.8	V
Lligh level output voltage			IOH=-1μA	4.75	2.3	2.8	3.2	
i ligit-level outpo	High-level output voltage (Note 2)	$V_{OH}$	V <sub>IS</sub> = V <sub>CC</sub>	5.0	2.5	3.0	3.4	V
	(Note 2)		NIS - ACC	5.25	2.7	3.2	3.6	
Input leakage c	urrent	I <sub>IN</sub>	V <sub>IN</sub> = 0 to 5.5 V	4.5 to 5.5	<i>Y</i>	_	±1.0	μΑ
Power off leaka	ge current	loff	A, B, $\overline{OE} = 0$ to 5.5 V	0	· –	_	±1.0	μΑ
Off-STATE leak (switch off)	age current	I <sub>SZ</sub>	A, B = 0~5.5 V, OE = V <sub>CC</sub>	4.5 to 5.5	_		±1.0	μА
ON resistance (Note 3) F	R <sub>ON</sub>	lia 64 mA	4.5	- /	5	9		
		V <sub>IS</sub> = 0 V	4.75	-6	5	8		
		$I_{IS} = 30 \text{ mA}$	4.5	7	5	9	Ω	
		118 = 30 1114	4.75	7	5/	8	22	
		V <sub>IS</sub> = 2.3 V, I <sub>IS</sub> = 15 mA	4.5 (		35	65		
		V S - 2.5 V, I S - 15 (IIA	4.75		35	50		
Quiescent supp	ly current	Icc	$V_{IN} = V_{CC}$ or GND, $I_{OUT} = 0$	(5.5/<	<b>\</b> -	_	10	μΑ
Increase in I <sub>CC</sub>	per input	Δl <sub>CC</sub>	V <sub>IN</sub> = 3.4 V (one input)	5.5	/ _	_	2.5	mA

Note 1: Typical values are at  $V_{CC} = 5 \text{ V}$ ,  $Ta = 25^{\circ}\text{C}$ .

Note 2: It recommends that this device uses Pull-up resistance when adding and using resistance for an output terminal. Since it couses to drop a V<sub>OH</sub> voltage level when using Pull-down resistance for an output terminal.

Note 3: Measured by the voltage drop between A and B pins at the indicated current through the switch. On resistance is determined by the lower of the voltages on the two (A or B) pins.

## AC Characteristics (Ta = -40~85°C)

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Min	Max	Unit
Propagation delay time (bus to bus)	t <sub>pLH</sub>	Figure 1, Figure 2 (Note)	4.5	_	0.32	ns
Output enable time	t <sub>pZL</sub> t <sub>pZH</sub>	Figure 1, Figure 3	4.5	l	7.0	ns
Output disable time	t <sub>pLZ</sub>	Figure 1, Figure 3	4.5		7.0	ns

Note: The propagation delay time is calculated by the RC (on-resistance and load capacitance) time constant.

## **Capacitive Characteristics (Ta = 25°C)**

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Тур.	Unit
Control pin input capacitance	C <sub>IN</sub>	(Note)	5.0	3	pF
Switch terminal capacitance	C <sub>I/O</sub>	$\overline{OE} = V_{CC}$ (Note)	5.0	10	pF

Note: This parameter is guaranteed by design.

### **AC Test Circuit**

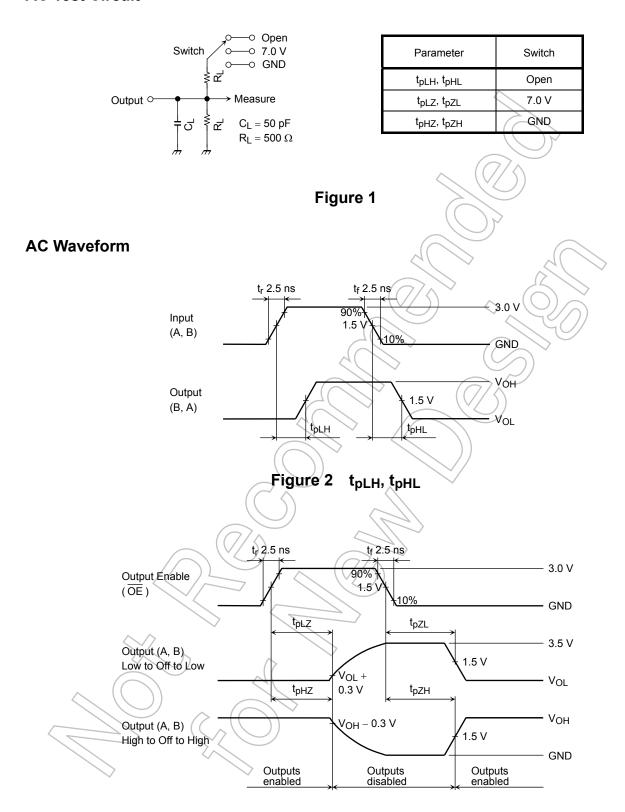


Figure 3  $t_{pLZ}$ ,  $t_{pHZ}$ ,  $t_{pZL}$ ,  $t_{pZH}$ 

# **V<sub>OH</sub> – V<sub>CC</sub> Characteristics** (typ.)

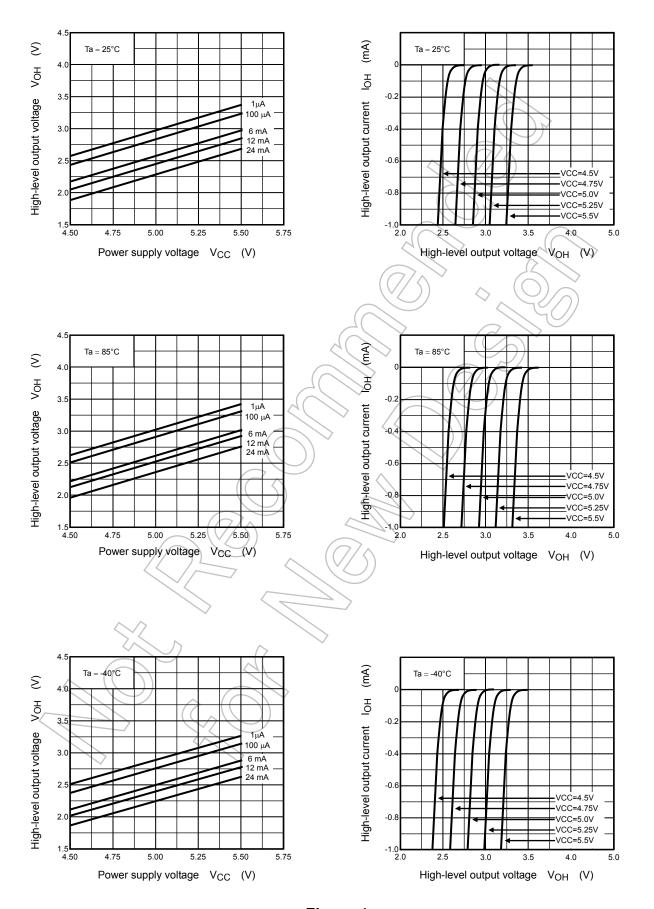
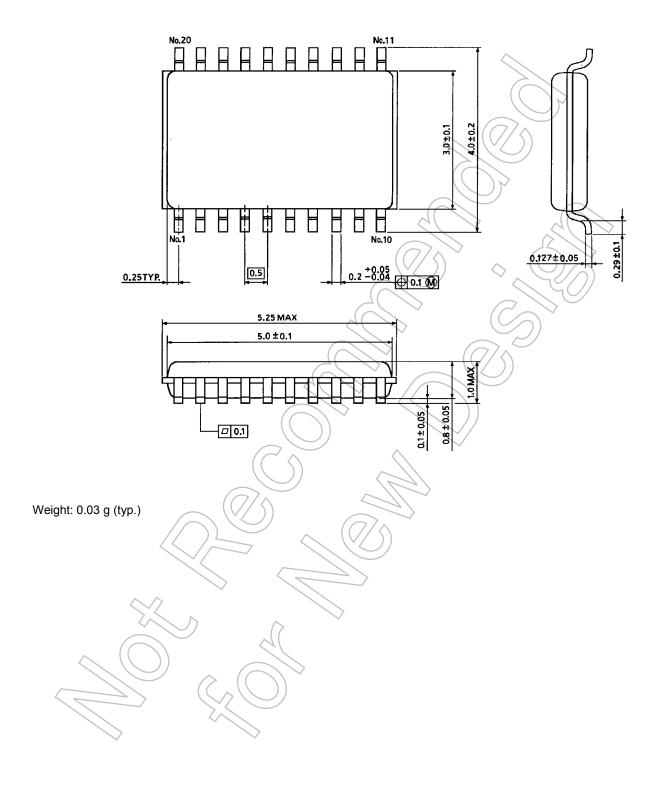


Figure 4

5 2014-03-01



# **Package Dimensions**



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