

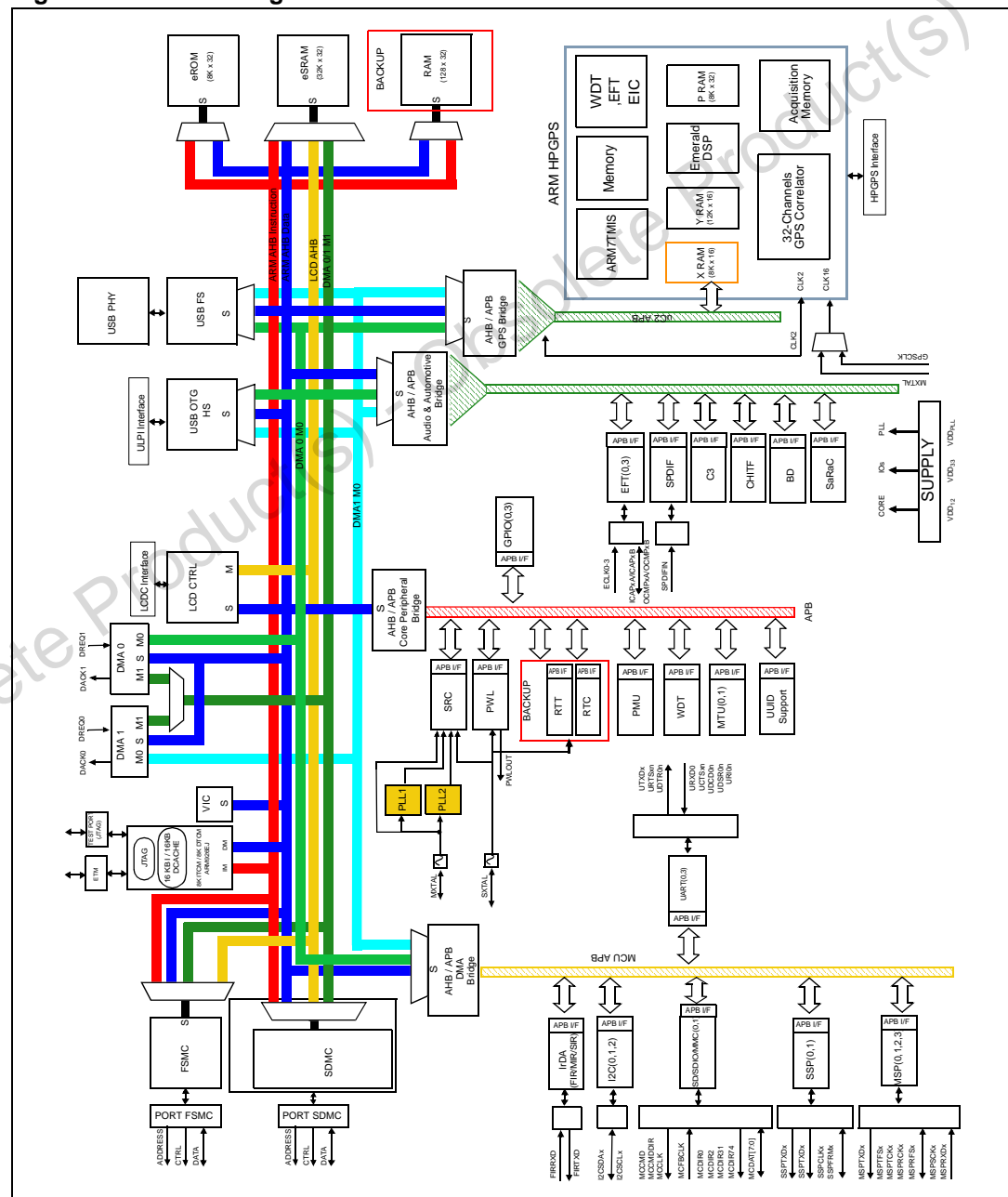
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The STA2062 is an highly integrated SOC application processor combining host capability with embedded GPS.

STA2062 targets in vehicle and mobile navigation (PND), telematics, advance audio and connectivity systems.

Figure 1: Block diagram gives an overview of the complete processor, showing how the ARM926 microcontroller and its peripherals are interfaced.

Figure 1. Block diagram

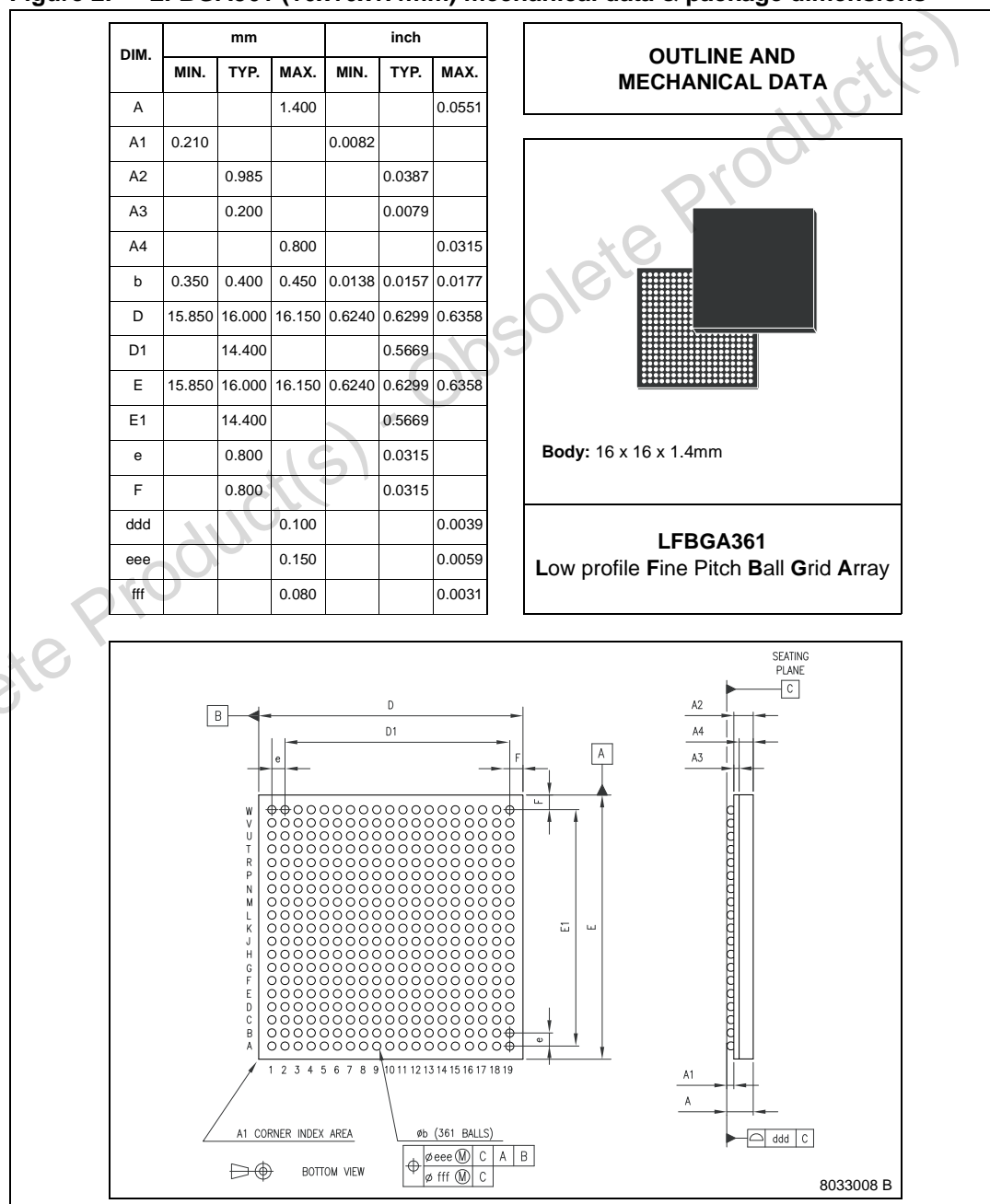


2 Package information

In order to meet environmental requirements, ST offers this device in ECOPACK[®] packages. This package has a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label.

ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.

Figure 2. LFBGA361 (16x16x1.4mm) mechanical data & package dimensions



3 Revision history

Table 2. Document revision history

Date	Revision	Changes
3-Oct-2007	1	Initial release.
12-Oct-2007	2	Minor changes.
11-Apr-2008	3	Typo and graphic errors correction.
17-Sep-2013	4	Updated disclaimer.

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