ABSOLUTE MAXIMUM RATINGS

Power Dissipation ..... Internally Limited
Storage Temperature Range ..... -65°C to +150°C
Operating Junction Temperature Range
SPX1582 Control Section ..... 0°C to +125°C
SPX1582 Power Transistor ..... 0°C to +150°C
Input Supply Voltage 6V

V_{CTRL} Input Voltage 13V

ELECTRICAL CHARACTERISTICS

Electrical characteristics at $V_{OUT} = V_{SENSE}$, V_{ADJ} = 0V, T_A=25°C, $C_{CTL} = C_{PWR} = C_{OUT}$ = 33µF tantalum cap unless otherwise specified. The ♦ denotes the specifications which apply over full temperature range -40°C to +85°C, unless otherwise specified.

PARAMETER	MIN	TYP	MAX	UNITS		CONDITIONS
2.5V Version						
Output Voltage	2.450	2.5	2.550	V		V_{CTRL} = 6.0V to 12V, V_{IN} = 3.0V to 5.0V, I_O =10mA
	2.400		2.600		♦	I_O = 10mA to 3A
2.8V Version						
Output Voltage	2.744	2.8	2.856	V		V_{CTRL} = 6.3V to 12V, V_{IN} =3.3V + 0.8V, I_O=10mA
	2.688		2.912		♦	I_O = 10mA to 3A
3.3V Version						
Output Voltage	3.234	3.3	3.366	V		V_{CTRL} = 6.3V to 12V, V_{IN} = 3.3V + 0.8V, I_O=10mA
	3.168	3.3	3.432		♦	I_O = 10mA to 3A
All Voltage Options						
Reference Voltage	1.238	1.25	1.263	V		V_{CTRL} = 2.75V, V_{IN} = 2.00V, I_O = 10mA
						V_{CTRL}=2.7V to 12V,V_{IN} = 2.05V to 5.5V,I_O=10mA to 7A
Line Regulation				mV		V_{CTRL} = 2.5V to 12V, V_{IN} = 1.75 to 5.5V, I_O=10mA
		1.0	3.0		♦	V_{ADJ} = 0V
Load Regulation (Note 1)		1.0	5.0	mV	♦	V_{CTRL} = 2.75V, V_{IN} = V_{OUT} + 1.5V, I_O=10mA to 3A, V_{ADJ} = 0V
Dropout Voltage Min. V_{CTRL} (Note 2) (V_{IN} - V_{OUT})		1.05	1.18	V		V_{ADJ} = 0V V_{IN} = 2.05V, I_O = 1A
Dropout Voltage Min. V_{IN} (Note 2) (V_{IN} - V_{OUT})		0.40	0.50	V		V_{ADJ} = 0V V_{IN} = 2.75V, I_O = 3A
Current Limit	3.1			A		V_{CTRL} = 2.75V, V_{IN} = 2.05V, Δ V_{OUT} = 100mV, V_{ADJ} = 0V
Minimum Load Current		5	10	mA	♦	V_{CTRL} = 5V, V_{IN}=3.3V, V_{ADJ}=0V
Thermal Regulation		0.002	0.02	%/W		30ms Pulse
Ripple Rejection	60	80		dB		V_{CTRL} = 3.75V, V_{IN} = 3.75V, I_O = 2.3A, V_{ADJ}=0V T_J=25, V_{RIPPLE}=1Vpp at 120Hz
Control Pin Current		60	120	mA	♦	V_{ADJ} = 0V V_{CTRL} = 2.75V, V_{IN} = 2.05V, I_O = 3A
Adjustble Pin Current		50	120	µA	♦	V_{CTRL} = 2.75V, V_{IN} = 2.05V, V_{ADJ}=0V, I_O = 10mA
Thermal Resistance TO-220-5			3	°C/W		Junction to Case (θ_{JC})
			29.3	°C/W		Junction to Ambient (θ_{JA})
Thermal Resistance TO-263-5			3	°C/W		Junction to Case (θ_{JC})
			31.2	°C/W		Junction to Ambient (θ_{JA})

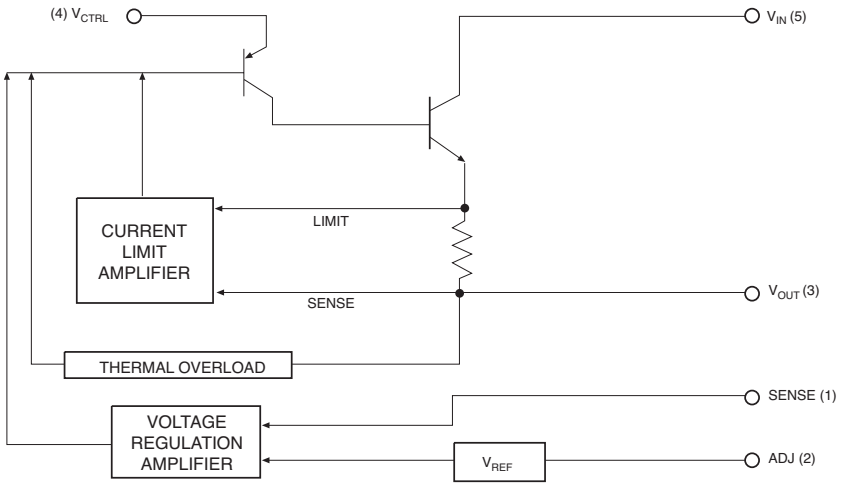
Note 1: Low duty cycle pulse testing with Kelvin connections is required to order to maintain accurate data.
Note 2: Dropout voltage is defined as the minimum differential between V_{IN} and V_{OUT} or V_{CTRL} and V_{OUT} required to maintain regulation at V_{OUT} 95% Nominal V_{OUT}.
Note 3: V_{REF} is measured across Adjust pin to Sense pin.

PIN DESCRIPTION

PIN NUMBER	PIN NAME	DESCRIPTION
1	SENSE	Allows Kelvin sense of V_{OUT} at the load. (Positive side of the reference voltage of the device).
2	ADJ	Negative side of the reference voltage for the device. Adding a small bypass capacitor from the ADJ pin to ground will improve the transient response.
3	V_{OUT}	Power output of the device.
4	V_{CTRL}	Supply pin for the control circuitry of the device. The current flow into this pin will be about 1% of the output current. V_{CTRL} must be between 1.0V and 1.3V greater than the output voltage for the device to regulate.
5	V_{IN}	Output load current is supplied through this pin. V_{IN} must be between 0.1V and 0.8V greater than the output voltage for this device to regulate.

Note that TAB is internally connected to Pin 3.

FUNCTIONAL DIAGRAM



The SPX1582 is designed as a high performance and low cost solution for applications requiring a lower dropout than traditional NPN regulators.

The SPX1582 uses a separate input voltage V_{CTRL} ($V_{CTRL} \geq V_{OUT} + 1.3V$) to minimize the dropout voltage. This allows the 2.5V power for the load to come from a 3.3V system supply. As an added benefit this will reduce the heat dissipation*, and lower heatsink and cooling fan costs.

The SPX1582 can power the 2.5V core voltage for microprocessors such as a PentiumPC™, P55C™, AMD5k86™ and K6™ and the IBM PowerPC™ 603EV and 604EV processors.

A typical application would use 3.3V for V_{IN} and 5.0V for V_{CTRL} from a motherboard power supply to provide a nominal 2.5V output. Using the sense pin provides a Kelvin measurement of output for reducing resistance-associated errors.

Power Up Sequencing

The SPX1582 requires a power up sequence in that V_{IN} must be applied before V_{CTRL} to prevent a latchup condition. If this is not possible, then a 10Ω series resistor should be added to the V_{CTRL} input to prevent the device from entering into latchup if V_{CTRL} is applied before V_{IN}.

Adjustable Regulator Design

1.25V reference voltage is being developed between the SENSE pin and the ADJ pin of the SPX1582. Adding two external resistors (see fig 1.) will allow setting the output voltage from 1.25V to 6V. R_1 is chosen so that this current is specified at a minimum load current of 10mA. R_2 is given by the formula:

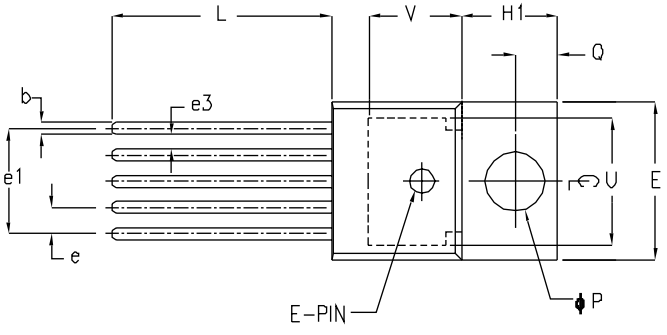
$$V_{OUT} = V_{REF} (1+ R_2/R_1) + I_{ADJ} (R_2).$$

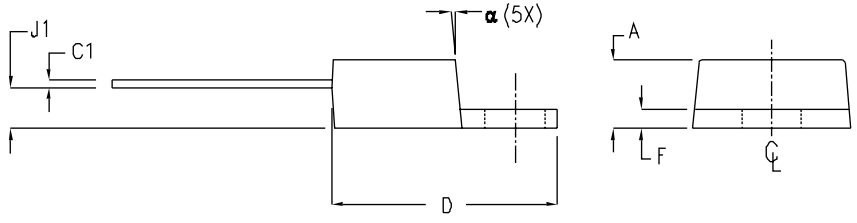
The current flowing from the ADJ pin is typicaly 50µA. This ADJ pin contributes to the final V_{OUT} but is usually neglected. Connecting the sense pin to the top of the resistor divider will improve load regulation.

Lowering Noise

For the fixed voltage device, adding a capacitor at the GND pin will improve transient response. This capacitor is chosen in the range of 1µF to 0.1µF and will depend on the amount of output capacitance in the system.

Controlling Dimension is Inches

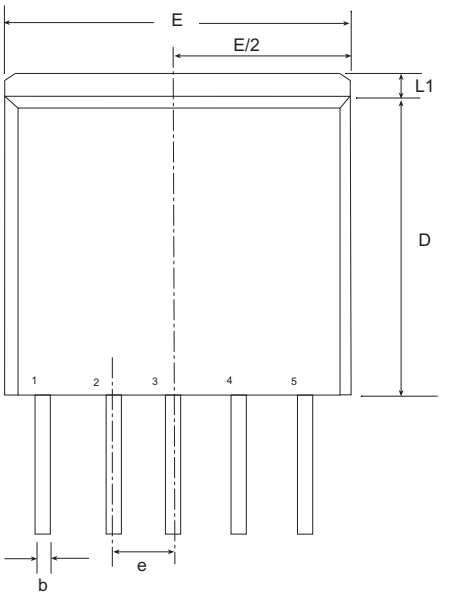




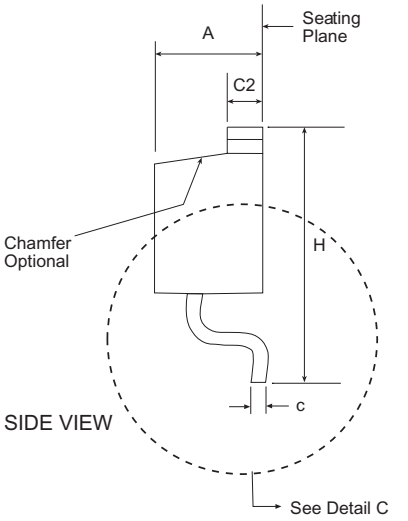
POS	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.160	.190	4.06	4.83
b	.025	.040	0.63	1.04
C1	.014	.022	0.36	0.56
D	.560	.590	14.22	14.99
E	.385	.415	9.78	10.54
e	.062	.072	1.57	1.83
e1	.263	.273	6.68	6.93
e3	.030	.040	0.76	1.02
F	.045	.055	1.14	1.40
H1	.234	.258	5.94	6.55
J1	.090	.115	2.29	2.92
ϕP	.146	.156	3.71	3.96
Q	.103	.113	2.62	2.87
L	.540	.560	13.72	14.22
α	3°	7°	3°	7°
U	.30 REF		7.62 REF	
V	.24 REF		6.10 REF	

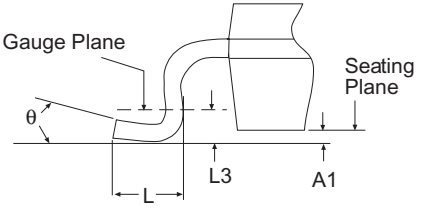
NOTE:
1. DIMENSION C1 APPLY FOR TIN PLATE FINISH.
2. FOR SOLDER DIP LEAD FINISH, DIMENSION C1 SHOULD BE .015"~.027"(0.38~0.69MM).
3. J1 DIMENSION TO BE GOVERNED BY FINAL VISUAL OUTGOING SPEC, CSM-220-017 (i.e. Y-AXIS BEND LEAD ALLOWED FOR 1 LEAD THICKNESS.

TO220 5L PKG OUTLINE

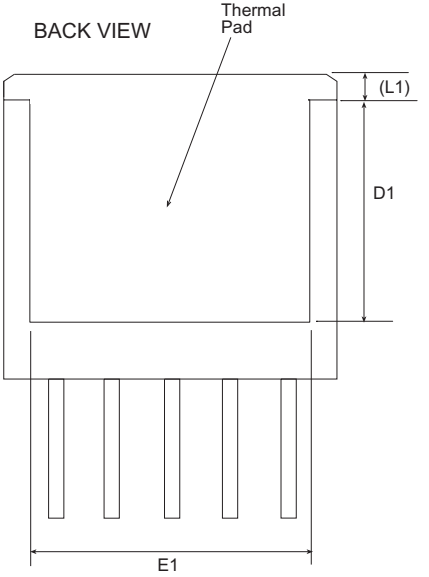


FRONT VIEW





DETAIL C



5 Pin TO-263 JEDEC TO-263 Variation BA						
	Dimensions in Inches: Controlling Dimension			Dimensions in Millimeters Conversion Factor: 1 Inch = 25.40 mm		
SYMBOL	MIN	NOM	MAX	MIN	NOM	MAX
A	0.160	-	0.190	4.06	-	4.83
A1	0.000	-	0.010	0.00	-	0.25
b	0.020	-	0.039	0.51	-	0.99
c	0.015	-	0.029	0.38	-	0.74
c2	0.045	-	0.065	1.14	-	1.65
D	0.330	-	0.380	8.38	-	9.65
D1	0.270	-	-	6.86	-	-
E	0.380	-	0.420	9.65	-	10.67
E1	0.245	-	-	6.22	-	-
e	.067 BSC			1.702 BSC		
H	0.575	-	0.625	14.61	-	15.88
L	0.070	-	0.110	1.78	-	2.79
L1	-	-	0.066	-	-	1.68
L3	.010 BSC			0.254 BSC		
Θ	0°	-	8°	0°	-	8°
SIPEX Pkg Signoff Date/Rev:				JL Jun12-06 / Rev B		

Part Number	Accuracy	Output Voltage	Packages
SPX1582U5	2.0%	Adj	5-Pin TO-220
SPX1582U5-2-5	2.0%	2.5V	5-Pin TO-220
SPX1582U5-2-8	2.0%	2.8V	5-Pin TO-220
SPX1582U5-3-3	2.0%	3.3V	5-Pin TO-220
SPX1582T5	2.0%	Adj	5-Pin TO-263
SPX1582T5/TR	2.0%	Adj	5-Pin TO-263
SPX1582T5-1-5	2.0%	2.5V	5-Pin TO-263
SPX1582T5-1-5/TR	2.0%	2.5V	5-Pin TO-263
SPX1582T5-1-8	2.0%	2.5V	5-Pin TO-263
SPX1582T5-1-8/TR	2.0%	2.5V	5-Pin TO-263
SPX1582T5-2-5	2.0%	2.5V	5-Pin TO-263
SPX1582T5-2-5/TR	2.0%	2.5V	5-Pin TO-263
SPX1582T5-2-8	2.0%	2.8V	5-Pin TO-263
SPX1582T5-2-8/TR	2.0%	2.8V	5-Pin TO-263
SPX1582T5-3-3	2.0%	3.3V	5-Pin TO-263
SPX1582T5-3-3/TR	2.0%	3.3V	5-Pin TO-263

Available in lead free packaging. To order add "-L" suffix to part number.

Example: SPX1582T5-3-3/TR = standard; SPX1582T5-L-3-3/TR = lead free.

/TR = Tape and Reel

Pack quantity is 500 for TO-263.

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